

# Coherent Hole Transport in Selective Area Grown Ge Nanowire Networks

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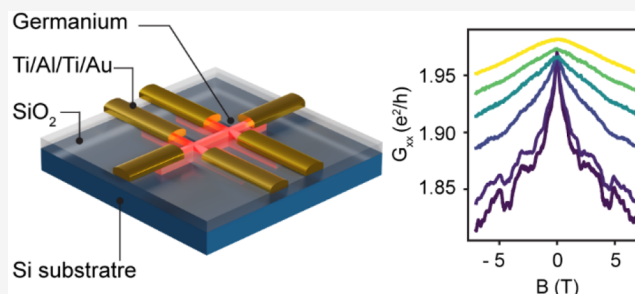
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Supporting Information

**ABSTRACT:** Holes in germanium nanowires have emerged as a realistic platform for quantum computing based on spin qubit logic. On top of the large spin–orbit coupling that allows fast qubit operation, nanowire geometry and orientation can be tuned to cancel out charge noise and hyperfine interaction. Here, we demonstrate a scalable approach to synthesize and organize Ge nanowires on silicon (100)-oriented substrates. Germanium nanowire networks are obtained by selectively growing on nanopatterned slits in a metalorganic vapor phase epitaxy system. Low-temperature electronic transport measurements are performed on nanowire Hall bar devices revealing high hole doping of  $\sim 10^{18} \text{ cm}^{-3}$  and mean free path of  $\sim 10 \text{ nm}$ . Quantum diffusive transport phenomena, universal conductance fluctuations, and weak antilocalization are revealed through magneto transport measurements yielding a coherence and a spin–orbit length of the order of 100 and 10 nm, respectively.

**KEYWORDS:** Germanium, nanowire networks, selective area epitaxy, weak antilocalization, spin–orbit interaction



Holes in germanium nanowires (NWs) are predicted to be a promising platform for spin-qubit-based quantum computing due to their strong spin–orbit interaction (SOI) and low susceptibility to hyperfine interaction.<sup>1–5</sup> The strong SOI of holes enables fast and electrical manipulation of qubits, while the low susceptibility to hyperfine interactions ensures long coherence lifetimes. Of particular interest for nanowires is the possibility of tuning the shape and crystal orientation, which can result in the cancellation of the influence of charge noise and hyperfine interaction on coherence.<sup>6</sup> Thanks to these exhaustive and convenient properties, ultrafast and electrically controlled hole-spin qubits in VLS-grown Ge/Si core/shell nanowires and in self-assembled Ge-hut wires have been demonstrated.<sup>7,8</sup>

Despite the advances in hole-spin qubits in Ge nanowires, the scalability of nanowire-based devices is a major challenge. The ability to grow isolated and connected nanowires with a high degree of controllability in geometry and crystal phase is a prerequisite for NW-based device applications. Although free-standing nanowires grown by VLS methods can form networks,<sup>9–11</sup> the process is inherently not scalable because the grown semiconductor nanowires must be transferred from the growth substrate to an alternate substrate to fabricate the devices. In addition, VLS growth of nanowires is not compatible with existing CMOS technology due to the use of Au catalysts.<sup>12</sup> A more scalable approach is to directly integrate semiconductor nanowires and their networks on a Si

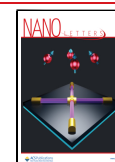
substrate in a planar configuration as has been demonstrated with III–Vs.<sup>13–16</sup> To the best of our knowledge, in-plane growth of germanium nanowires on a Si substrate has been limited to the site-controlled growth of so-called “Ge-hut wires” (HWs) using molecular beam epitaxy.<sup>17</sup> Katsaros and colleagues have demonstrated germanium HWs with a strong and tunable SOI obtained along the edges of trenches defined by electron beam lithography and reactive ion etching (RIE). A drawback of this approach is that Ge-HWs are limited to  $\langle 100 \rangle$  crystallographic orientations and to Si (100) substrates. Since the SOI in Ge NWs is predicted to be strongly dependent on the relative orientation of the NWs with respect to the substrate,<sup>6,18</sup> flexibility in the selection of growth substrate and NW orientation is beneficial to investigate the properties of holes in these structures.

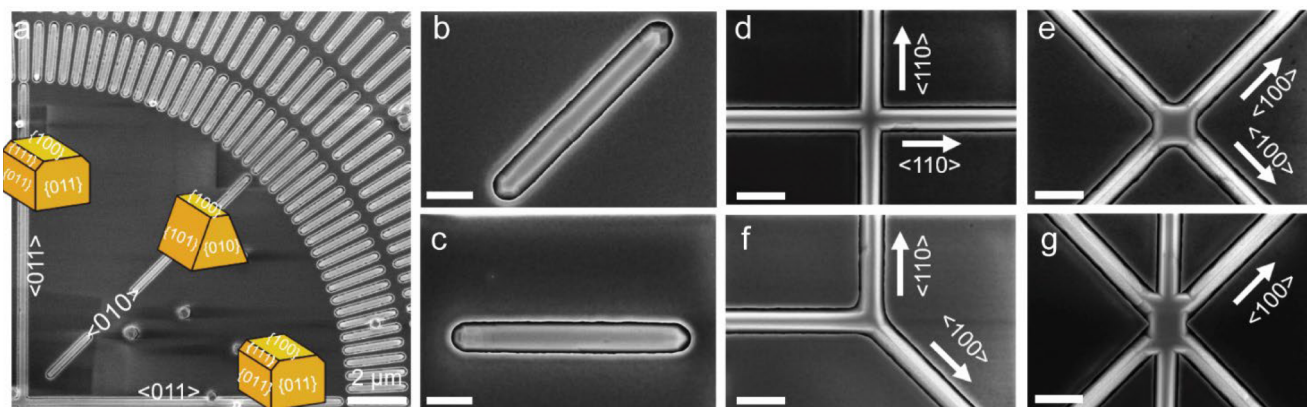
A reliable and scalable strategy to obtain in-plane nanowires is the selective area growth approach (SAG).<sup>13,19</sup> Here, the growth of the semiconductor material is limited to openings in a selective mask, typically  $\text{SiO}_2$ . The openings are obtained by patterning the mask and uncovering (nanoscale) regions of the

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**Figure 1.** In-plane Ge nanowires grown on Si (100) substrate. (a) Top view SEM image of Ge NWs grown along different crystallographic direction. SEM micrographs of NWs grown along highly symmetric crystallographic orientations, that is, (b)  $\langle 100 \rangle$  and (c)  $\langle 110 \rangle$  directions. SEM micrographs of in-plan NW junctions (d)  $\langle 110 \rangle / \langle 110 \rangle$  type, (e)  $\langle 100 \rangle / \langle 100 \rangle$  type, (f)  $\langle 100 \rangle / \langle 110 \rangle$  type and (g)  $\langle 100 \rangle / \langle 110 \rangle / \langle 100 \rangle$  type. Scale bar shown in panels b–g indicate 200 nm.

crystalline substrate. Under optimized growth conditions, such as the precursor flow rate and growth temperature, the semiconductor material deposits only in the nonmasked regions. The low sticking coefficient of adatoms on the dielectric mask compared to the substrate ensures the selectivity of the growth.<sup>20</sup> Thus, the position and in-plane disposition of the nanowires can be lithographically defined prior to growth. This allows reducing the defects arising due to the faceting of the NWs by orienting them along highly symmetric crystallographic directions.<sup>21</sup> Furthermore, the nanoscale nature of the SAG also helps in lowering the density of the defects formed during the integration of lattice mismatched materials.<sup>22</sup> Threading dislocations formed at the interface of the heterostructure are blocked by the walls of the mask defining the nanoscale openings, leaving the upper part of the NW free of defects (aspect ratio trapping).<sup>23–25</sup> Most previous SAG studies have focused on the growth of high-quality III–V nanowires and their networks.<sup>13,19,25–27</sup> While there is some work on epitaxial lateral overgrowth of Ge thin films,<sup>28–30</sup> to the best of our knowledge there is no report on the SAG of in-plane germanium nanowires and networks on Si.

In this Letter, as a first step toward realizing scalable spin qubits, we demonstrate the SAG of in-plane germanium nanowires and their networks on Si (100) substrates. Magnetotransport measurements through the nanowire at low temperature indicated phase coherent transport and weak antilocalization. The observed properties make such NWs promising candidates for future quantum information processing schemes.

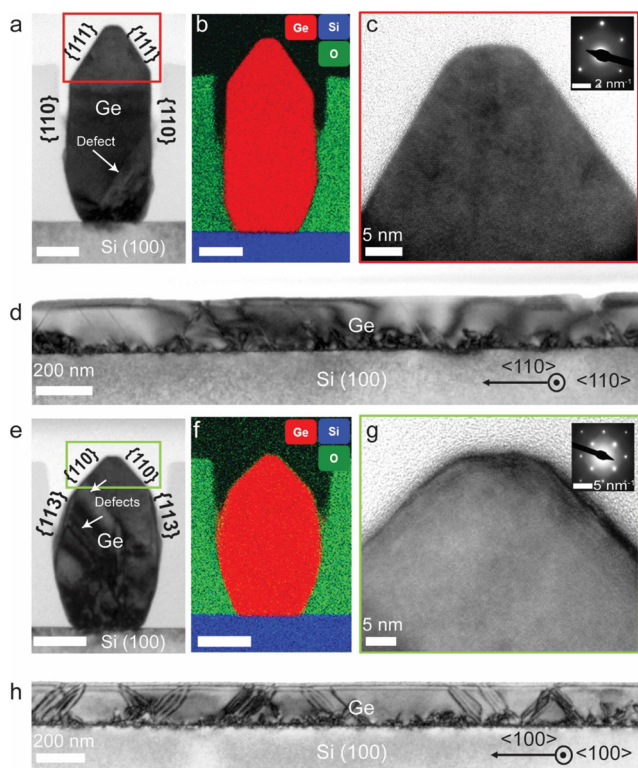
It is well-known that in SAG, the shape and crystalline orientation of the unmasked regions (from now on referred to as slits) affect the geometry, microstructure, and size of the final nanostructure.<sup>21</sup> This is the result of energy minimization for the stable facet formation as well as kinetics that define preferential growth directions. Our study starts by providing an overview of the effect of slit orientation on the shape of the individual Ge NWs as well as their junctions. To do so, we defined rectangular slits of different sizes, and orientations on an intrinsically doped Si (100) substrate. The mask consists of 175 nm thick thermally grown SiO<sub>2</sub>. Ge nanowires were grown inside these nanoscale slits by metal–organic vapor phase epitaxy (MOVPE). We used isobutyl germane as a precursor species and N<sub>2</sub> as the carrier gas. A detailed description of the

substrate patterning and growth procedure can be found in the Supporting Information (SI).

The design flexibility of the SAG process for in-plane Ge NWs is illustrated in Figure 1. Figure 1a–g provide top-view SEM images of the individual Ge nanowires and junctions grown on a Si (100) substrate patterned with 150 nm wide slits. The NW growth is guided by the in-plane geometry, with dimensions defined by the substrate patterning. Figure 1a shows the azimuthal dependence of the in-plane NW growth. The inset illustrates the shape of the nanowires in selected directions, for which the faceting will be justified further below. Two preferred orientations lead to smooth faceting along the slits, while the orientations in between result in surface roughening by nanofaceting, as described by Albani et al.<sup>21</sup> Figure 1b and c zoom in to show the structure of growths inside slits oriented along the  $\langle 100 \rangle$  and  $\langle 110 \rangle$  directions (see detailed SEM images in the SI). The orientation, size, and length of the slits define the design of the Ge NWs and associated geometries. In principle, there is no limitation in the NW design from the macroscopic point of view. However, defects arising from the lattice mismatch with the substrate as well as from the nanofaceting may be orientation-dependent.

We thus turn to the assessment of the microstructure of the NWs. We performed high-resolution transmission electron microscopy (HRTEM) of the NW cross-sections as a function of their orientation. Figure 2 includes the main characterizations of 80 nm wide NWs grown along the two highly symmetric crystallographic orientations. The 100 nm thick TEM lamellas were prepared by focused ion beam (FIB). Figure 2a and e show the bright field (BF) TEM images of the NW cross-sections acquired perpendicular to the two symmetric growth directions, that is,  $\langle 110 \rangle$  and  $\langle 100 \rangle$  orientations, respectively.

Both NW cross-sections exhibit a noninversion symmetric triangular facet morphology, as desired for spin qubit applications.<sup>31</sup> The chemical analysis by energy-dispersive X-ray spectroscopy mapping (EDX) of the cross-sections is shown in Figure 2b and f, confirming the Ge composition and a sharp interface with the Si substrate and SiO<sub>2</sub> mask. We indexed the facets by capturing their relative orientation with the Si (100) substrate. NWs oriented along the  $\langle 110 \rangle$  direction exhibited well-defined vertically oriented  $\{110\}$  sidewalls, inclined  $\{111\}$  facets at the top corners, and a flat  $\{100\}$

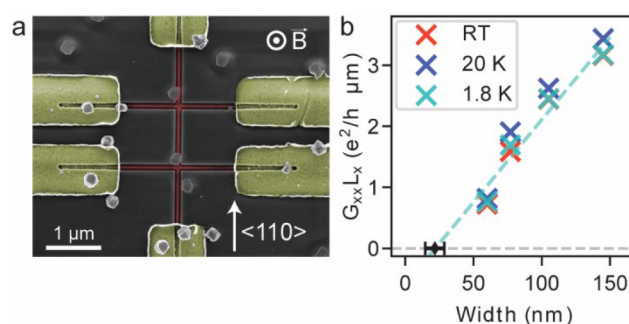


**Figure 2.** TEM/STEM-EDX images of the in-plane Ge NWs grown along different crystallographic orientations. (a–d) TEM image/STEM-EDX elemental mappings of the NW grown along  $\langle 110 \rangle$  orientation. (a) BF TEM image of the NW cross-section exhibiting inclined  $\{111\}$  top facets and  $\{110\}$  side facets. (b) EDX elemental mapping showing the elemental distribution. (c) HR-TEM image acquired from the tip of the NW shown in panel a. (d) BF TEM image of the NW cross-section acquired parallel to the NW orientation. (e–h) TEM image/STEM-EDX elemental mappings of the NW grown along  $\langle 100 \rangle$  orientation. (e) Conventional TEM image of the NW cross-section exhibiting slanted  $\{110\}$  top facets. (f) EDX elemental mapping showing the elemental distribution. (g) HR-TEM image acquired from the tip of the NW shown in panel e. (h) BF TEM image of the NW cross-section acquired parallel to the ridge orientation. The indicated scale bar represents 50 nm.

top facet (Figure 2a). In the case of NWs grown along the  $\langle 100 \rangle$  directions, the sidewalls belong to the high index  $\{113\}$  family and the inclined top facets to the  $\{110\}$  family (Figure 2e). From a thermodynamic point of view, the facets in the crystal are formed to minimize the total surface energy.<sup>32</sup> Well-defined facets of the  $\{110\}$  family develop along the direction corresponding to the local minimum of the surface energy in the Winterbottom construction.<sup>21,33</sup> Facets of the  $\{113\}$  and  $\{111\}$  families are often observed in regimes highly affected by the kinetics of the system.<sup>13,21</sup> In summary, restricting the in-plane growth of nanowires to different orientations produces nanowires with different cross-sectional morphologies that are determined by the surface energy minimization criterion, interfacial energy, and growth kinetics.

We now proceed with the discussion of the crystal quality by analysis of the BF-TEM micrographs of the NW cross-sections shown in Figure 2a and e. We observe that the NW orientation is directly linked to the crystal quality. For the Ge NWs grown along the  $\langle 110 \rangle$  direction (Figure 2a), defects generated at the heterostructure interface (indicated by the white arrow) are efficiently trapped by the  $\text{SiO}_2$  wall, leaving the upper part of

the NW free of defects. Figure 2c presents the HRTEM micrograph obtained from the defect-free region close to the tip of the  $\langle 110 \rangle$  oriented NW. The selected area electron diffraction (SAED) pattern acquired from the NW tip (Figure 2c, inset) reveals the expected zinc-blende crystal structure of the  $\langle 110 \rangle$  oriented Ge NWs. In contrast to the NWs grown along the  $\langle 110 \rangle$  orientation, the NWs grown along the  $\langle 100 \rangle$  orientation (Figure 2e) exhibited a relatively higher defect density. The majority of the defects formed at the interfaces (indicated by the white arrow) extended into the upper part of the NW. However, we still observe a region free of defects close to the NW tip as shown in the HRTEM image presented in Figure 2g. Similar to  $\langle 110 \rangle$  oriented NWs,  $\langle 100 \rangle$  oriented NWs also exhibited a zinc-blende crystal structure as shown in the Figure 3 inset. A comparison of six perpendicular sections of  $\langle 110 \rangle$  and  $\langle 100 \rangle$  oriented NWs showed similar behavior (see SI).

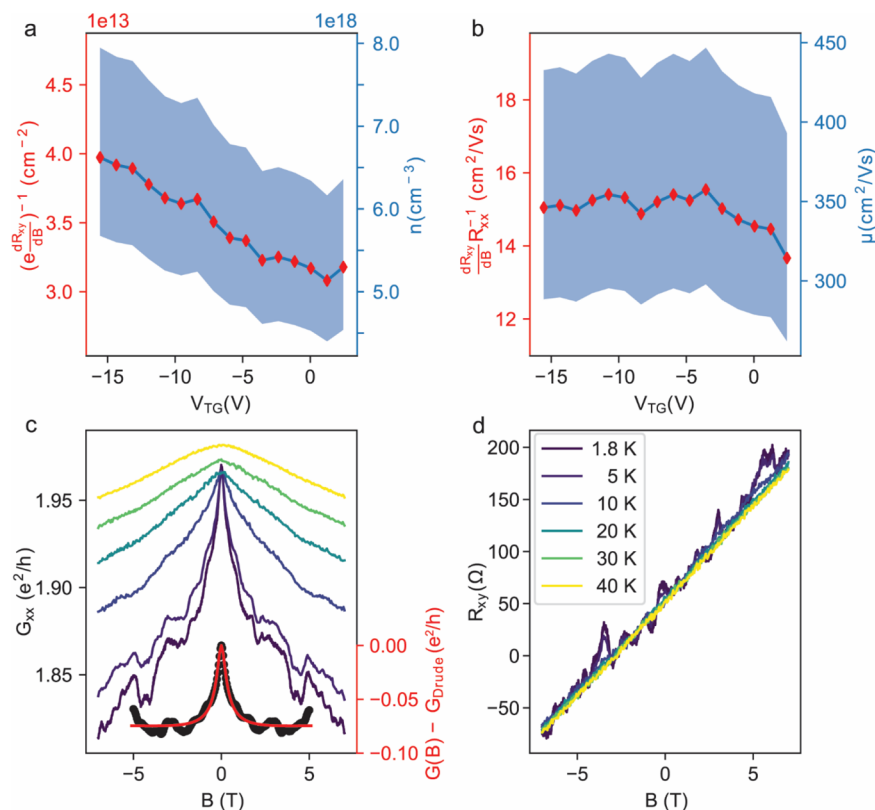


**Figure 3.** (a) False color SEM image of the nanowire Hall bar device where contacts and nanowire are indicated by gold and red, respectively. Orientation of the main NW axis is indicated by the white arrow. Small bits scattered over the device are Ge parasitic growth on the mask. (b) Normalized conductance  $G_{xx}L_x$  as a function of the average NW width measured at  $T = 1.8$  K, 20 K, and room temperature. For the  $T = 1.8$  K data set, a linear fit to the data is shown with a dashed cyan line and the result from the transport cross-section model, twice the depletion width, described in the main text is indicated with a black error bar.

To investigate the presence of defects along the NW length, we made FIB cross-sections parallel to the NW orientation. Figure 2d and h show the BF-TEM image of such parallel cross-sections obtained from  $\langle 110 \rangle$  and  $\langle 100 \rangle$  oriented NWs, respectively. Several inclined defects with a spacing ranging between 100 and 500 nm are observed. We suspect these defects to be  $\{111\}$  oriented and their nucleation can be caused due to inhomogeneities on the substrate surface.<sup>30,34–36</sup>

**Electrical Transport Measurements.** We fabricated four Hall bar devices with  $\langle 110 \rangle$  and  $\langle 100 \rangle$  orientation of the main nanowire axis with average widths ranging from  $\sim 60$  nm to  $\sim 150$  nm. The average width is introduced since NWs do not have a rectangular cross-section (see SI) and we define it as  $W_{\text{avg}} = \int_0^H w(h) dh / \int_0^H dh = A_{\text{c.s.}}/H$  where  $A_{\text{c.s.}}$  and  $H$  are the NW cross-section area and height, respectively, and  $w(h)$  is the height-dependent nanowire width. In the remaining manuscript, we focus on the transport experiments on  $\langle 110 \rangle$  NWs (for  $\langle 100 \rangle$ , see SI).

In Figure 3a, we show an SEM image of the nanowire Hall bar device with Ti (10 nm)/Al (30 nm)/Ti (20 nm)/Au (100 nm) stacks serving as ohmic contacts. A top gate is fabricated on the  $\sim 75$  nm wide  $\langle 110 \rangle$  device by depositing a 35 nm  $\text{AlO}_x$  ALD layer followed by metal evaporation of 10 nm Ti and 90



**Figure 4.** First factor of the (a) density,  $(e dR_{xy}/dB)^{-1}$ , and (b) mobility equation,  $(R_{xx}^{-1}dR_{xy}/dB)$ , that contains no geometrical assumptions of the NW geometry is plotted in red as a function of the gate voltage with  $y$ -axis on the left side of the respective figures. Blue shaded regions indicate the corresponding (a) density and (b) mobility range, with  $y$ -axis on the right side, assuming  $H_{\text{eff}} = 60 \pm 10$  nm and  $W_{\text{eff}} = 50 \pm 10$  nm, respectively. (c)  $G_{xx}$  as a function of perpendicular magnetic field measured at  $T = 1.8, 5, 10, 20, 30,$  and  $40$  K with a waterfall color code scheme. At the bottom of the figure,  $G_{xx}$  averaged over the top-gate voltage interval  $[-1.1$  V,  $0.4$  V] with subtracted background is plotted (black dots) and the corresponding fit (red line) with the corresponding  $y$ -axis on the right side of the figure. (d)  $R_{xy}$  corresponding to the same measurements in panel c.

nm Au. The devices are then measured in a 4-terminal voltage-bias setup using standard low-frequency lock-in techniques with  $V_{\text{sd}} = 400$   $\mu\text{V}$  in a  $^4\text{He}$  cryostat with a base temperature of 1.8 K, equipped with temperature control enabling temperatures up to 40 K. Additionally, we investigate the devices using DC measurements in a cryogenic needle probe at room temperature and 20 K.

In Figure 3b, we plot the product  $G_{xx}L_x$  of the longitudinal conductance measured on  $\langle 110 \rangle$  oriented nanowires at 1.8 K, 20 K, and room temperature, and the distance  $L_x$  between voltage contacts as a function of the average nanowire width. We observe that temperature has only a small influence on the nanowire conductance. Linearly fitting the data in Figure 3b, we extrapolate to the average width of the NW corresponding to zero conductance and find that nanowires below a width of about 30 nm should not conduct. If we take this value as an estimate of twice the width of a depletion region at the nanowire surface, we get a depletion width of 10–15 nm (see SI).

In addition to this empirical model, we estimated the depletion width by modeling the transport cross-section area as  $A_{\text{trans}} = A_{\text{c.s.}} - C_{\text{c.s.}}d$  where  $C_{\text{c.s.}}$  is the circumference of the wire cross-section and  $d$  is the depletion width. Again, similar values are obtained for the depletion width for all temperatures (see SI). In Figure 3b, we indicate both the empirical linear fit (dashed line) and the result of the extended model (black error bar), as an example for the 1.8 K data.

Interestingly, the conductance in all NWs exhibits a small, nonmonotonic temperature dependence. High defect density causes the defect scattering rate  $1/\tau_D$  to dominate over the phonon scattering rate  $1/\tau_{\text{ph}}$  in a large temperature range, causing a small dependence on temperature. This observation indicates that transport may also occur in the part of the NW, which shows many defects in the TEM images (see Figure 2a,e).

From measurements of the Hall ( $R_{xy}$ ) and longitudinal ( $R_{xx}$ ) resistance as a function of perpendicular magnetic field  $B$  and gate voltage  $V_G$ , we can extract density and mobility as a function of  $V_G$  (see Figure 4a,b). The data were measured at 1.8 K on a 75 nm wide  $\langle 110 \rangle$  NW Hall bar and averaged over the gate-voltage interval  $\Delta V_G = 1.2$  V to suppress the contribution of universal conductance fluctuations (UCF). To extract the density and mobility, an assumption about the effective transport height  $H_{\text{eff}}$  and width  $W_{\text{eff}}$  of the nanowire must be made due to the unknown transport cross-section.

Then the Hall-density can be written as  $n = (e dR_{xy}/dB)^{-1}H_{\text{eff}}^{-1}$  where  $e$  is the elementary charge. In Figure 4a, we plot on the left axis the first factor of this equation, which contains no geometrical assumptions about  $H_{\text{eff}}$  and has units of a 2D density, and on the right axis the result of the hole density equation as a density range is indicated with shaded blue region assuming  $H_{\text{eff}} = 60 \pm 10$  nm (see NW cross-section in SI). The plot indicates a large hole density of  $\sim 5 \times 10^{18}$   $\text{cm}^{-3}$ , which might be the result of unintentional doping via

vacancy defects<sup>37</sup> or surface states.<sup>38</sup> Furthermore, the density shows a linear trend in gate voltage from which a 2D capacitance of  $\sim 8 \times 10^{-4}$  F/m<sup>2</sup> can be extracted. The extracted capacitance is 50% smaller than a parallel plate capacitance model  $\epsilon\epsilon_0/d_{\text{ALD}} \approx 1.5 \times 10^{-3}$  F/m<sup>2</sup> would indicate. This is consistent with the notion that transport occurs through the whole cross-sectional area of the nanowire.

In analogy to the extraction of the density, the Drude mobility can be written as a product of an assumption-free factor and a geometry factor, that is,  $\mu = (R_{xx}^{-1} dR_{xy}/dB) L_x/W_{\text{eff}}$  where the first factor is plotted on the left axis of Figure 4b and has units of mobility. Assuming  $W_{\text{eff}} = 50 \pm 10$  nm, and correcting for WAL peak in  $R_{xx}$  (see SI), we plot the result of the mobility equation on the right axis of Figure 4b. The range of  $W_{\text{eff}}$  was chosen such that the extracted mobility fits well with the background of the magnetoconductance (see SI). The extracted mobility is in the range of 300–400 cm<sup>2</sup>/(V s) and shows a weak gate-voltage dependence with saturation at  $V_G < -5$  V. From  $n$  and  $\mu$ , we extract the Fermi wavelength  $\lambda_F \approx 15$  nm and the elastic mean free path  $l_e \approx 10$  nm. Such a small  $l_e$  is expected due to the high defect density.

Coherent transport phenomena in our devices are probed by magneto-transport measurements (see Figure 4c,d). Universal conductance fluctuations can be seen both in  $G_{xx}$  and  $R_{xy}$ , indicating coherent transport over a length scale of the order of

$$\Delta G = \frac{e^2}{h} \frac{1}{L_x} \left[ 3 \left( \frac{1}{l_\phi^2} + \frac{4}{3l_{\text{SO}}^2} + \frac{1}{l_B^2} \right)^{-1/2} - \left( \frac{1}{l_\phi^2} + \frac{1}{l_B^2} \right)^{-1/2} - 3 \left( \frac{1}{l_\phi^2} + \frac{3}{l_e^2} + \frac{4}{3l_{\text{SO}}^2} + \frac{1}{l_B^2} \right)^{-1/2} + \left( \frac{1}{l_\phi^2} + \frac{3}{l_e^2} + \frac{1}{l_B^2} \right)^{-1/2} \right] \quad (2)$$

where  $l_{\text{SO}}$  is the spin–orbit length,  $l_B$  is the magnetic dephasing length, and  $h$  is Planck's constant. The model is valid for a multimode,  $\lambda_F < W$ , quasi-1D NW,  $l_\phi > W$ . In the dirty metal regime where  $l_e < W$ , the magnetic dephasing length is given by  $l_B^2 = 3l_m^4/W^2$  where  $l_m = \sqrt{\hbar/eB}$  is the magnetic length.

When fitting  $\Delta G$ ,  $l_\phi$  and  $l_{\text{SO}}$  are the free fitting parameters, and  $l_e$  is fixed to 10 nm, while  $W_{\text{eff}}$  is fixed within the assumed interval. In the inset of Figure 4c, we show the fit to the conductance  $G(B)$  trace averaged over the  $V_G$  interval [−1.1 V, 0.4 V] for  $W_{\text{eff}} = 50$  nm. After repeating the fitting procedure for the range of  $W_{\text{eff}}$ , we obtain  $l_\phi \sim 100$  nm, validating the use of the expression for the quasi-1D regime, and  $l_{\text{SO}} \sim 10$  nm. The extracted  $l_{\text{SO}}$  is similar to values extracted for holes in Ge/Si core–shell nanowire<sup>41</sup> and smaller than  $l_{\text{SO}}$  for electrons in InAs nanowires.<sup>42</sup> The similarity between the extracted spin–orbit length and the mean free path,  $l_{\text{SO}} \approx l_e$ , makes it likely that the Elliott–Yafet mechanism is the dominant spin relaxation mechanism.<sup>43,44</sup>

In summary, we have demonstrated a scalable way to obtain in-plane Ge NWs and their networks on Si substrates with a great degree of controllability over the size, shape, and orientation of the NWs. By employing the selective area growth approach, in plane Ge NWs with well-defined facet morphology were obtained along the highly symmetric crystallographic orientations, that is,  $\langle 110 \rangle$  and  $\langle 100 \rangle$ . We investigated transport in NW Hall bar devices and found a high hole density  $\sim 5 \times 10^{18}$  cm<sup>−3</sup>, possibly caused by a large density of vacancy defects or surface states, a weakly top-gate voltage dependent mobility  $\sim 400$  cm<sup>2</sup>/(V s), and the corresponding mean free path  $l_e \approx 10$  nm. Together with weak temperature

$L_x$ . From the autocorrelation field of the UCF amplitude  $\Delta G_{xx}$  at  $T = 1.8$  K, we estimate the order of magnitude of the coherence length  $l_\phi \approx 100$  nm (see SI). Furthermore, a weak antilocalization (WAL) peak around  $B = 0$  T is observed in  $G_{xx}$ , suggesting strong spin–orbit interaction. Increasing the temperature leads to a decrease in  $l_\phi$ , most likely caused by an increase in electron–electron scattering. As seen in Figure 4c and d, raising the temperature above 20 K suppresses all interference phenomena and reveals the classical magnetoconductance background. The overall conductance shows a weak increase with temperature which is in accordance with the low extracted  $k_F l_e$  and the data in Figure 3b. Similar coherent transport behavior was observed in Hall bar devices with  $\langle 100 \rangle$  direction as the main growth axis (see SI).

We fit the WAL peak in magneto-conductance  $G_{xx}(B)$  in two steps. First, we remove the background by fitting the Drude model expression:

$$G_{\text{Drude}} = \frac{G_0}{1 + (\mu B)^2} \quad (1)$$

where  $G_0$ , the conductivity at  $B = 0$ , and  $\mu$  are the two fitting parameters. The fit is performed at  $|B| > 1$  T to exclude the WAL peak from the Drude fit (see SI). Second, we fit the amplitude  $\Delta G = G_{xx}(B) - G_{\text{Drude}}$  with WAL correction:<sup>39,40</sup>

dependence of conductivity, these results indicate that transport may occur both in the defective and defect free part of the nanowire. Magneto-transport measurements showed coherent transport phenomena, UCF, and WAL indicating strong spin–orbit interaction. By fitting WAL, we obtained  $l_\phi \approx 100$  nm and  $l_{\text{SO}} \approx 10$  nm, which point to the Elliott–Yafet mechanism as the dominant spin-relaxation process due to its similarity with the mean free path.

## ■ ASSOCIATED CONTENT

### Supporting Information

The Supporting Information is available free of charge at <https://pubs.acs.org/doi/10.1021/acs.nanolett.2c00358>.

Substrate preparation methodology, selective area growth parameters, orientation dependency of Ge NW morphology, comments on growth homogeneity, structural characterization on in-plane Ge NWs, details of device fabrication, TEM cross-section of NWs, estimation of depletion width, mobility estimation, coherence length from autocorrelation field, weak antilocalization fitting, transport measurements along  $\langle 110 \rangle$  and  $\langle 100 \rangle$  oriented nanowires (PDF)

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## Author Contributions

<sup>#</sup>S.P.R. and P.T. contributed equally to this work. S.P.R. performed the substrate fabrication, growth of NWs, structural and morphological characterization, and wrote the corresponding parts of the manuscript. P.T. fabricated the top gated devices and performed the electrical characterization, magneto transport measurements, and wrote the corresponding section of the manuscript. N.P.M. participated with the substrate fabrication and assisted with growth experiments. A.R. significantly participated in optimizing the growth of Ge NWs. A.G. participated in growth discussions and assisted with the growth. A.F.i.M., T.I., and K.E. supervised the work and provided valuable input throughout the project and in manuscript writing. All authors commented on the work and approved the final version of the manuscript.

## Notes

The authors declare no competing financial interest.

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