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Modeling Frequency Dependent Core Loss of Ferrite Materials using Permeance-Capacitance Analogy for System-Level Circuit Simulations

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Modeling Frequency Dependent Core Loss of Ferrite Materials using Permeance-Capacitance Analogy for System-Level Circuit Simulations

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Abstract—Ferrite core materials are preferred for magnetic components in power electronic converters considering their low cost and high permeability. Thanks to the negligible electrical conductivity, core loss is dominated by the frequency independent hysteresis effect in most operating conditions. In the applications of high switching frequency under certain modulation scheme, however, the core loss of ferrite materials do exhibit obvious frequency dependency, known as relaxation effect, which is strongly influenced by the magnetic flux's rate of change and can not be ascribed to material conductivity. In order to incorporate the core loss including both frequency dependent- and independent effect into system level simulation environment of power converters, so that the system efficiency can be pre-evaluated during the design-phase as reference for optimization, dynamic model which is physically intuitive and easy to parametrise is desired. This work proposes an approach to model the core loss of ferrite materials combining both frequency dependent relaxation effect and frequency independent hysteresis, which can be flexibly applied for various operation conditions with good accuracy.

Index Terms—core loss, frequency dependent, ferrite material, magnetic circuit, permeance-capacitance

NOMENCLATURE

B	Flux density.
H	Field strength.
F	Magnetomotive force.
Φ	Magnetic flux.
\mathcal{P}	Magnetic permeance.
μ	Magnetic permeability.

I. INTRODUCTION

Ferrite is a popular type of core material to produce magnetic components in power electronic applications, taking advantage of its low cost and relatively high permeability. In order to predict the overall efficiency and determine the cooling condition, core loss of the ferrite based magnetic components need to be estimated during the design phase of the power converters. In general, the core loss of magnetic materials can be separated into three parts, according to [1]:

- Hysteresis loss: Frequency independent effect related to the material nonlinearity.

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- Eddy current loss: Frequency dependent effect related to the material conductivity.
- Relaxation loss: Frequency dependent effect related to other physical mechanisms.

Due to the low electrical conductivity, eddy current which is obviously taking place in metal-based core materials (e.g. silicon steel) is negligible in ferrites, so that the core loss is dominated by frequency independent hysteresis effect in most operating conditions of power converters. However in high frequency (e.g. $> 20kHz$) applications under certain modulation schemes, the core loss of ferrite materials do exhibits strong frequency- and rate dependency, which can not be ascribed to eddy current but the relaxation effect.

In practice, the core loss is usually estimated using empirical formulas based on Steinmetz equation (SE), whose parameters are extracted from experimental measurements under sinusoidal excitation. Authors of [2] proposed an improved form of Steinmetz equation (iGSE) to generalize it to non-sinusoidal excitations, as is the case in power electronic applications. In the work of [3], the authors have observed that under phase-shift modulation (e.g. in the case of galvanically isolated type of DC-DC converters shown in Fig. 1) where a zero-voltage period is present, and strongly asymmetrical PWM with duty-cycle far away from 50%, the core loss increases significantly in comparison to the case of 50% symmetrical PWM. In the aforementioned modulation schemes, authors of [4] have demonstrated that the improved form proposed by [2] still significantly underestimated the core loss. Aiming to reduce the error, an additional term has been added to the iGSE, resulting in improved-improved generalized Steinmetz equation (iiGSE). Alternatively, the approach based on the composite waveform hypothesis (CWH) proposed by [3] was further developed by [5]. Physically, this considerable relaxation loss under high switching frequency PWM excitation with zero-voltage period or strongly asymmetrical duty cycle, takes place if the thermal equilibrium of a magnetic system is suddenly altered by some external force and the reestablishment of a new thermal equilibrium happens progressively, as introduced in the work of [1]. More detailed analysis of relaxation effect can be found in [6].

Although the continuously improved empirical equations proposed by the existing publications provide even better accuracy in core loss estimation, the physics in behind are still obscure and unintuitive for power electronic engineers whose knowledge, unfortunately, is often not very deep in the area of magnetics. In the design phase of power converters, to help

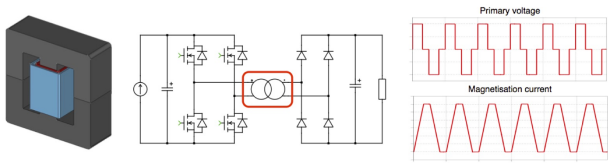


Fig. 1: Isolation transformer in DCDC converter with phase-shift modulation and the typical operational waveforms of primary voltage and magnetisation current.

engineers with understanding the physical interaction between the components and evaluating the dynamic behavior of the whole system, time-domain circuit simulation is commonly adopted. The empirical equations, however, are difficult to be incorporated into the circuit simulation environment. In order to reflect the core loss's impact on the dynamic behavior of the converter system, authors of [7] managed to interpret iGSE directly into SPICE-type circuit simulator via a variable resistor, which achieved good accuracy in various operating conditions. However the loss mechanism still remains as a black box having the same transparency level as the empirical equation, where all the three loss components (hysteresis, eddy current, relaxation) are mixed together.

To provide a clear insight of the loss mechanism, it's desired to present the loss components separately in the simulation environment. Frequency independent hysteresis loss can be exclusively represented in time domain by classical Preisach model in a flexible way [8]. In the work of [9] and [10], the classical Preisach model is extended in circuit simulation for static hysteresis effect of ferrite materials, where good accuracy was achieved in various excitation amplitudes. Beside using Preisach model for frequency independent hysteresis, the other two loss components - eddy current and residual effect are incorporated by the authors of [11] via introducing two resistive components. However the model was only verified under symmetrical PWM excitation without zero-voltage period, where the relaxation loss hardly has visible impact while the eddy current loss is intrinsically negligible in ferrite materials. Authors of [12] modeled the relaxation effect explicitly using a third-order circuit model, the similar model structure was also applied later by [13], while the frequency independent hysteresis effect was not taken into account and the fidelity is verified in frequency response rather than time domain.

Aiming to provide a model which is sufficiently accurate to capture the total core loss of ferrite materials, and is meanwhile clear and intuitive enough in terms of the individual loss mechanism, the modeling approach proposed by this work differs from the existing publications in the following aspects:

- Frequency dependent relaxation effect is specifically modeled. An explicit way of parametrization from measurement result is introduced.
- Frequency independent hysteresis effect is also individually covered using Preisach model, and combined in the same circuit structure.
- The total core loss is physically present in the circuit model, while the mechanism of individual loss compo-

nents (hysteresis, relaxation) can be clearly investigated.

- The permeance-capacitor based magnetic circuit proposed by [14] and further adopted in the work of [15], [10] is chosen as the fundamental platform, which has seamless interface to electrical circuit. In permeance magnetic circuit, the relaxation effect can be explained in a intuitive way, as will be presented in the following sections.

In section II, the experimental result of operating condition, where both frequency independent hysteresis and frequency dependent relaxation effect are obviously visible, is presented. In section III, referring to the experimental result, the fundamental model structure and physic origins of the frequency dependent relaxation effect is interpreted into permeance-magnetic circuit, as starting point of the modeling. The parametrization process of the proposed model for ferrite material and the required measurement data are introduced in section IV. Afterwards in section V, the proposed model is verified by experimental test of different ferrite materials under various operating conditions including switching frequency, duty cycle and zero-voltage period. At the end, summary and outlook are provided in section VI.

II. EXPERIMENTAL MEASUREMENT

A. Test setup

For material characterization and model verification purpose, a multi-functional magnetic characterization test setup has been constructed, which is able to provide both low frequency sinusoidal and high frequency PWM excitations. The hardware of the characterization setup is designed to have 24V rated output voltage and 6A rated output current. The basic idea is derived from the V-I approach proposed by the work of [16]. The circuit schematic and photo of a actual setup are shown in Fig. 2 and Fig. 3b respectively, and include the following elements:

- Controlled voltage source V_1 : Realized using power amplifier of type LM3886 with 2MHz gain-bandwidth-product and is supplied by $\pm 24V$. The main usage is to generate sinusoidal excitation.
- Single phase T-type bridge: Composed of three MOSFET switches (pairs) (Q_{01} & Q_{02} , Q_1 , Q_2) to generate three-level PWM excitation. Both Q_1 and Q_2 are connected to controlled voltage sources (V_3 , V_4) which are realized using the same type of power amplifier as V_1 .
- Core sample: Following the V-I approach for magnetic material characterization, the core sample is equipped with two windings as demonstrated in Fig. 2. The primary winding is excited by the power stage whose current is measured to obtain field strength H . The secondary winding is left open, the voltage of which is measured and integrated in time domain to obtain flux density B .
- Current measurement: Realized using low-inductance shunt resistor. The voltage drop of the shunt resistor is sensed and amplified using differential instrumentation amplifiers. Two sets of shunt resistors (different resistance) circuitry are mounted to achieve different resolutions of measurement.

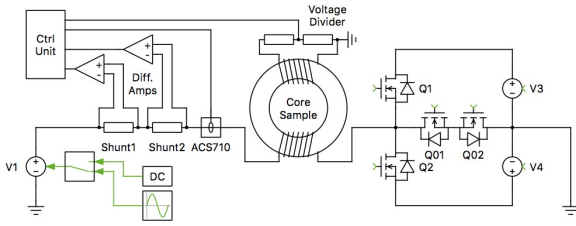


Fig. 2: Circuit schematic of the test setup.

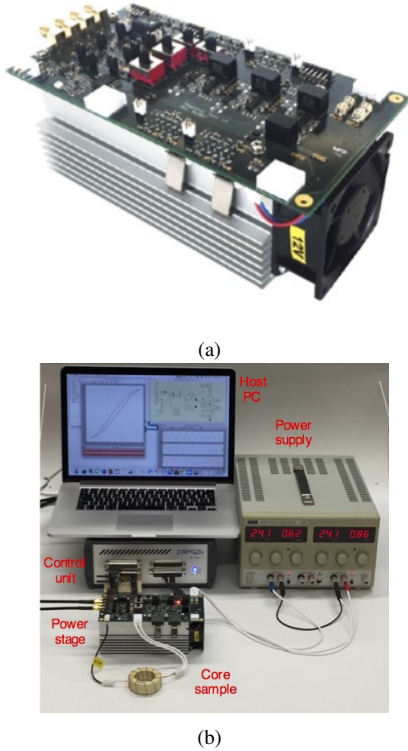


Fig. 3: (a) Power stage hardware of the test setup; (b) Complete configuration of the test setup including the power stage, core sample, power supply, control unit (PLECS RT-Box) and host PC.

- Voltage measurement: Realized using low capacitance resistive voltage divider. The scaled voltage is buffered and amplified by operational amplifier with 180MHz bandwidth.
- Control unit: One PLECS RT-Box is deployed as control unit for the test setup, which is graphically operated by a host PC.

B. Measurement results

To demonstrate the impact of relaxation effect at high switching frequency excitation, comparative measurement has been carried out based on the ferrite material N87 from EPCOS, with the toroidal core of size code "R 20.0 x 10.0 x 7.00" chosen as sample. The turns number of both windings are $N_1 = N_2 = 5$. As the temperature dependency is not in the scope of this work, all tests for parametrization and

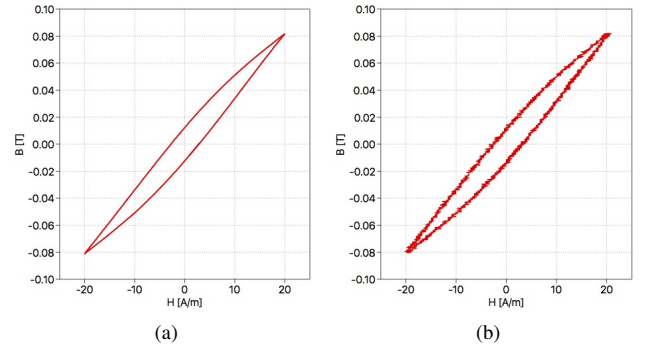


Fig. 4: Measured $B - H$ characteristic of ferrite N87 with peak strength $\hat{H} = 20A/m$ excited by: (a) $500Hz$ sinusoidal excitation; (b) $50kHz$ symmetrical PWM excitation without zero voltage period.

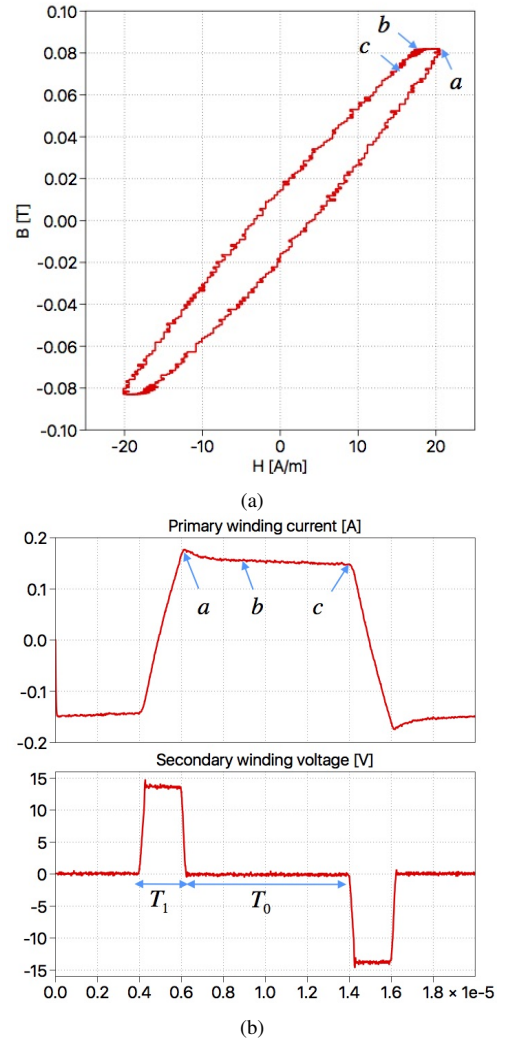


Fig. 5: Measurement results of ferrite N87 (peak strength $\hat{H} = 20A/m$) excited by $50kHz$ symmetrical PWM excitation with zero voltage period of $T_0 = 8\mu s$ (a) $B - H$ characteristic (b) Primary winding current and secondary winding voltage

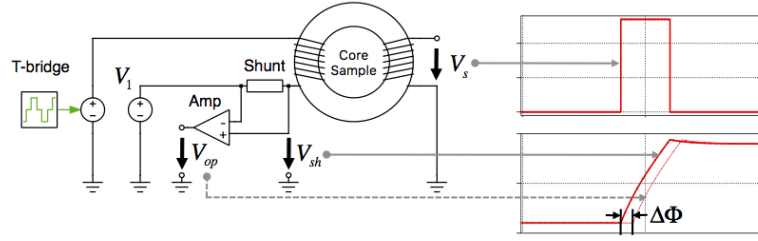


Fig. 6: Phase error compensation of the differential amplifier output in the current measurement.

verification have been conducted under room temperature of 25°C.

At first, the core sample is excited by sinusoidal excitation generated from the controlled voltage source V_1 (with switches Q_{01} & Q_{02} closed and Q_1 & Q_2 open), the frequency is configured to be 500Hz where the frequency dependent effect is negligible. The amplitude of V_1 is adjusted to generate a $B-H$ loop with peak field strength $\hat{H} = 20A/m$, as shown in Fig. 4a. Secondly, the test setup is reconfigured to provide 50kHz symmetrical PWM excitation without zero-voltage period, where the switches Q_{01} & Q_{02} are open, Q_1 & Q_2 actively switched. The output of the voltage sources V_3 and V_4 are provided by the reference signal with the same amplitude but opposite polarity, which is adjusted to generate a $B-H$ loop with peak field strength $\hat{H} = 20A/m$, as shown in Fig. 4b (V_1 is tuned to ensure zero DC bias). Comparing Fig. 4a and Fig. 4b, only slight shape difference can be detected, while the per-cycle energy loss (indicated by the loop area) exhibits less than 10% difference. This indicates that the part of frequency independent core loss still dominates in this operating condition, although the switching frequency 50kHz is significantly higher than 500Hz.

Afterwards, a zero voltage period of duration $t_0 = 8\mu s$ is inserted to the PWM excitation with the mid-point switches Q_{01} & Q_{02} activated, the voltage output of V_3 and V_4 are adjusted to ensure that the peak amplitude is still equal to $\hat{H} = 20A/m$. The measured $B-H$ characteristic and time domain primary current and secondary voltage are shown in Fig. 5a and Fig. 5b, respectively. In comparison to Fig. 4, significant larger $B-H$ loop area can be observed especially in the location approaching the peak. The flat part between the points "a" and "b" arise from the relaxation effect, where the field strength decreases although nearly zero excitation is applied, as has also been observed in the work of [4]. According to the observed phenomenon, the basic model structure of relaxation effect is derived using permeance-capacitor based magnetic circuit in the next section.

C. Discussion of measurement accuracy

During measurement and data post processing, one should pay attention to the influence of the phase error between the current and voltage. Authors of [16] has explored that the percentage error of the core loss power can be expressed as:

$$\Delta\% = \tan(\Phi_{v-i}) \cdot \Delta\Phi \quad (1)$$

where Φ_{v-i} is the absolute phase shift between the measured primary winding current and second winding voltage under sinusoidal excitation, while $\Delta\Phi$ is the error of the phase shift arising from the measurement system. Equation (1) indicates that, the closer to 90° is Φ_{v-i} , the more sensitive is the percentage error to $\Delta\Phi$. Different modifications on the measurement circuit structure has been proposed by the work of [17], [18] and [19], so as to drag Φ_{v-i} away from 90° and thus decrease the sensitivity to $\Delta\Phi$. There high excitation frequency of several or tens of MHz was discussed, the frequency in this work (50 kHz) however, is far below that range. Considering the fact that $\Delta\Phi$ is proportional to the excitation frequency [20], the influence of $\Delta\Phi$ is significantly lower in the frequency range of this work. Moreover, with the modification although the core loss power can be more accurately measured, the exact shape of the $B-H$ is not available anymore, which however, is necessary for the model parametrization and verification in this work. Therefore the original circuit structure from [16] is still adopted.

The influence of phase error in the measurement of low frequency (500 Hz) sinusoidal excitation can be neglected. For the case of 50 kHz PWM excitation, phase error compensation has been applied in post processing. Phase error may exist in both voltage and current measurements:

- Voltage measurement: In the 50 kHz measurement, due to the sufficiently large amplitude of the excitation voltage, the amplification stage is bypassed. The secondary winding is directly connected to the oscilloscope via shielded axial cable (V_s in Fig. 6), which has the same length as that of the current measurement, so that the phase delay of the voltage measurement can be neglected.
- Current measurement: According to the datasheet of the ferrite material N87 [21], the per-volume core loss (25°C) at frequency $f = 50$ kHz and peak-to-peak flux density $\Delta B = 200$ mT (amplitude 100 mT) is 54 kW/m², which corresponds to $P_{loss} = 0.08$ W loss power for the core "R 20.0 x 10.0 x 7.00", if multiplied by the core volume. The equivalent phase shift between voltage and current can be calculated as: [20]

$$\Phi_{v-i} = \arctan\left(\frac{(N_1 \cdot A \cdot 2\pi f \cdot \Delta B)^2}{8 \cdot P_{loss}} \cdot \frac{1}{2\pi f \cdot L}\right) \quad (2)$$

The equation above $\Phi_{v-i} = 84.6^\circ$, where $N_1 = 5$ is the primary winding turns number, A is the cross section area of the core and L the equivalent inductance calcu-

lated from the A_L value provided by the manufacturer's datasheet [21].

If we target on $\epsilon_{ph} = \pm 5\%$ phase error in equation (1), the allowed uncompensated phase error is $\pm 0.272^\circ$ at 50 kHz. The first source of the uncompensated phase error is the sample step size of the oscilloscope, which is in our case equal to 10 ns (0.18° for 50 kHz). The second source of the uncompensated phase error arise from the stray inductance of the shunt resistor. The manufacturer of the 0.1Ω shunt resistor [22] specifies that the stray inductance is typically 0.5 nH, which result in a phase angle of 0.09° at 50 kHz. The sum of the phase error of the oscilloscope and shunt resistor is within the criteria 0.272° .

The differential amplifier aims to filter out EMC noise and eliminate the DC voltage bias of the power amplifier V_1 , the phase delay between the voltage drop on the shunt resistor and the differential amplifier's output can be explicitly compensated: As demonstrated in the simplified circuit schematic shown in Fig. 6, for each measurement, the voltage V_{sh} between the positive terminal of the shunt and ground (with DC bias of V_1) is connected to the oscilloscope using a coaxial cable with the same length as the differential amplifier's output V_{op} (without DC bias of V_1). At the moment when the excitation voltage becomes positive from zero, the time difference between the turning point of V_{sh} and that of V_{op} is considered as the phase delay $\Delta\Phi$ to be compensated (with the uncompensated phase error discussed before). During the data post processing to calculate core loss, the current measurement should be shifted by $-\Delta\Phi$. For the measurement of other materials and cores, compensation is applied following similar consideration.

Beside the phase error, the equipment accuracy also add up to the measurement error, which can be found in the datasheet of the corresponding components and devices. The major contribution comes from:

- The shunt resistor has an error of $\epsilon_{sh} = \pm 0.5\%$.
- The chip resistor to configure the gain of the differential amplifier has $\epsilon_{amp} = 0.1\%$ error.
- The oscilloscope has a measurement error of $\epsilon_{osc} = \pm 2\%$.

Combining these three error sources and the phase error together, the system measurement error is calculated by

$$\epsilon_{sys} = (1 + \epsilon_{sh}) \cdot (1 + \epsilon_{amp}) \cdot (1 + \epsilon_{osc}) \cdot (1 + \epsilon_{ph}) - 1 \quad (3)$$

The equation above yields $\epsilon_{sys} = \pm 7.7\%$.

III. PROPOSED MODEL

A. Model behaviour

The model for frequency independent hysteresis effect introduced in [10] is taken as starting point for the modeling approach in this work. The equivalent electric-magnetic circuit of the test setup is demonstrated in Fig. 7a, which includes the PWM excitation represented by a voltage source, the winding

block as interface to magnetic circuit (Fig. 7b) and the permeance building block modeling the core sample. The internal structure of the permeance block, which is able to represent a complete magnetic core of simple geometry or one part of that with complex geometry, is demonstrated in Fig. 7c. Please note that in the work of [10] two variable permeance blocks accounting for irreversible- and reversible magnetization are connected in parallel for illustration purpose, while in this work they are structurally merged into one and the differential permeability of the irreversible- and reversible components are summed up as $\mathcal{P}_1(H)$, which is physically equivalent to that in [10].

After the parameters of the formula in calculating $\mathcal{P}_1(H)$ are completely identified using the experimental measurement at low frequency sinusoidal excitation, as has been described by [10], the relaxation effect can be incorporated. As illustrated in Fig. 8, at first, the permeance block \mathcal{P}_1 is split into two permeance blocks in parallel, namely \mathcal{P}'_1 and \mathcal{P}_2 . It is firstly assumed that the permeance \mathcal{P}_2 is constant.

$$\mathcal{P}'_1(H) = \mathcal{P}_1(H) - \mathcal{P}_2 \quad (4)$$

Secondly a "magnetic" resistor R_m is connected to the branch of \mathcal{P}_2 . Similar to the behavior of a electrical resistor, the "magnetic" voltage drop (magneto-motive force MMF F) across R_m is equal to R_m multiplied by the "magnetic" current (flux changing rate $\dot{\Phi} = d\Phi/dt$), governed by the equation:

$$F = R_m \cdot \dot{\Phi} \quad (5)$$

Please note that there is no magnetic resistor placed at the left hand side of the hysteresis permeance, which was present in the structure proposed by [23] mainly accounting for eddy current effect. This complies to the fact that we neglect the

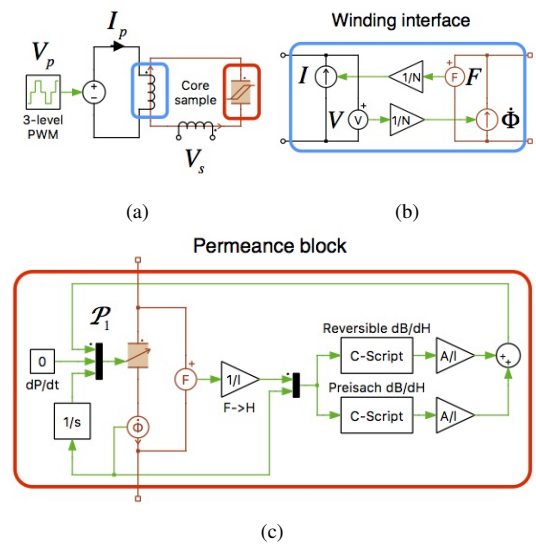


Fig. 7: (a) Simplified model of the test setup including both electrical- and magnetic circuit; (b) Internal structure of the permeance block; (c) Winding interface using gyrator structure.

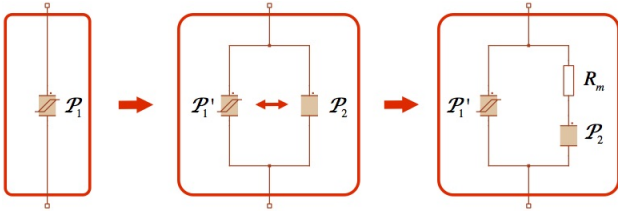


Fig. 8: Modification of the original hysteresis permeance block to incorporate relaxation effect, where the permeance value of \mathcal{P}'_1 is provided by subtracting \mathcal{P}_2 from \mathcal{P}_1 .

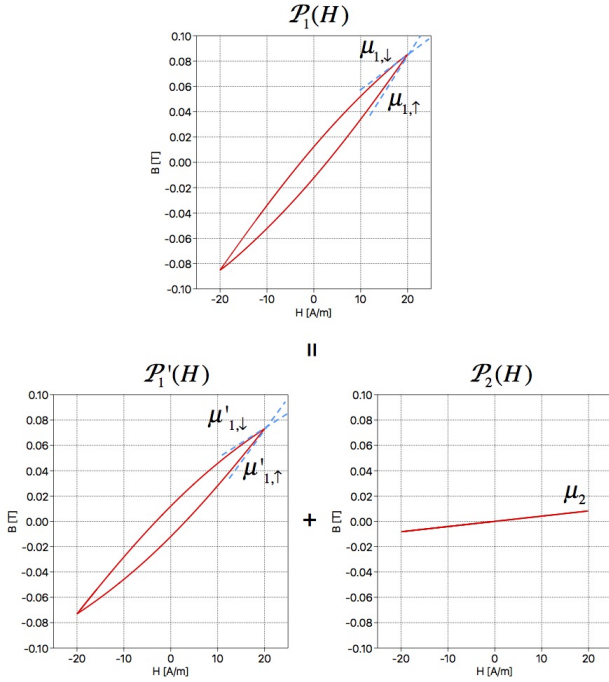
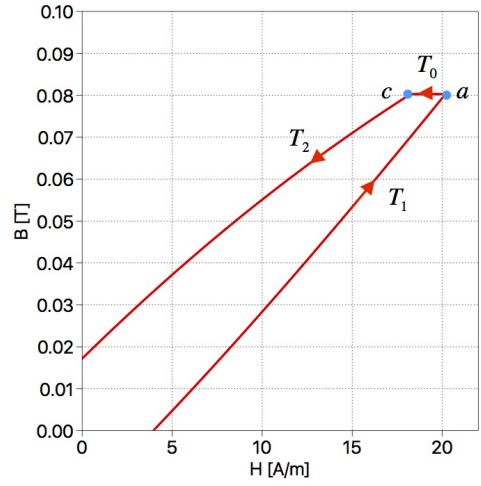
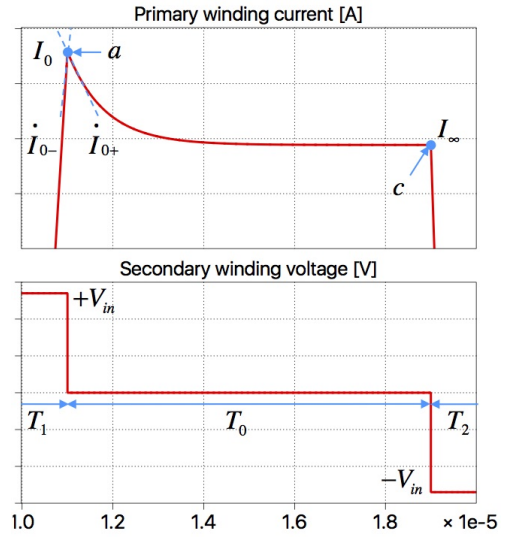


Fig. 9: Summation of the $B - H$ characteristic of \mathcal{P}'_1 and \mathcal{P}_2 under low frequency excitation.

eddy current effect of ferrite materials, which has negligible contribution to the core loss, compared to the relaxation effect and frequency independent hysteresis. It is also to be emphasized that if sufficiently low frequency sinusoidal excitation is applied, R_m has negligible effect and the $B - H$ characteristic of the overall system is equal to that of the original \mathcal{P}_1 (since the summation of \mathcal{P}'_1 and \mathcal{P}_2 is equal to \mathcal{P}_1), as the symmetrical loops within the range $[-\hat{H}, +\hat{H}]$ in Fig. 9 demonstrated. If the excitation is high frequency PWM especially with zero voltage period, the relaxation effect taking place in reality can be covered by R_m . In the next step, the mechanism of relaxation effect is interpreted in the magnetic circuit. We still take the simplified circuit model of the test setup in Fig. 7a as example, with the original permeance block proposed in [10] extended to the new structure. The circuit resistance and winding resistance are neglected. The time area in which the external excitation (generated via switching the T-shape bridge) varies from positive to negative (including the zero voltage period in between) is analyzed, the DC voltages of V_3 and V_4 are assumed to be tuned (equal to $\pm V_{in}$) so that the



(a)



(b)

Fig. 10: Circuit behavior of the proposed model in reproducing relaxation effect (a) $B-H$ characteristic including a flat part near the peak (from point a to c). (b) Time domain waveform with current descending rapidly at the beginning of the zero-voltage period.

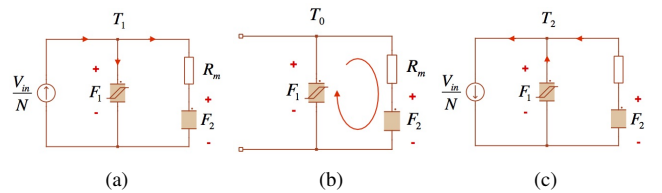


Fig. 11: Equivalent circuit configuration (a) Pos. excitation; (b) Zero voltage period; (c) Neg. excitation.

equivalent field strength is controlled in the range $[-\hat{H}, +\hat{H}]$:

- Period T_1 : When the switch Q_1 is turned on, positive voltage $+V_{in}$ is applied on the primary winding and the circuit current rises, as the interval T_1 in Fig. 10b demonstrates. In the permeance-capacitor based magnetic

circuit, the voltage excitation is translated as a "magnetic" current source (flux rate $\dot{\Phi} = V_{in}/N$), which charges the two permeance-capacitors \mathcal{P}'_1 and \mathcal{P}_2 , as demonstrated in Fig. 11a. The "magnetic" voltage (magneto-motive-force MMF, denoted as F_1) of permeance \mathcal{P}'_1 increases, which leads to increased equivalent field strength, as shown in Fig. 10b (rising interval T_1). The equivalent field strength looking from the excitation source is given by:

$$H = F_1/l \quad (6)$$

where l is the equivalent magnetic path length of the core. Under positive excitation, the MMF of \mathcal{P}_2 (denoted as F_2) also increases, however in a slower rate due to the existence of the resistor R_m . This leads to the fact that at the end of period T_1 (point a in Fig. 10a), $F_{2,0}$ is lower than $F_{1,0}$. The rising rate $\dot{F}_{1,0-} = dF_1/dt(0_-)$ directly before point a is given as

$$\dot{F}_{1,0-} = \frac{l}{(\mu_{1,\uparrow} - \mu_2) \cdot A} \cdot \left(\frac{V_{in}}{N} - \frac{F_{1,0} - F_{2,0}}{R_m} \right) \quad (7)$$

In the equation above, A is the cross section area of the core sample and N the primary winding's turns number. $\mu_{1,\uparrow}$ is the ascending permeability of the original frequency-independent model at the peak $H = \hat{H}$ (Fig. 9). The peak MMF ($F_{1,0}$) and the rising rate ($\dot{F}_{1,0-}$) can be determined from the peak primary winding current I_0 and its rising rate \dot{I}_{0-} using equation below.

$$F_{1,0} = I_0 \cdot \frac{l}{N}, \quad \dot{F}_{1,0-} = \dot{I}_{0-} \cdot \frac{l}{N} \quad (8)$$

- Period T_0 : After the switch Q_1 is turned off and Q_{01} & Q_{02} turned on, zero voltage is applied on the primary winding. In the magnetic circuit, this situation corresponds to an open circuit on the winding side, as demonstrated in Fig. 11b. Starting from point a on, there is only a circulating "magnetic" current (flux rate) flowing in the loop composed of \mathcal{P}'_1 and \mathcal{P}_2 . Due to the higher initial MMF on \mathcal{P}'_1 , the circulating "magnetic" current charges \mathcal{P}_2 and discharges \mathcal{P}'_1 , the initial value of the MMF's falling rate immediately after the zero-voltage period starts is given by:

$$\dot{F}_{1,0+} = \dot{I}_{0+} \cdot \frac{l}{N} = \frac{l}{(\mu_{1,\downarrow} - \mu_2) \cdot A} \cdot \frac{F_{2,0} - F_{1,0}}{R_m} \quad (9)$$

$\mu_{1,\downarrow}$ is the descending permeability of the original frequency-independent model at the peak $H = \hat{H}$ (Fig. 9). Afterwards, on one hand, \mathcal{P}'_1 is discharged and F_1 falls down, which is reflected by the decreasing equivalent field strength in Fig. 10a (from point a to c). Since the flux rate out of the winding side is zero, the equivalent flux density remains unchanged. On the other hand, \mathcal{P}_2 is charged up, and the MMF of both permeances gradually approach a common value. This behavior complies to the explanation of relaxation effect given in [1], that a new thermal equilibrium is gradually achieved where the

system has minimum magnetic energy state (the magnetic energy stored in the permeance capacitors is reflected by the MMF). Please note that after the MMF of the two permeances are balanced, the total energy stored is lower than that at the beginning of the zero-voltage period, and the energy difference is actually burned by the magnetic resistor R_m , which generates extra core loss. After sufficiently long time (e.g. point c in Fig. 10), following similar law of series connected electrical capacitors, the balanced MMF F_∞ fulfills the relation in below:

$$\frac{\mu_2 \cdot A}{l} \cdot (F_{2,0} - F_{1,0}) = \frac{\mu_{1,\downarrow} \cdot A}{l} \cdot (F_\infty - F_{1,0}) \quad (10)$$

F_∞ can be derived from the stabilized current I_∞ (denoted in Fig. 10b) as $F_\infty = I_\infty \cdot N/l$.

- Period T_2 : Negative voltage appears on the primary winding when the switch Q_2 is turned on at point c in Fig. 10b, which corresponds to a flux rate source in the magnetic circuit flowing in opposite direction compared to that in the period T_1 , as demonstrated in Fig. 11c. From then on, both field strength and flux density decreases rapidly as shown in Fig. 10a. Also that after this negative voltage period there is another zero-voltage interval T_0 coming and the situation is similar as discussed before, however with the relaxation effect observable at the negative peak $-\hat{H}$.

B. Parameter identification

Up to this stage, if we substitute the slopes \dot{I}_{0-} , \dot{I}_{0-} and the peak current I_0 , the stabilized current I_∞ by the corresponding values from the measurement results (e.g. that in Fig. 5b), as well as the permeability $\mu_{1,\downarrow}$ and $\mu_{1,\uparrow}$ by that measured under low frequency sinusoidal excitation (with the same peak field strength), the equations (7), (9) and (10) can be solved for R_m and μ_2 and $F_{2,0}$. Thus all parameters of the extended model can be explicitly determined. With μ_2 and the geometry of the core sample, \mathcal{P}_2 is calculated as

$$\mathcal{P}_2 = \frac{\mu_2 \cdot A}{l} \quad (11)$$

The measurement results for parameter identification need to be taken with the highest switching frequency and longest zero-voltage period of concern, where the relaxation effect can be obviously observed. In the next section, it is proved that the parametrized model is able to reproduce the material's behavior at any lower switching frequency and shorter zero-voltage period.

Also please note that in the real test setup, the circuit resistance leads to the fact that the current damped down slowly after the active interval of relaxation effect, rather than stabilized on a final value I_∞ , as the part between point b and c in Fig. 5b indicates. The time point b when flux starts to decrease can be identified in the measured corresponding $B-H$ plot (in Fig. 5a), which indicates that the active period of relaxation effect is over. In this case, the current at point b should be considered as I_∞ for parameter identification.

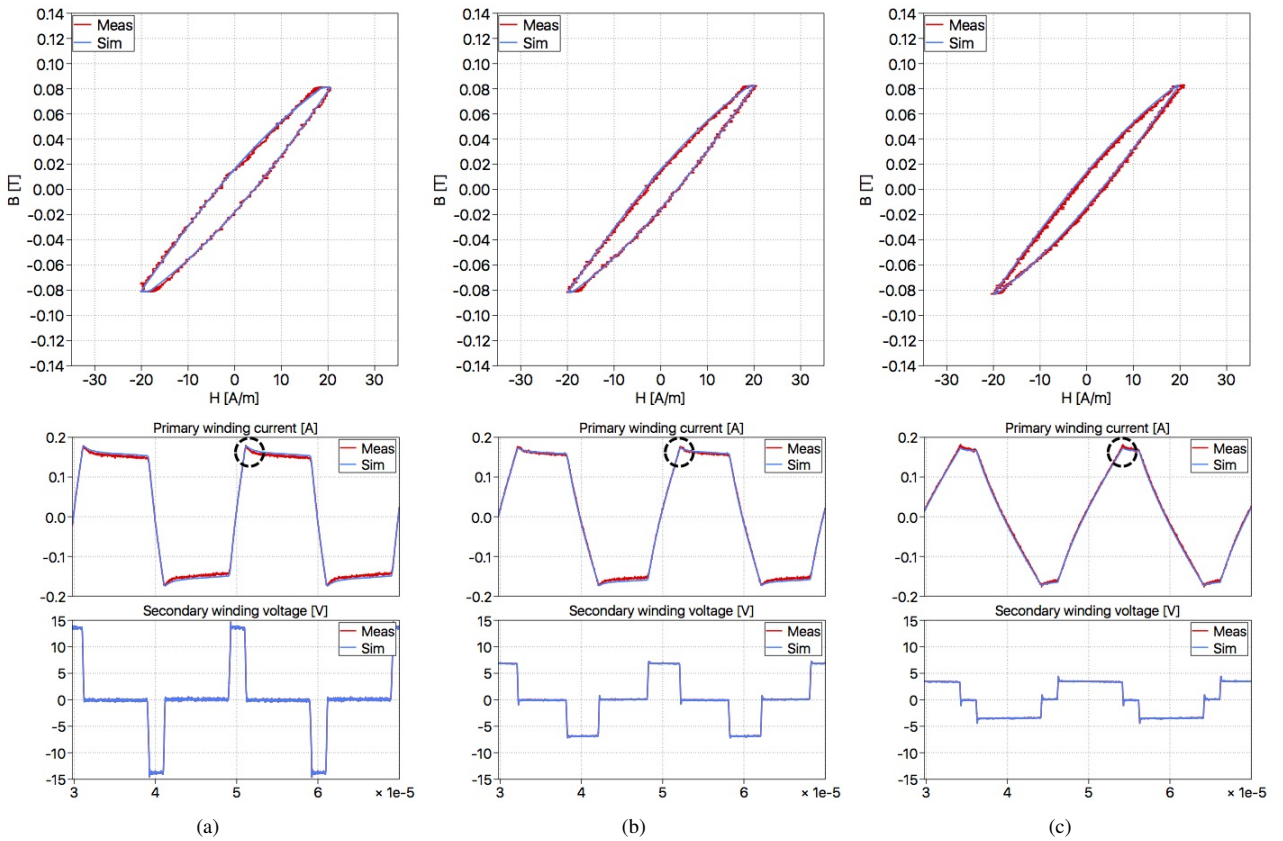


Fig. 12: $B - H$ characteristic and time domain waveform comparison between measurement and simulation under $50kHz$ PWM excitation with peak field strength $\hat{H} = 20A/m$ and different percentage of T_0 (a) 80%; (b) 60%; (c) 20%.

IV. EXPERIMENTAL VERIFICATION

In this section, several verification schemes of the proposed model are carried out using the test setup introduced in section II. The ferrite material N87 is considered as the first study case with the toroidal core of shape code "R 20.0 x 10.0 x 7.00" as sample. The model is parametrized following the process from section III-B, which is composed of two steps:

- Parameters of frequency independent effect (model proposed in [10]) are identified using experimental measurement under sinusoidal excitation of $500Hz$, where frequency dependent effect is negligible.
- Parameters of relaxation effect (\mathcal{P}_2 and R_m) are identified using experimental measurement under $50kHz$ PWM excitation with 80% zero-voltage period, where the amplitudes of the DC sources are trimmed to obtain peak field strength amplitude equal to $\hat{H} = 20A/m$. The resulted parameters are $\mathcal{P}_2 = 3.18 \cdot 10^{-7} H$ and $R_m = 3.03 \Omega^{-1}$.

The circuit model is established in system level simulation software PLECS complying to the test setup, with the same structure shown in Fig. 7a. In order to exclude the influence of the circuit resistance of the test setup, the equivalent voltage source connected to the primary winding is fed with the measured secondary voltage multiplied by N_1/N_2 , where

$N_1 = N_2 = 5$ are the turns number of the primary- and secondary winding, respectively.

A. Verification under PWM excitation with different zero-voltage period

In the first group of verification schemes, the fidelity of the proposed model is verified under $f_{sw} = 50kHz$ PWM excitation with different percentage of zero-voltage period (80%, 60%, 20%), while the DC voltage sources are adjusted to maintain the equivalent peak field strength $\hat{H} = 20A/m$. As demonstrated in Fig. 12, with decreasing percentage of zero-voltage period, the flat part near the peak of the $B - H$ loops becomes narrower, which result in decreasing per-cycle energy loss (loop area) and indicates that the contribution from relaxation effect reduces. The proposed model is able to reproduce the hysteresis loop with good accuracy, and the corresponding core loss power are compared to measurement in Fig. 13, the error turns out to be under 5%. The presence of relaxation effect is also clearly reflected on the time domain waveform, where the primary winding current sinks with different steepness immediately after the zero-voltage period begins, as has been highlighted by dashed circles. In the simulation model, the frequency independent core loss (magnetic hysteresis loss) can be easily extracted by multiplying the F and $\dot{\Phi}$ of the permeance \mathcal{P}'_1 , integrating over one switching period and scaled by the switching frequency:

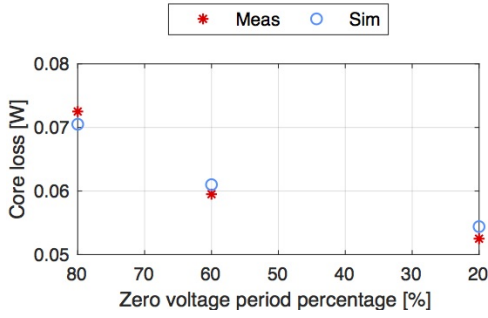


Fig. 13: Core loss comparison between measurement and simulation under $50kHz$ PWM excitation with peak field strength $\hat{H} = 20A/m$ and different percentage of T_0 .

$$P_{loss,hyst} = f_{sw} \cdot \int_T F \cdot \dot{\Phi} \cdot dt \quad (12)$$

Subtracting the frequency independent part from the simulated total loss, the frequency dependent part due to relaxation effect can be directly obtained, as demonstrated in TABLE I. With decreasing percentage of the zero-voltage period, the frequency dependent core loss reduces, while the frequency independent one remains the same.

TABLE I: Loss splitting of the simulation model into frequency independent- and dependent parts [W]

T_0 in percentage	80%	60%	20%
Total loss	0.071	0.061	0.054
f_{sw} independent loss	0.045	0.045	0.045
f_{sw} dependent loss	0.026	0.016	0.009

Afterwards, the frequency of the PWM excitation is reduced to $25kHz$, while the DC voltage sources are configured so that the equivalent peak field strength remains at $\hat{H} = 20A/m$. The core loss powers from the same model with 90%, 60% and 20% zero-voltage period (longer than that in $50kHz$ case) are compared to the measurement in Fig. 14. Good match is achieved here as well. One should notice that the hysteresis loop under $25kHz$ excitation with 90% zero-voltage period has almost the same shape as that under $50kHz$ excitation

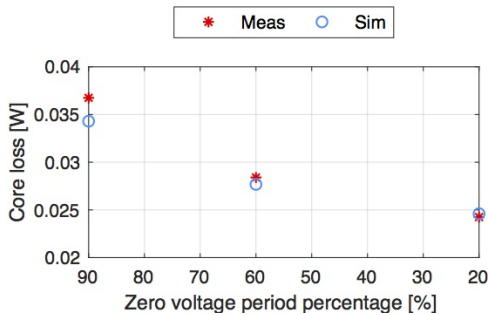


Fig. 14: Core loss comparison between measurement and simulation under $25kHz$ PWM excitation with peak field strength $\hat{H} = 20A/m$ and different percentage of T_0 .

with 80% zero-voltage period, and the core loss power of the later is two times of the former. This relation indicates that the per-cycle energy loss are the same, which has also been observed by the authors of [4]. This can be ascribed to the fact that the amplitude and duration of the non-zero (positive or negative) voltage excitation are the same in these two cases, while the zero-voltage phase are both long enough, so that the new thermal equilibrium can be completed reestablished after the relaxation process (permeance capacitors fully recharged) and the same amount of excessive energy loss is burned.

B. Verification under PWM excitation with different duty cycle

In multiple existing publications, significantly larger core loss was reported under strongly asymmetrical PWM excitation than that with 50% duty-cycle, which can be ascribed to frequency dependent effect of ferrite materials. In the second group of verification schemes, $50kHz$ PWM excitation with different duty cycles $d = 10\%, 20\%, 50\%$ (without zero-voltage period) is applied to the same core sample. The DC voltage sources are configured following the constraint $V_3 = (1 - d) \cdot V_4$, so that symmetrical hysteresis loop with equivalent peak field strength $\hat{H} = 20A/m$ can be obtained. The $B - H$ characteristic and time domain waveforms are compared between simulation result and experimental measurement in Fig. 16.

In comparison to the result under symmetrical PWM excitation shown in Fig. 16c, the top-right corner of the $B - H$ loop under 10% duty-cycle PWM in Fig. 16a is obviously blunter, which adds up to the loop area. This phenomenon arises from the fact that the amplitude of the positive voltage (V_3) is significantly higher than that of the negative one (V_4), the situation at the positive-to-negative transition is similar to the positive-to-zero transition under PWM excitation with zero-voltage period, so that relaxation effect is taking place with considerable strength. In the case of symmetrical PWM excitation ($d = 50\%$) however, the material is demagnetized in the same rate as it was magnetized, so that relaxation effect has nearly no time to reestablish the new thermal equilibrium. Using the magnetic circuit in Fig. 11, this can be intuitively interpreted as: Although \mathcal{P}'_1 is charged to higher F than \mathcal{P}_2

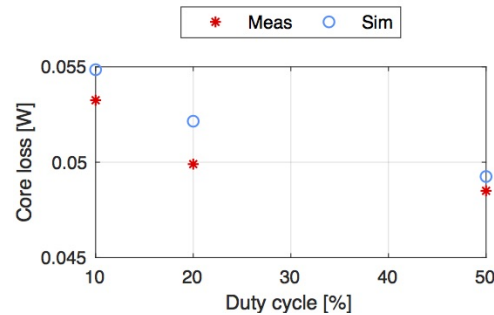


Fig. 15: Core loss comparison between measurement and simulation under $50kHz$ PWM excitation with peak field strength $\hat{H} = 20A/m$ and different duty cycle.

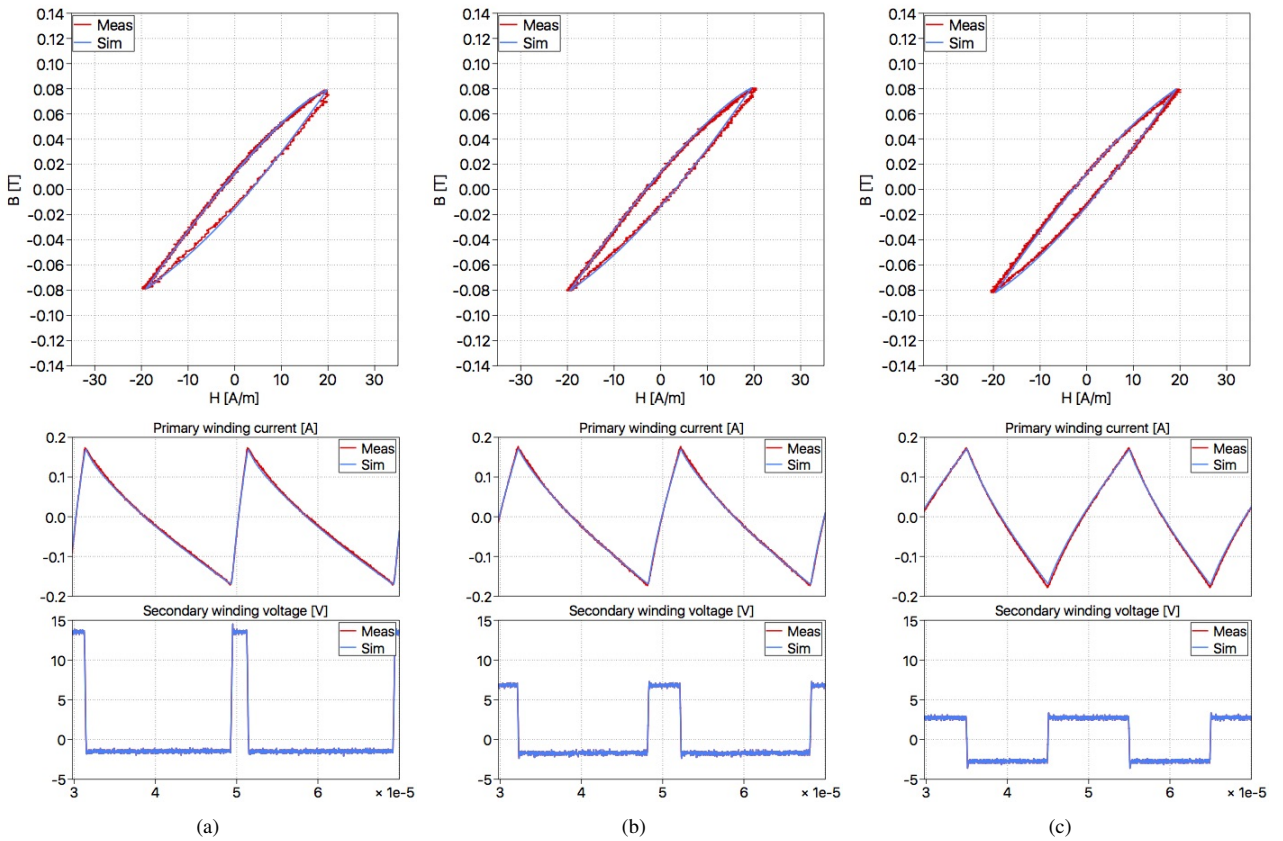


Fig. 16: $B - H$ characteristic and time domain waveform comparison between measurement and simulation under $50kHz$ PWM excitation with peak field strength $\hat{H} = 20A/m$ and different duty cycle (a) 10%; (b) 20%; (c) 50%.

after the positive excitation, it is rapidly discharged afterwards and its F sinks towards that of \mathcal{P}_2 in such short time that very less energy can be burned on R_m . All the aforementioned phenomena have been captured by the proposed model both qualitatively and quantitatively, good match has been obtained in $B - H$ characteristic and time domain waveform shown in Fig. 16 and the core loss shown in Fig. 15.

C. Verification under PWM excitation with different amplitude

In the third group of verification schemes, the core sample is still excited by $50kHz$ PWM excitation with inclusion of zero-voltage period, the DC voltage sources are configured to generate $B - H$ loops with higher equivalent peak field strength $\hat{H} = 30A/m$. Please note that in this case, the parameters of \mathcal{P}_2 and R_m are re-identified making use of the slopes \dot{I}_{0-} , \dot{I}_{0+} and the currents I_0 , I_∞ measured under 80% zero-voltage period and the permeability $\mu_{1,\downarrow}$ and $\mu_{1,\uparrow}$ by that measured under low frequency sinusoidal excitation, with equivalent peak field strength $\hat{H} = 30A/m$. The resulted values are $\mathcal{P}_2 = 5.34 \cdot 10^{-7}H$ and $R_m = 2.19\Omega^{-1}$. The simulated $B - H$ loops and time domain waveforms with different zero-voltage period are compared to that from experimental measurements in Fig. 18, and the core loss power are compared in Fig. 17, again the proposed model is able to achieve good accuracy.

In order to accommodate the model to arbitrary amplitude of excitation, \mathcal{P}_2 and R_m need to be realized as piecewise-linear variables with the value on the supporting points identified by \dot{I}_{0-} , \dot{I}_{0+} , I_∞ , $\mu_{1,\downarrow}$ and $\mu_{1,\uparrow}$ measured under the corresponding amplitudes of excitation (with sufficiently long zero-voltage period). The modified structure of the model is demonstrated in Fig. 19, where the piecewise-linear relation between the equivalent field strength and the permeance, magnetic resistor values are included in the $f(u)$ function block, with the

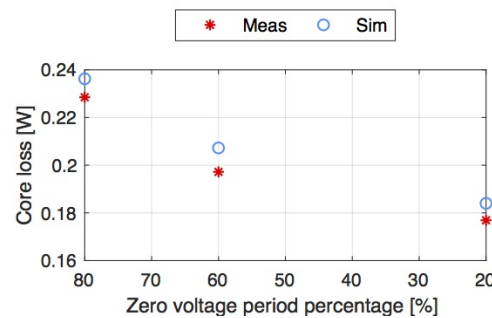


Fig. 17: Core loss comparison between measurement and simulation under $50kHz$ PWM excitation with peak field strength $\hat{H} = 30A/m$ and different percentage of zero-voltage period T_0 .

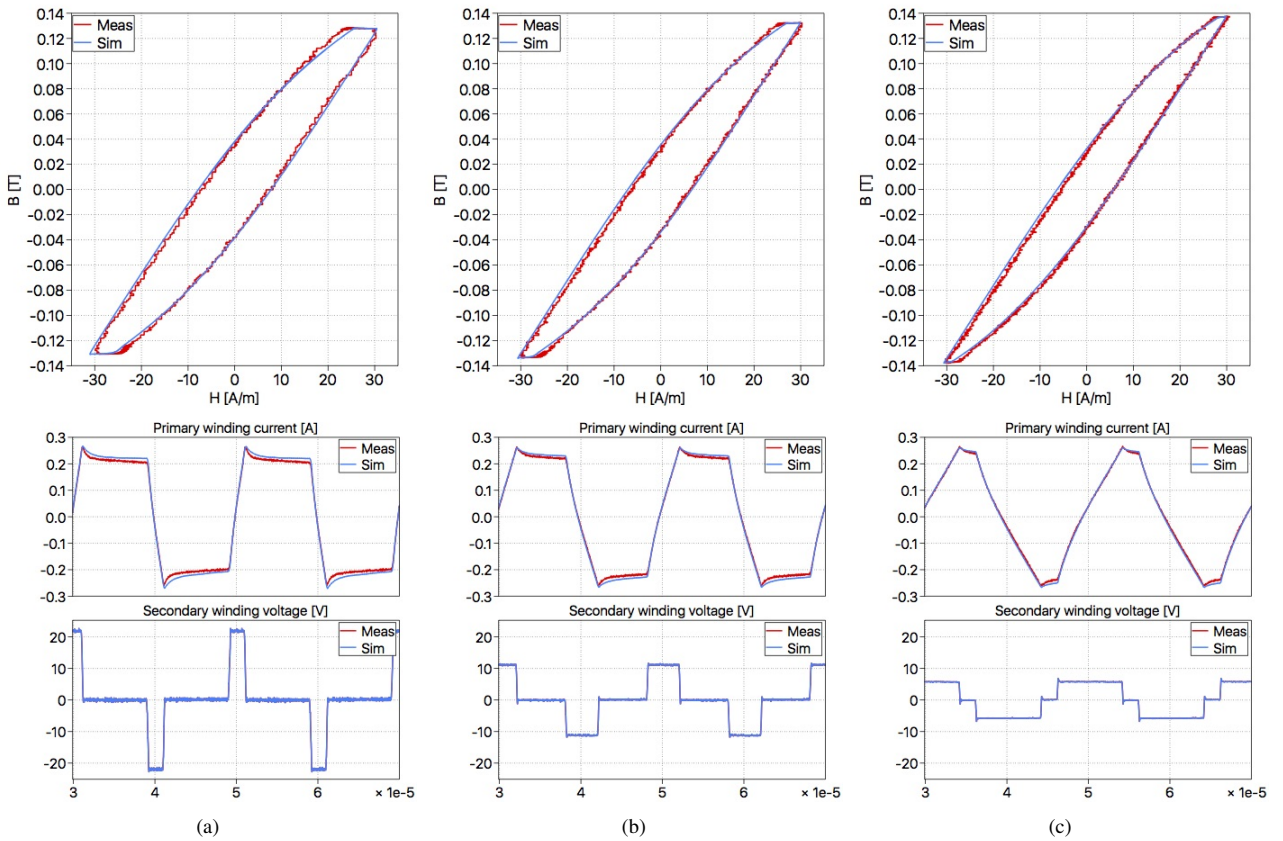


Fig. 18: $B - H$ characteristic and time domain waveform comparison between measurement and simulation under $50kHz$ PWM excitation with peak field strength $\hat{H} = 30A/m$ and different percentage of T_0 (a) 80%; (b) 60%; (c) 20%.

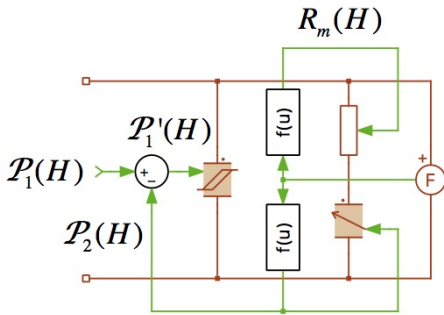


Fig. 19: Piecewise-linear realization of \mathcal{P}_2 and R_m to accommodate arbitrary amplitudes of excitation.

measured MMF of the permeance \mathcal{P}'_1 as input.

D. Verification under PWM excitation with DC offset

Using the model structure proposed in Fig. 19, arbitrary hysteresis loop inside the field strength range $H \in [-30, +30] A/m$ can be represented, even with DC bias. For verification, a differential component ΔV_{dc} is introduced into the voltage reference of the power amplifiers V_3 and V_4 , that is:

$$V_3 = V_{dc} + \Delta V_{dc}, \quad V_4 = V_{dc} - \Delta V_{dc} \quad (13)$$

Due to the existence of circuit resistance on the primary side, the differential component ΔV_{dc} is able to stabilize the hysteresis loop at desired field strength offset. Adjusting both V_{dc} and ΔV_{dc} , the biased hysteresis loop in the field strength range $H \in [0, +30] A/m$ is obtained under different percentage of zero-voltage period. The simulation result is compared to the measurement in Fig. 20, where the $B - H$ characteristic exhibits slight asymmetry and is captured by the simulation model. The core loss comparison in Fig. 21 also provides good match. The trend that with shorter zero voltage period, the core loss decreases, is also valid for the biased loop. Observable discrepancy of the loop shape is present in Fig. 20c, where the relaxation effect has minor impact and the frequency independent hysteresis loss dominates, due to the fact that the static hysteresis model is not particularly parametrized taking biased loops into account. Still thanks to the physics-based intricacy of the Preisach model, acceptable accuracy of the biased loop is still achieved. Also please pay attention that the flux density of the biased loop depends on the historical pre-magnetization in reality, which can not be explicitly measured in the existing test setup. Therefore in the $B - H$ plots of Fig. 20 the B range is displayed centered at zero (as integration of the secondary winding voltage).

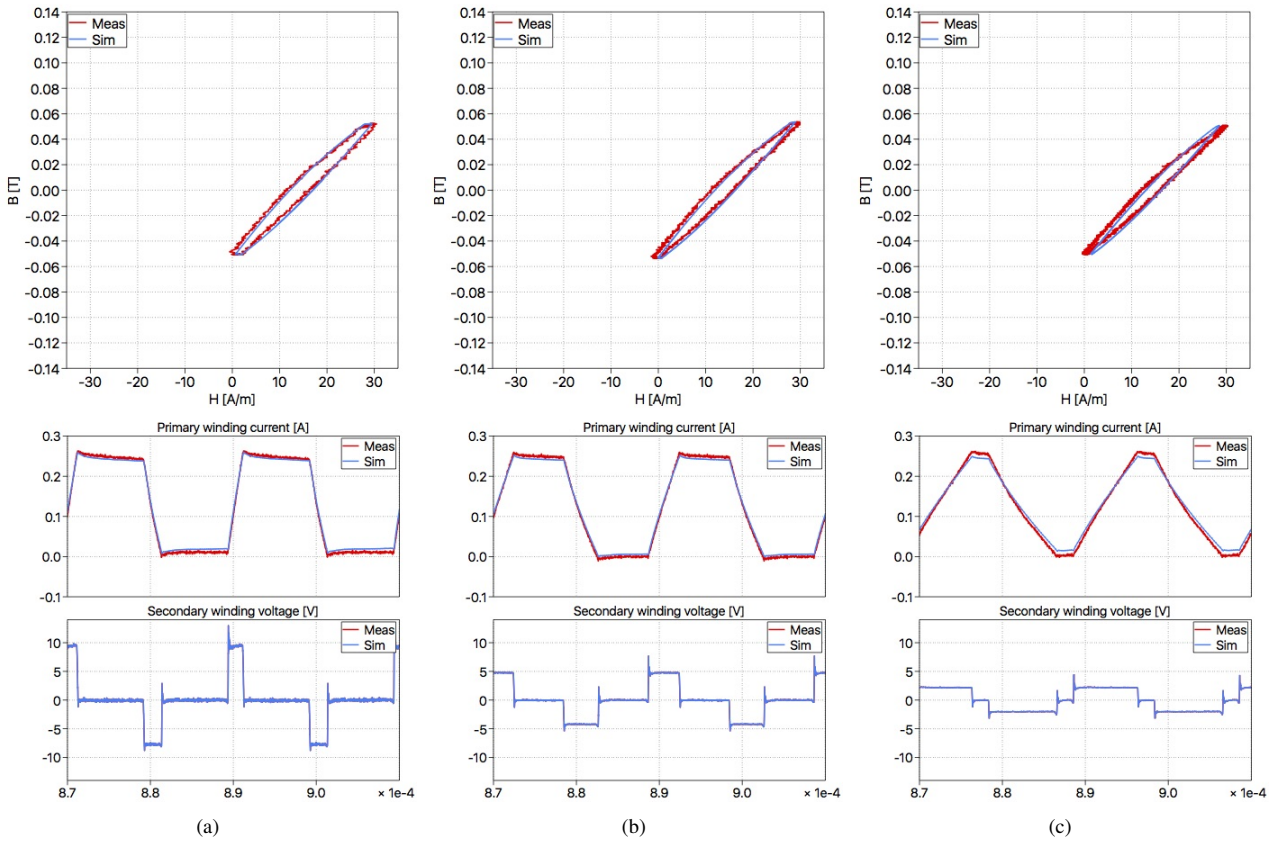


Fig. 20: $B - H$ characteristic and time domain waveform comparison between measurement and simulation under $50kHz$ PWM excitation with biased field strength range $H \in [0, +30]$ A/m and different percentage of T_0 (a) 80%; (b) 60%; (c) 20%.

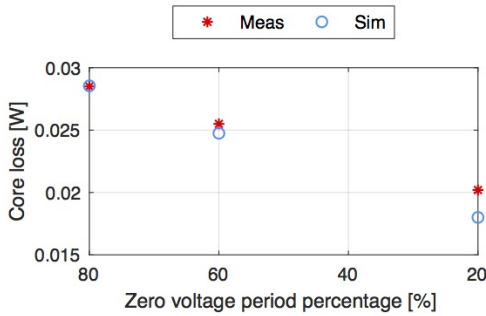


Fig. 21: Core loss comparison between measurement and simulation under $50kHz$ PWM excitation with biased field strength range $H \in [0, +30]$ A/m and different percentage of zero-voltage period T_0 .

E. Verification on core sample with different geometries

Once the parameters of the frequency-independent hysteresis and the frequency-dependent relaxation effect are identified using one core sample for certain ferrite material, the model can be easily extended to other geometries. Assuming that the core sample for parametrization has cross section area A_1 and equivalent magnetic path length l_1 , the permeance $\mathcal{P}'_{1,(2)}$, $\mathcal{P}_{2,(2)}$ and magnetic resistor $R_{m,(2)}$ of another core with other cross section area A_2 and equivalent magnetic path length l_2

can be scaled from $\mathcal{P}'_{1,(1)}$, $\mathcal{P}_{2,(1)}$ and $R_{m,(1)}$, given as:

$$\mathcal{P}'_{1,(2)} = \mathcal{P}'_{1,(1)} \cdot \frac{A_2}{A_1} \cdot \frac{l_1}{l_2} \quad (14)$$

$$\mathcal{P}_{2,(2)} = \mathcal{P}_{2,(1)} \cdot \frac{A_2}{A_1} \cdot \frac{l_1}{l_2} \quad (15)$$

$$R_{m,(2)} = R_{m,(1)} \cdot \frac{A_1}{A_2} \cdot \frac{l_2}{l_1} \quad (16)$$

The verification is firstly carried out on the toroidal core with shape code "R 25.3 x 14.8 x 10.0", the geometry difference compared to the previous core sample "R 20.0 x 10.0 x 7.00" is visible in Fig. 22a. PWM excitation of $50kHz$ switching frequency and different zero-voltage period is applied, while the DC voltage sources are configured to obtain $\hat{H} = 20A/m$ peak equivalent field strength, which is similar to the case presented in the section IV-A. The comparison between simulated and measured core loss power are displayed in Fig. 22b, where only small error exists. Please note that the scaling can be also applied to the model of a complex core, where separate parts of the core are represented by a circuit of multiple permeances.

The E-core with shape code "E 20/10/6" (product number "B66311G0000X187") is taken as the next verification case as shown in Fig. 23a. The circuit model is established according to the real geometry, as shown in Fig. 23b. Each of the

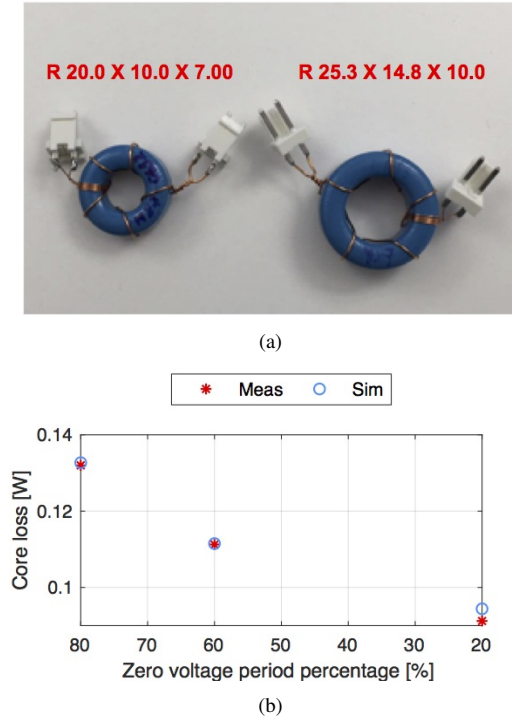


Fig. 22: (a) Core sample "R 25.3 x 14.8 x 10.0" and "R 20.0 x 10.0 x 7.00" of different geometries; (b) Core loss comparison between measurement and simulation under 50kHz PWM excitation with peak field strength $\hat{H} = 20A/m$ and different percentage of zero-voltage period T_0 , obtained from the core "R 25.3 x 14.8 x 10.0".

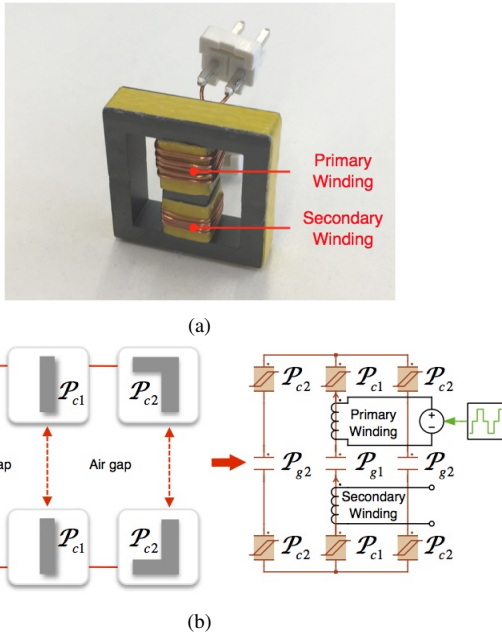


Fig. 23: (a) E-shape core sample "E 20/10/6"; (b) Simulation model with separate permeances representing middle-, side limbs and air gap of the core.

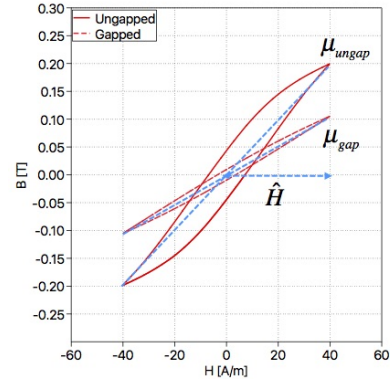


Fig. 24: $B - H$ characteristic measured from toroidal core without air gap (peak flux density \hat{B}_{ungap}) and E-core with tiny air gap (peak flux density \hat{B}_{gap}), under 500 Hz sinusoidal excitation.

permeances \mathcal{P}_{c1} and \mathcal{P}_{c2} represents one single part of the core, which has internal structure shown in Fig. 19. The equivalent geometrical parameters (cross section area and magnetic path length) are calculated following the formula proposed by [24]. All the values \mathcal{P}'_1 , \mathcal{P}_2 and R_m for the individual permeances are scaled by the geometry of the corresponding part of the E-core from that obtained previously using the toroidal core sample, following equations (14)~(16). Please note that there is no new experimental measurement needed for parametrization of the core permeances \mathcal{P}_{c1} and \mathcal{P}_{c2} , where the core material characteristic is included.

Considering the accuracy limitation of the test setup in measuring gapped cores, where the phase shift Φ_{v-i} becomes close to 90° and result in extremely high $\tan(\Phi_{v-i})$ in equation (1), the two E-core parts are tightly pressed together. Nevertheless, tiny air gap still exist in between, depending on the treatment of the joining surface during manufacture process. The length l_g of the tiny air gap between the two E-core parts is difficult to measure, but can be estimated using the measured frequency independent $B-H$ characteristic of the E-core and that of the toroidal core: The air gap result in different shape of the static hysteresis loop obtained under 500 Hz sinusoidal excitation, in comparison to that of the toroidal core, as demonstrated in Fig. 24. With the same equivalent peak field strength $\hat{H} = 40 A/m$, the loop of toroidal core and E-core exhibits different equivalent permeability μ_{ungap} and μ_{gap} (slope of the virtual straight line connecting the two peaks), respectively. l_g can be calculated as the equations in below. Please note that the air gap has linear $B-H$ characteristic, so that the permeance obtained in the two equations above remains constant for different operating conditions.

$$l_g = \frac{1}{2} \cdot \mu_0 \cdot l_e \cdot \left(\frac{1}{\mu_{gap}} - \frac{1}{\mu_{ungap}} \right) = 4.5 \mu m \quad (17)$$

where l_e is the equivalent magnetic path length of the E-core provided by the datasheet. In the circuit model shown in Fig. 23b, the air gap on the middle limb is represented by the constant permeance \mathcal{P}_{g1} and that on the side limbs by \mathcal{P}_{g2} . Please note that \mathcal{P}_{g1} and \mathcal{P}_{g2} has the same length l_g , while

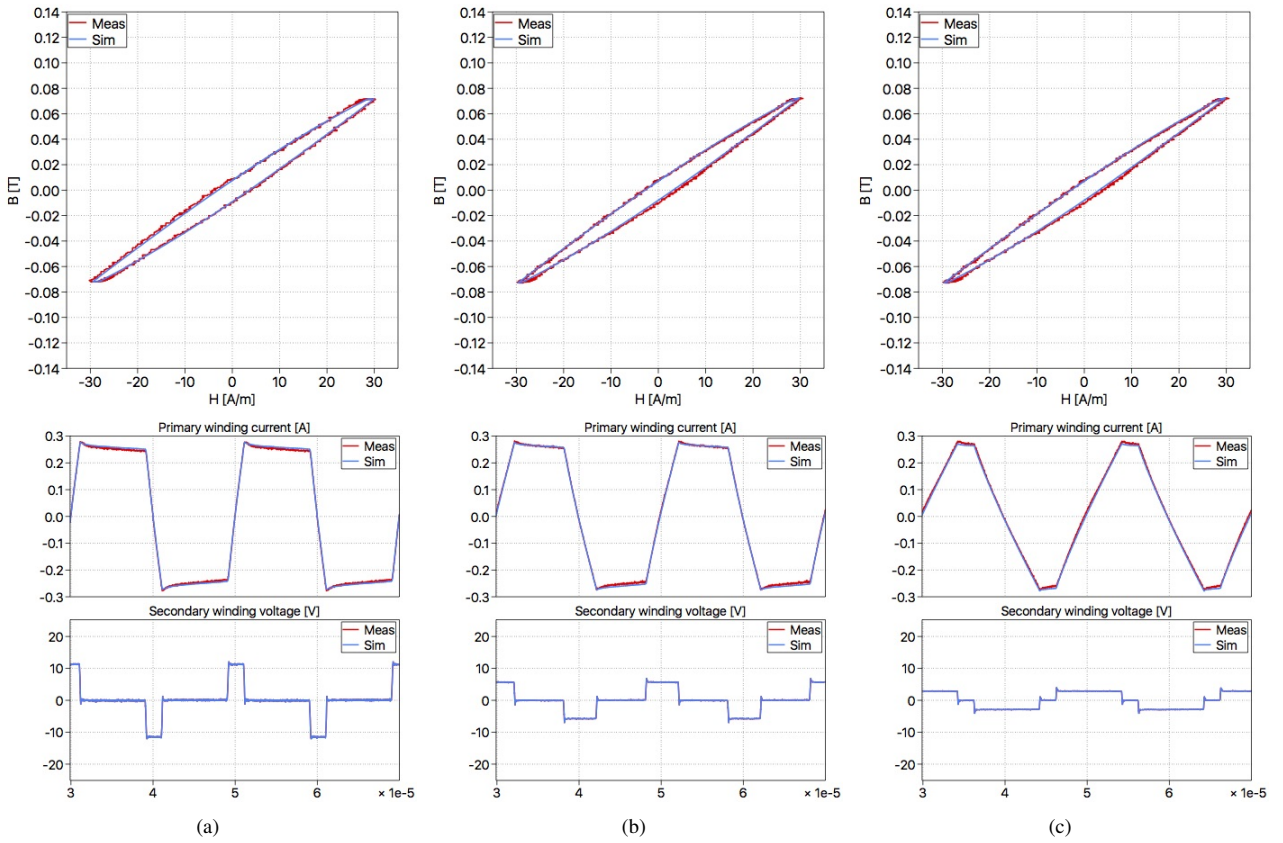


Fig. 25: $B - H$ characteristic and time domain waveform comparison of E-core "E 20/10/6" between measurement and simulation under $50kHz$ PWM excitation with equivalent peak field strength $\hat{H} = 30A/m$ and different percentage of zero-voltage period T_0 (a) 80%; (b) 60%; (c) 20%.

the cross section area are configured to be the same as \mathcal{P}_{c1} and \mathcal{P}_{c2} , respectively.

$$\mathcal{P}_{g1} = \mu_0 \cdot \frac{A_{c1}}{l_g}, \quad \mathcal{P}_{g2} = \mu_0 \cdot \frac{A_{c2}}{l_g} \quad (18)$$

The verification is still carried out under PWM excitation with different length of zero-voltage period, the DC voltage output of V_3 and V_4 are configured to generated unbiased $B - H$ loop with peak field strength $\hat{H} = 30 A/m$. The time domain waveforms and the $B - H$ characteristic are compared between measurement and simulation in Fig. 25. Due to the presence of the tiny air gap, the $B - H$ characteristic has lower loop area and slope, in comparison to that of the toroidal core in Fig. 18. The influence of the zero-voltage period on the core loss is well reflected by the proposed model with maximum error of 13.5%, as demonstrated in Fig. 26. The error is larger than the case of the toroidal core presented in Fig. 17, which can be ascribed to the estimation error of the air gap length l_g .

F. Verification on core sample of different ferrite material

In the last group of verification schemes, the model is verified with another ferrite material N30 from EPCOS. The toroidal core with shape code "R 16.0 X 9.60 X 6.30" is

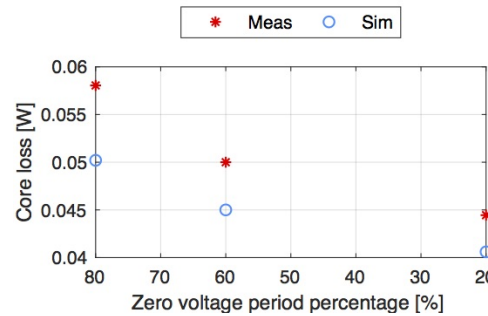


Fig. 26: Core loss comparison of E-core "E 20/10/6" between measurement and simulation under $50kHz$ PWM excitation with equivalent peak field strength $\hat{H} = 30A/m$ and different percentage of zero-voltage period T_0 .

taken as sample. Again $50kHz$ PWM excitation with different zero-voltage period is applied to the core sample, in which case the relaxation effect can be obviously observed. The model is parametrised using the measurement with 80% zero-voltage period and peak equivalent peak field strength $\hat{H} = 20A/m$. The resulted parameters are $\mathcal{P}_2 = 3.79 \cdot 10^{-7} H$ and $R_m = 2.43 \Omega^{-1}$, which are constant since measurement of one field strength amplitude is taken for parametrization. The

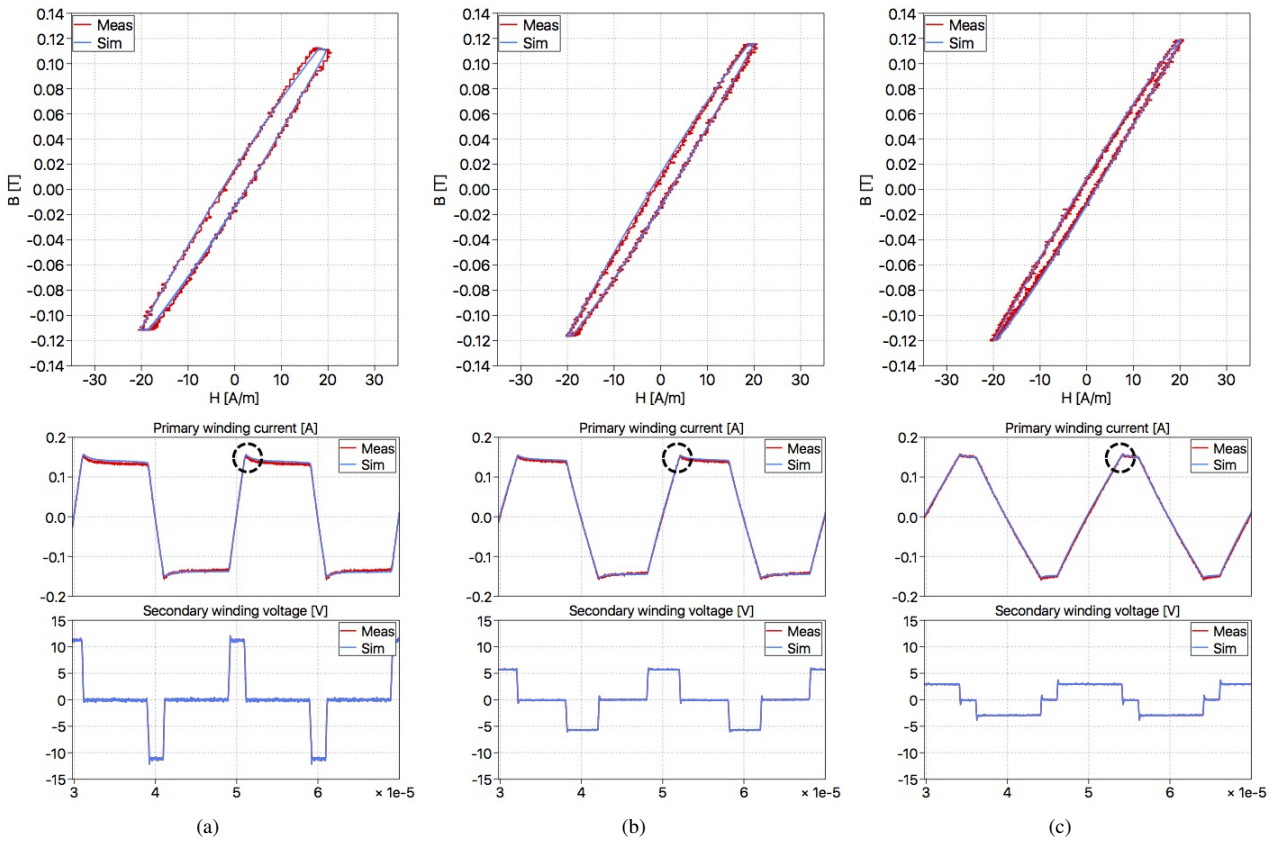


Fig. 27: $B - H$ characteristic and time domain waveform comparison between measurement and simulation of ferrite material N30 under 50kHz PWM excitation with peak field strength $\hat{H} = 20\text{A/m}$ and different percentage of zero-voltage period T_0 (a) 80%; (b) 60%; (c) 20%.

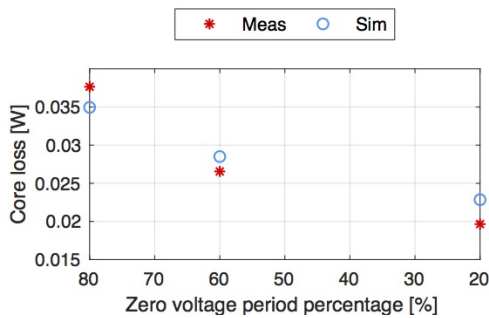


Fig. 28: Core loss comparison between measurement and simulation of ferrite material N30 under 50kHz PWM excitation with peak field strength $\hat{H} = 20\text{A/m}$ and different percentage of zero-voltage period T_0 .

comparison between simulation and measurement regarding the $B - H$ characteristic, the time domain waveform and the core loss power are presented in Fig. 27 and Fig. 28, respectively. In comparison to the ferrite material N87, the $B - H$ loop of N30 exhibits significantly different shape, and still, good match has been achieved by the proposed model.

This work focus on frequency dependent core loss of ferrite materials, whose electrical conductivity is extremely low. In

this way, the conductivity related eddy current effect can be neglected in the frequency range that has been considered ($\leq 50\text{kHz}$), and the relaxation effect which is not related to the material conductivity can be clearly analyzed. For the other metal based materials (e.g. metal powders or nanocrystalline), the frequency dependent core loss can be dominated by the eddy current effect, which is however, not in scope of this work.

V. APPLICATION OF THE PROPOSED MODEL

One application of the proposed model is the optimization of the magnetic component, which can be carried out in an intuitive way, taking into account the its desired operating condition in the target power converter system. Since optimization method is not in scope of this work, only a simplified workflow is demonstrated in Fig. 29 and described in below:

- (1) At first, the required material characteristic under low frequency sinusoidal- and high frequency PWM excitation is obtained using a toroidal core sample.
- (2) Secondly the permeance model is parametrized following the procedure introduced in section III.
- (3) Then, the iterative optimization of the magnetic component can be started. In each iteration, a different core geometry and turns number of the windings are chosen.

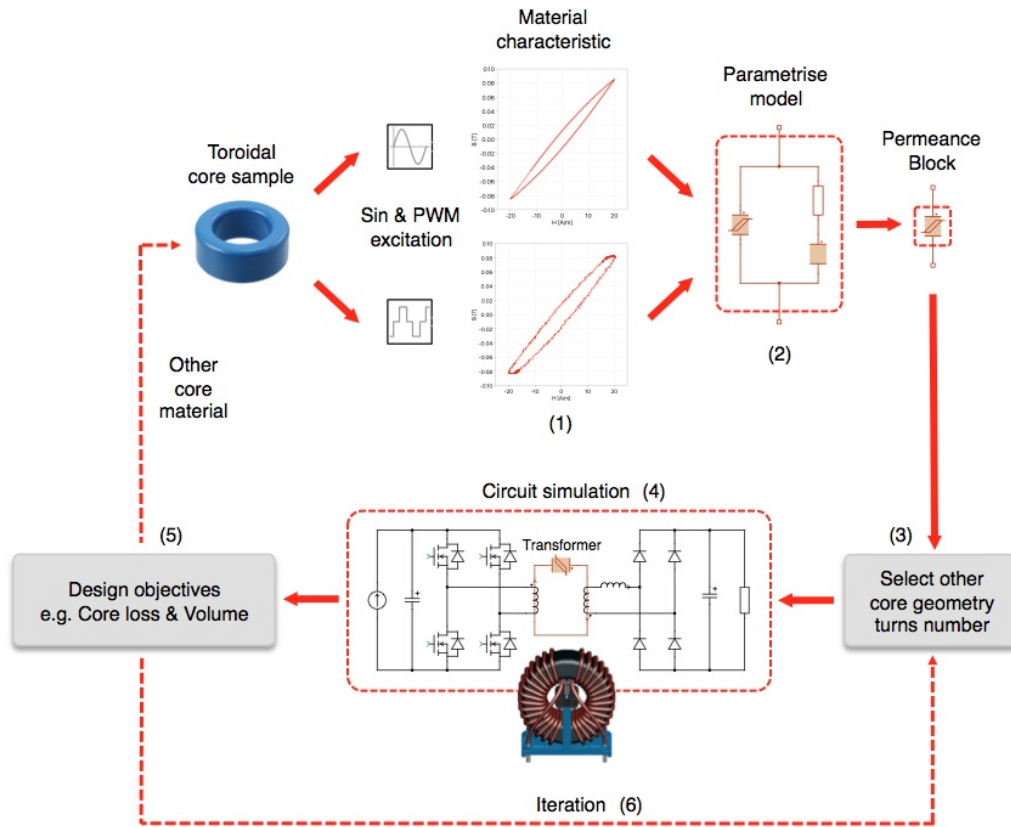


Fig. 29: Work flow using the proposed model to optimize magnetic component design

- (4) The permeance block is integrated into the circuit simulation environment of the target power converter system and scaled by the core geometry, according to the description in section IV-E. Also the winding components is configured by the desired turns number. The circuit model is simulated in time domain and the core loss power is obtained.
- (5) Taking the simulated core loss power and the volume of the selected core in the current iteration, certain objective function as trade-off between power loss and volume is evaluated.
- (6) The output of the objective function indicates the direction to change the core geometry and winding turns number, afterwards the new core geometry and winding turns number are applied to the circuit model in the next iteration.

VI. CONCLUSION

This work proposes a modeling approach for frequency-dependent core loss of ferrite core materials, which takes into account both frequency-independent magnetic hysteresis and frequency-dependent relaxation effect. The model can be seamlessly integrated into system-level circuit simulations, and can be easily extended to different geometries. The fidelity of the proposed model has been verified under PWM excitations of different switching frequencies, zero voltage-period and duty-cycle. With the proposed model, the power electronic

designers are able to get intuitive insight to the magnetic core loss of magnetic components using ferrite materials, in relation to the individual operating conditions. Based on the simulation result, improvement or **optimization** on the magnetic component itself or the power electronic circuitry can be carried out in an efficiently way.

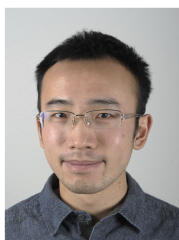
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