Fully textured monolithic perovskite/silicon tandem solar

cells with 25.2% power conversion efficiency

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Abstract

Tandem devices combining perovskite and silicon solar cells are promising candidates to

achieve power conversion efficiencies >30% at reasonable costs. State-of-the-art monolithic

2-terminal perovskite/silicon tandems have so far featured silicon bottom cells that are

polished on their front side to be compatible with the perovskite fabrication process. This

concession leads to higher potential production costs, higher reflection losses and non-ideal

light trapping. To tackle this issue, we developed a top cell deposition process that achieves

the conformal growth of multiple compounds with controlled optoelectronic properties

directly on the micron-sized pyramids of textured monocrystalline silicon. Tandem devices featuring a silicon heterojunction cell and a nanocrystalline silicon recombination junction demonstrate certified steady-state efficiencies of 25.2%. Our optical design achieves a current density of 19.5 mA/cm² thanks to the silicon pyramidal texture and suggests a path for the realization of 30% monolithic perovskite/silicon tandems.

The photovoltaics market is dominated by the crystalline silicon (c-Si) technology with ever improving efficiencies and lowering costs. Thanks to continuous research, c-Si solar cells are approaching their theoretical Auger efficiency limit of 29.4%, with a current record at 26.7%. One approach to overcome this single-junction limit consists in stacking several absorbers of different bandgaps together in a multi-junction device to reduce thermalization losses. Recently, a power conversion efficiency of 32.8% was reached for a dual-junction combining III-V materials and Si. However, costly III-V deposition techniques have so far hindered the market penetration of this technology. Recently, organic-inorganic lead halide perovskite solar cells have been identified as a promising and potentially cheaper top cell alternative for tandems with bottom cells made of c-Si⁶ and chalcogenides. Perovskites exhibit a high power conversion efficiency (up to 22.7% at the single-junction level^{3,8}), a sharp optical absorption edge⁹ and a tunable bandgap in the ideal range for silicon-based tandems (1.5-1.8 eV). These properties should theoretically allow perovskite/silicon tandem solar cells to reach efficiencies >30% at reasonable production costs. ^{6,11}

Experimentally, values over 23% and 26% have been demonstrated for serially-connected 2-terminal monolithic and mechanically-stacked 4-terminal configurations, respectively. ^{12,13} So far, the latter approach has enabled higher performances, as both subcells are fabricated independently with fewer constraints imposed by the other cell (regarding texture, cell polarity and processing temperature) and because each subcell can be operated at its

respective maximum power point. However, monolithic 2-terminal tandems are more attractive considering installation/system aspects and efficiency potential. They require fewer additional power electronic circuits (inverters and wires) and their high efficiency potential results from a gain in photocurrent due to reduced parasitic absorption (they do not require the two intermediate transparent conductive oxide electrodes, one for each subcell, needed in a 4-terminal design). Despite these advantages, this approach comes with more constraints with respect to device processing. The main challenge is to develop a tailored top cell process that i) preserves the optoelectronic quality of the c-Si bottom cell and recombination layer, ii) ensures that the two subcells generate a high and equivalent photocurrent (at an open-circuit voltage as close as possible to the bandgap limit) and iii) yields conformal perovskite cells of high quality on double side textured c-Si cells to benefit increased light trapping in the infrared and from the reduced primary reflection due to double bounce effects (transforming a 10% reflection on a flat surface to 1% on a textured one as incident light reflected by a pyramid will likely reach an adjacent pyramid and be absorbed)

These first two challenges have already been addressed in state-of-the-art tandem devices. First, low-temperature (< 200°C) top cell deposition steps have been developed to prevent the degradation of silicon heterojunction (SHJ) bottom cells, ^{14,15} a c-Si technology selected for its high open-circuit voltage ¹⁶ and excellent near-infrared response. ¹⁷ Secondly, the thickness of the perovskite absorber has been tuned in coordination with its bandgap, e.g. by depositing a 500-nm-thick absorber consisting of a perovskite material with a bandgap of 1.63 eV¹² to achieve matched subcell currents >18 mA/cm². The last challenge, the development of fully textured monolithic tandems directly compatible with monocrystalline Si industry standards, has so far not been mastered. Most perovskite deposition methods rely on solution-based spin coating. ¹⁸ These methods do not produce conformal layers on fully textured monocrystalline Si bottom cells with their micron-sized (111) c-Si pyramids (see Figure 1). Due to this issue, current record devices are made with front side polished c-Si bottom cells. This leads to

potentially higher production costs,⁶ strong front surface primary reflection losses and poor light trapping in the infrared part of the spectrum where c-Si absorbs weakly. The use of wafers without a front side texture limits the photocurrent to realistic values of ~19 mA/cm² (including shadow losses induced by the front metal grid), a value that is ~1 mA/cm² lower in comparison to a fully textured design.^{19,20} While various fabrication protocols based on physical and chemical vapor phase deposition have been developed,^{21–23} including hybrid sequential recipes combining thermal evaporation of the Pb-containing precursor followed by spin coating of the organohalide compounds,^{7,24} perovskite cells deposited on textured c-Si bottom cells have not been reported yet.

To fully harvest the high photocurrent/efficiency potential of monolithic 2-terminal tandems, we developed a hybrid two-step deposition method combining sequential co-evaporation and spin coating²⁵ to yield conformal perovskite absorber layers on textured SHJ bottom cells. The other layers of the perovskite cell are produced by means of thermal evaporation, atomic layer deposition and sputter deposition. A nanocrystalline silicon recombination junction is used between the two subcells, providing a high resilience to shunts.^{26,27} Combining these features, we demonstrate a fully textured monolithic perovskite/ SHJ tandem with a current density of 19.5 mA/cm² and certified steady-state power conversion efficiency of 25.2%.

Conformal perovskite layers and cells on textured c-Si

Figure 1a schematically illustrates the fully textured monolithic perovskite/SHJ tandem cells developed here. A double side textured SHJ bottom cell with the n-type hydrogenated amorphous silicon layer, a-Si:H(n), facing sunwards was capped on its front side by either an indium tin oxide (ITO) or a nanocrystalline hydrogenated silicon (nc-Si:H) recombination junction: The former is a transparent conductive oxide (TCO) often used as recombination junction in perovskite/SHJ tandem devices 12,14,15,28 or as front and rear electrodes in SHJs, 29

while the nc-Si:H recombination junction has shown good optical and electrical properties in a previous study focusing on tandems using front side polished wafers.²⁶

The perovskite cell was then deposited in the p-i-n configuration. The use of textured SHJ bottom cells precludes solution processing for the perovskite absorber and selective layers. Indeed, attempts to spin-coat them resulted in the accumulation of the materials in the c-Si valleys, while the summits remained bare (Figure S1). This created shunts between the front side transparent conductive oxide and the SHJ bottom cell. An alternative hybrid deposition approach was developed to prevent this issue. A 2,2',7,7'-tetra(N,N-di-tolyl)amino-9,9-spirobifluorene (spiro-TTB) hole-selective layer was first deposited by thermal evaporation (Figure S2a-c). The perovskite was produced using a two-step process by co-evaporating a porous lead iodide (PbI₂) and cesium bromide (CsBr) template (Figure S2d) before spin-coating an organohalide solution (formamidinium iodide, FAI, and formamidinium bromide, FABr, Figure S2e). The organohalide solution infiltrates the pores of the PbI₂/CsBr scaffold, while the rest of the solution is ejected off the substrate during spin coating. This effect prevents any accumulation of organohalides, e.g. in the pyramid valleys (Figure S2e). The Cs_xFA₁-_xPb(I,Br)₃ absorber was then crystallized by annealing at 150°C in ambient air.²⁵ The electron-selective layers, here a stack of LiF and C₆₀, were subsequently deposited by thermal evaporation. A buffer layer of SnO₂ was then deposited by atomic layer deposition and indium zinc oxide (IZO) was sputtered onto the stack, serving as the transparent front electrode.30,31 The front metal grid was made of evaporated Ag, while MgF2 acted as antireflective coating. Scanning electron microscopy (SEM) images shown in Figure S2 detail the different steps of this process. Overall, this top cell deposition process does not exceed 150 °C, which is compatible with the use of hydrogenated silicon thin films for the bottom cell and recombination junction.³²

This process yields a perovskite top cell that fully covers the micron-sized Si pyramids, as confirmed by the atomic force microscopy (AFM) 3D reconstructions and SEM images

shown in Figure 1b-d and Figure S2f-g. Furthermore, cross sections of the perovskite top cell of a fully textured tandem device prepared by focused ion beam (FIB) and imaged by SEM demonstrate that the perovskite layer is dense and uniformly thick (Figure 1e and Figure S2h-i). A scanning transmission electron microscopy (STEM) energy-dispersive X-ray spectroscopy (EDX) map of the perovskite top cell indicates that the inorganic elements of the perovskite, namely Cs, Pb, Br and I, interdiffuse completely during the annealing step (Figure 1f). Additional SEM, STEM and EDX data characterizing the perovskite top cell at various magnifications are shown in Figure S3. Furthermore, X-ray diffraction data indicates a full conversion of PbI₂ to the perovskite phase (Figure S4).

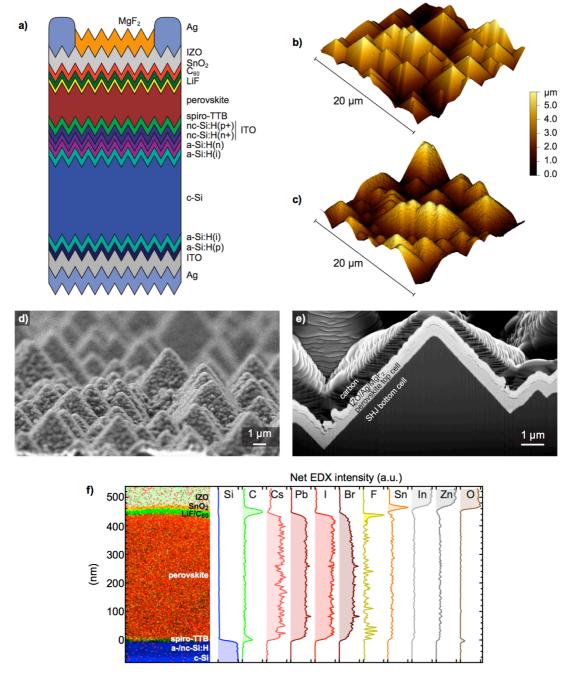


Figure 1: Cell design and microstructure of the perovskite top cell on textured silicon. a) Schematic view of a fully textured monolithic perovskite/SHJ tandem; AFM surface morphology 3D views of b) bare c-Si pyramids and c) c-Si pyramids covered with the perovskite layer; Secondary electron SEM image of d) the perovskite layer and e) a cross section of the full perovskite top cell deposited on the SHJ bottom cell, which was prepared at the position of a Ag finger by FIB (sample tilted to 54°, dimensions along the y axis to be multiplied by a factor 1.2 when compared to f). A carbon coating was applied to protect the top surface during the FIB milling process; f) Cross section STEM EDX map and corresponding line profiles of the relevant elements after background subtraction (K

ionization edges for all elements, with the exception of Pb, for which the L edge is used). The noise in the F signal within the perovskite layer is an artefact of the background subtraction step.

Benefits of nanocrystalline hydrogenated silicon

The current density-voltage (J-V) characteristics of the best tandem devices that combine the layers detailed above and either an ITO or an nc-Si:H junction are reported in Figure 2a. The J-V curve of the ITO-based cell is comparable to that of a filtered single-junction SHJ with an open-circuit voltage (V_{oc}) < 700 mV. This indicates that the top cell is shorted. On the other hand, the cell that employs an nc-Si:H layer stack as a recombination junction yields a V_{oc} > 1780 mV with a high efficiency (25.2% at maximum power point, MPP, as discussed below).

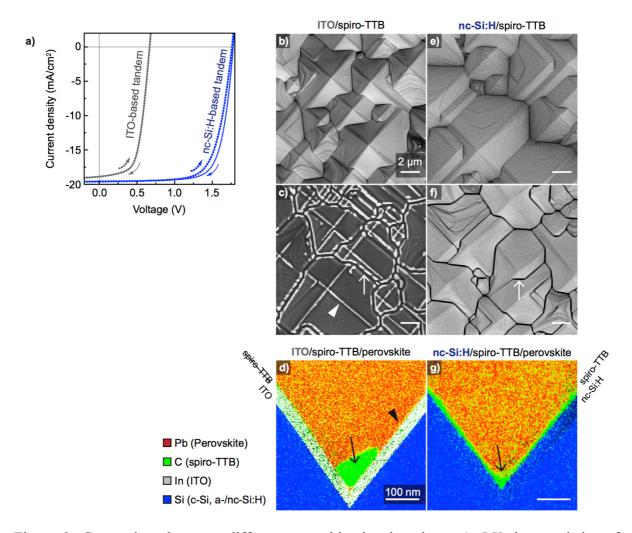


Figure 2: Comparison between different recombination junctions. a) *J-V* characteristics of fully textured perovskite/SHJ tandems that feature either an ITO or an nc-Si:H recombination

junction according to the architecture described in Figure 1. The cells were measured with a scan rate of 100 mV/s; Top view secondary electron SEM images of spiro-TTB b) as deposited on the ITO recombination layer without any perovskite layer on top and c) after annealing at 150°C to simulate the effect of the temperature applied during the perovskite crystallization process; d) STEM EDX map of a cross section of the ITO/spiro-TTB/perovskite stack at the bottom of a valley; SEM images of spiro-TTB e) as deposited on the nc-Si:H recombination junction without any perovskite layer on top and f) after annealing at 150°C; g) STEM EDX map of the nc-Si:H/spiro-TTB/perovskite stack at the bottom of a valley. Spiro-TTB slightly accumulates within the valleys during annealing when deposited on the nc-Si:H junction, while the effect is more severe with ITO (arrows, regions of dark contrast in the SEM images). This eventually leads to a local exposure of ITO (arrowheads, bright regions in the SEM images).

This significant difference in cell performance arises from a suboptimal ITO/spiro-TTB interface. While spiro-TTB is conformal after its evaporation on ITO (Figure 2b), it detaches from the pyramid summits and ridges during the 150°C annealing step required to crystallize the perovskite (Figure 2c). This locally uncovers the ITO, which appears as bright regions in the secondary electron SEM images shown in Figure 2c (indicated by arrowheads). The STEM EDX analysis shown in Figure 2d confirms the significant accumulation of spiro-TTB at the bottom of the Si valleys and its absence from some of the pyramid edges after annealing. These regions with a direct ITO/perovskite contact may act as recombination centers due to poor carrier selectivity. In comparison, when evaporated on nc-Si:H, spiro-TTB remains conformal, as confirmed by Figure 2e-g. Its thickness slightly increases at the bottom of the pyramid valleys after annealing (arrow in Figure 2g) but it still fully covers their edges. We assume that this effect is caused by a difference in surface tension between spiro-TTB and ITO when compared to nc-Si:H. Another reason that contributes to these differences in properties between ITO- and nc-Si:H-based tandem cells is the low lateral conductivity of nc-Si:H. ^{26,27,33} A high density of grain boundaries in the nc-Si:H recombination junction hinders lateral carrier transport, while the presence of large single grains that span across the junction improves transversal conductivity.²⁷ This effect increases the shunt resistance and, e.g.,

enabled the production of larger-scale perovskite/front side polished SHJ tandems in a previous study.²⁶

Improved optical system leading to a high efficiency

After identifying the adequate combination of materials, the optical quality of our device architecture was assessed. External quantum efficiency (EQE) measurements highlight the advantage of switching from a polished front side to a double side textured architecture (Figure 3). While reflections occurring in the former design induce a current loss of $3.14 \, \text{mA/cm}^2$ in the wavelength range $\lambda = 360\text{-}1200 \, \text{nm}$, the introduction of a front texture reduces the overall reflectance, which drops to an equivalent integrated photocurrent value of $1.64 \, \text{mA/cm}^2$. This optical gain stems directly from the presence of the c-Si front pyramidal texture, which i) suppresses the strong reflectance peak at $\lambda = 850 \, \text{nm}$ (reflection at the front of the Si wafer), ii) lowers primary reflections at the front of the tandem cell (double bounce effect) and iii) increases light trapping of infrared light in the Si cell. Overall, the reflection losses in fully textured silicon monolithic tandem are below 2% in the range of $\lambda = 350\text{-}1000 \, \text{nm}$, excluding metallization.

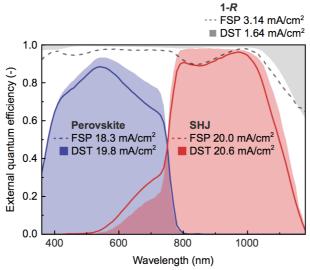


Figure 3: Improved optics with a fully textured architecture. EQE of perovskite/SHJ monolithic tandem cells with double side textured (DST) or front side polished (FSP) bottom

cells, alongside corresponding total absorbance curves, 1-*R*, where *R* is the reflectance of the device. These EQE and 1-*R* spectra do not include losses due to the front side metal grid.

A thickness/bandgap optimization to achieve current-matching conditions is shown in Figure S5. Overall, an optimum is reached for a 440-nm-thick perovskite absorber with a bandgap of 1.6 eV, with 20.1 mA/cm² generated in the perovskite and 20.3 mA/cm² in the SHJ (Figure 4a, cross section shown in Figure 1f). The simple optical system presented here, which does not contain any light trapping feature other than the c-Si pyramids, exhibits a cumulative current of 40.4 mA/cm², a value that approaches that of record silicon devices (42.5 mA/cm²)⁴ and surpasses what can realistically be achieved in front side polished devices without any complex light trapping scheme. 11,19 This high EQE photocurrent translates to an improved device performance. Fully textured monolithic perovskite/SHJ tandems featuring a nc-Si:H recombination layer yield a $V_{\rm oc} > 1.78 \text{ V}$ (over an aperture area of 1.42 cm², Figure 4b). This is comparable to the sum of the standalone subcells ($V_{\text{oc, SHJ}}$ at 0.5 sun = 700 mV; $V_{\text{oc, perovskite}}$ = 1050 mV, as shown in Figure S6). Thanks to the pyramidal Si texture, a short-circuit current density (J_{sc}) of 19.5 mA/cm² is obtained, a value this time including shadow losses induced by the front metal grid (covering 2.3% of the aperture area). The highest efficiency η , certified by Fraunhofer ISE CalLab, is 25.52% \pm 0.70% obtained from the reverse scan and 24.09 \pm 0.66% from the forward scan (unencapsulated cell, Figure 4b). Other parameters retrieved from the J-V reverse scan include a V_{oc} of 1788 \pm 12 mV, a J_{sc} of 19.53 \pm 0.37 mA/cm² and a fill factor FF of 73.0 ± 1.45 %. Maximum power point tracking yields a certified steady-state value of $25.24 \pm 0.72\%$ during 700 s (Figure S7). In-house measurement gave similar results (Figure S8). The J-V hysteresis stems likely from the presence of mobile halide ions, as demonstrated in literature. $^{34-37}$ For the same J-V scan rate, this behavior is reduced when the temperature is increased (Figure S9) in accordance with literature data.³⁸ Finally, the

sequential perovskite deposition method presented here yields reproducible results (Figure S10).

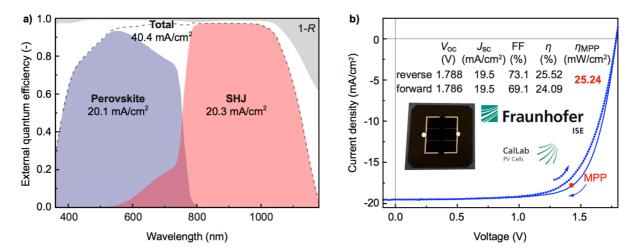


Figure 4: Certified performance of the fully textured perovskite/silicon tandem. a) EQE spectra of a current-matched fully textured monolithic perovskite/SHJ tandem cell featuring a 1.6 eV perovskite absorber with a thickness of 440 nm alongside the 1-*R* curve, both excluding losses due to the front side metal grid; b) Corresponding certified *J-V* data (1.42-cm² aperture area, device shown in the inset), which was measured with a scan rate of 100 mV/s.

The perovskite composition employed in high efficiency tandem devices yields a lower performance at the single-junction level when compared to that inferred from tandem results due to several factors (Figure S6). Perovskite single-junctions feature an ITO/spiro-TTB interface (Figure S6), which is not optimal as demonstrated in Figure 2. Even if TEM indicates that spiro-TTB remains conformal on a flat ITO substrate (Figure S11), some dewetting might occur at the position of surface irregularities. In addition, the lateral conductivity of ITO reduces the shunt resistance of the device. These effects impact the FF and V_{oc} of perovskite single-junctions when compared to their top cell counterparts. Furthermore, with the deposition process used here, perovskite compositions that achieve current-matched conditions in tandems exhibit an increased surface roughness on flat surfaces. Opening the bandgap to 1.65 eV yields a smoother film morphology and enables single-junction efficiencies of 15% at MPP, as reported in Ref. These differences between

tandems and single-junctions highlight the necessity to optimize each configuration individually.

Overall, the tandem cell presented in Figure 4b outperforms our best monolithic perovskite/SHJ tandems that feature a polished front side by more than 3% in absolute26 and the best in-house-made single-junction both sides-contacted SHJ cell by more than 2%_{abs}. ³⁹ In addition, this efficiency surpasses the current reported record for monolithic perovskite/silicon tandems by >1.6%_{abs}. 12 The results presented here demonstrate that our device architecture is optically optimized. Still, parasitic absorption could be reduced further, e.g. in the front electrode stack by thinning the C₆₀/SnO₂/TCO stack, opening the bandgap of the TCO and/or lowering its free carrier concentration.²⁵ Moreover, metallization-induced shadow losses could be mitigated further by using screen-printed Ag lines, which have a more favorable aspect ratio when compared to thermally evaporated Ag. For the cell size of ~1 cm² presented here, the 200 µm wide evaporated Ag fingers lead to shadow losses of 2.3%, a value that could be decreased to 0.6% by replacing them with 50 μ m wide screen-printed fingers (J_{sc} gain of ~ 0.3 mA/cm²). Overall, reducing both parasitic absorption and shadow losses should enable a $J_{\rm sc}$ >20 mA/cm² for this cell size. With further realistic improvements in the $V_{\rm oc}$ of the perovskite top cell to 1.2 V (while keeping a similar bandgap)⁴⁰ and reaching a fill factor value of 80% (a value close to the one of state-of-the-art perovskite/SHJ tandems with a front side polished wafer)¹² could lead to perovskite/SHJ tandem with an efficiency over 30% (V_{oc} = 1875 mV, FF = 80%, J_{sc} = 20 mA/cm², η = 30.0%).

Device stability

The devices developed above exhibit high efficiencies thanks to improved optics, a first prerequisite before any commercialization of the technology can be envisaged. The second key requirement for commercialization concerns device reliability. Planar single-junction produced with the hybrid method presented here pass 1000-hours damp heat degradation tests

at 85°C and a relative humidity of 85% (Figure S12). The stability of perovskite-based devices was then investigated further by exposing fully textured perovskite/silicon monolithic tandem cells to continuous light irradiation. So far, such light soaking tests have been reported only for single-junction perovskite devices. 41,42 Figure 5a shows the electrical characterization of a fully textured perovskite/SHJ tandem cell at MPP conditions under a continuous AM1.5G illumination without any UV-blocking filter. This measurement was carried out in ambient air (with a relative humidity level of 20-30%) without encapsulation for 61 hours. J-V scans were recorded regularly to monitor the evolution of the electrical parameters. After an initial light soaking period, the performance increases⁴³ before remaining stable at 24% at MPP for 6 hours. The efficiency then starts to decrease linearly to ~22% after 61 hours (linear rate of -0.15 %_{rel}/hour). The corresponding J-V data indicates that J-V hysteresis increases during aging (Figure 5a-b). This behavior suggests that more ions become available for migration with time. This effect might be triggered by halide migration into the carrier-selective layers under bias, 36,38 which leaves additional vacancies in the perovskite layer, or increased ionic conductivity from the diffusion of water into the hybrid perovskite material.⁴⁴ Similar degradation tests were carried out using tandem cells that were encapsulated using cover glass sheets and a butyl-edge sealant (without any laminated encapsulant, Figure 5c). This simple encapsulation scheme reduces the degradation rate such that the cell retained 90% of its initial power conversion output after 270 hours under constant illumination at MPP. Similar to previously reported degradation data for single-junction perovskite cells, 42 an exponential decay was also observed during the first 20 hours of illumination, followed by a linear regime for the next 250 hours (linear rate of -0.03 $\%_{\rm rel}$ /hour). Both $J_{\rm sc}$ and $V_{\rm oc}$ decrease accordingly after an initial increase in $V_{\rm oc}$ during the first few hours of operation (Figure 5d). Overall, the tandems presented here exhibit an encouraging light soaking stability when compared to single-junction literature data, 42,45 with tandems retaining most of their electrical properties for tens (unencapsulated) to hundreds (encapsulated) of hours without any sudden failure. But further stability improvements are required to yield fully reliable devices. These will likely arise from the optimization of the device architecture, e.g. by replacing organic carrier-selective layers by inorganic materials and by introducing buffer layers to prevent the migration iodine and its reaction with the Ag electrode. Ag oxidation was found to occur at the front and back Ag metallization, the latter indicating that some iodide transport may occur in the form of volatile species (Figure S13). This halide vapor transport could be mitigated further by introducing a polymer encapsulant, a material that may also act as an UV light filter.

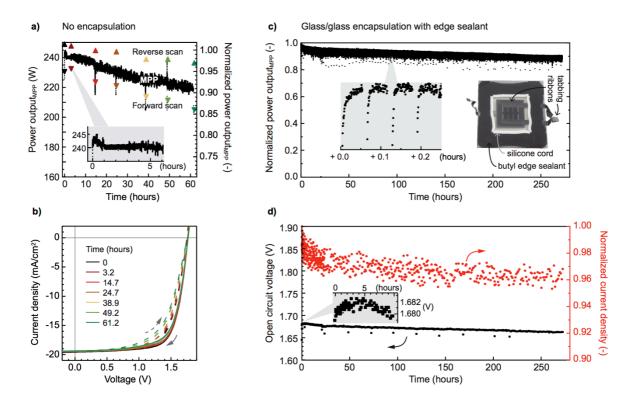


Figure 5: Device stability. a) MPP tracking of an unencapsulated fully textured perovskite/SHJ tandem solar cell measured at 25°C in air (relative humidity of 20-30%); b) J-V scans were recorded every 10 hours during the experiment shown in a). Power output values extracted from these reverse and forward J-V scans are indicated in a) by triangles pointing upwards and downwards, respectively; c) MPP tracking of a glass/glass butyl-edge sealed fully textured perovskite/SHJ tandem solar cell in air (20-30% relative humidity). This device had an initial efficiency of 23.8% before encapsulation (measured at MPP for 1000 s) and 20.7% after encapsulation. This drop was mainly due to a decrease in J_{sc} as no indexmatching encapsulant was present between the cell and the glass cover sheet. The spread in data points results from the recovery to MPP values after each J-V scan (recorded every 5 min). The left inset illustrates this recovery behavior. The right inset is a picture of the encapsulated device; d) V_{oc} and J_{sc} values obtained from reverse J-V scans. The inset shows

the $V_{\rm oc}$ during the first 10 hours of operation. Normalized power output efficiencies and currents are shown in c) and d) as the spectrum was not calibrated to 1 sun and no illumination mask was used on the encapsulated tandem cell. All J-V measurements were recorded with a scan rate of 100 mV/s.

Outlook

We demonstrate fully textured monolithic perovskite/silicon tandem solar cells and achieve a certified MPP efficiency of 25.2%. Key to this result is a top cell fabrication process that does not require any modification of the textured c-Si bottom cell, therefore allowing the use of existing industrial textured monocrystalline Si wafers. The process yields conformal perovskite, carrier-selective and contact layers directly on the micron-sized c-Si pyramids. Electrical and microstructural characterization highlights the advantages of an nc-Si:H recombination junction over a conventional TCO, especially when featuring a thermally evaporated organic hole selective layer. Our design exploits the high photocurrent potential of monolithic tandems to achieve values of 19.5 mA/cm². The pyramidal texture of the c-Si bottom cell directly reduces primary reflection losses and enhances light trapping in the infrared. In addition, the stability of tandem devices to light soaking and electrical bias was monitored up to 270 hours.

Overall, the developed tandem fabrication procedure simplifies the overall process flow, yields high efficiencies thanks to improved optics and pave the way towards monolithic perovskite/silicon tandem cells with efficiencies >30%.

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Author contributions

F.S. and J.W. contributed equally to this work. F.S and J.W designed the experiments and carried out the devices fabrication. B.A.K. and M.Br. contributed to the development of the perovskite top cell. R.M., B.P-S., L.B., M.Bo. and M.D. developed and fabricated the silicon heterojunction bottom cells. D.S., L.D. and J.J.D.L developed the ALD buffer layer. G.C. and B.N. carried out the encapsulation for stability tests. Q.J. performed the FIB and TEM characterization and, with J.W., the SEM analysis. M.Br. recorded the AFM data. F.S., J.W. and Q.J. carried out data analysis and prepared the figures. J.W. carried out the degradation stability tests and analyzed the data. Q.J., B.N., M.Bo., M.D., S.N. and C.B. supervised different parts of the work. F.S. and Q.J. wrote the paper, all authors commented on the manuscript.

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Methods

Monolithic tandem fabrication

SHJ bottom cells were fabricated using n-type double side textured silicon float-zone wafers with a resistivity of 1-5 Ω and a thickness of 260 μ m. Intrinsic and doped hydrogenated amorphous silicon layers were deposited on both sides of the wafer in a plasma-enhanced chemical vapor deposition (PECVD) reactor to passivate the silicon surface and to create the carrier-selective contacts. The SHJ bottom cells were produced by depositing the n-type contact at the front and the p-type contact at the back. The back electrode consisted of a sputtered ITO/Ag stack. The front side n-type amorphous silicon layer was then capped by the recombination junction, which consisted of either an ITO layer (50 nm; 190 Ω /sq) or an n-type/p-type nc-Si:H layer stack. The former was sputtered, while the latter was deposited in the same PECVD reactor as the amorphous silicon layers (< 200 °C). The processed 4-inch wafers were then laser cut to 2.5 x 2.5 cm substrates.

Perovskite solar cells were fabricated in the *p-i-n* configuration and deposited directly onto the ITO or nc-Si:H recombination junctions as follows. First, 12 nm of 2,2',7,7'-tetra(N,N-ditolyl)amino-9,9-spiro-bifluorene (spiro-TTB, >99 %, Lumtec) was thermally evaporated to form the hole transport layer in an home-made evaporation system (base pressure of $< 2 \cdot 10^{-6}$ mbar, working pressure of $< 3 \cdot 10^{-6}$ mbar, evaporation rate of 0.2 Å/s measured by a quartz crystal monitor, aluminum oxide crucible, power applied of about 40 W, reached with a ramp up of 10 W/min, substrate holder at room temperature). The perovskite absorber was then deposited using a sequential two-step method. First, PbI₂ (>99.99%, beads from Alfa Aesar) was co-evaporated with CsBr (>99.99%, Abcr) in a Lesker mini-Spectros system (base pressure of $< 2 \cdot 10^{-6}$ mbar, working pressure of $< 5 \cdot 10^{-6}$ mbar, evaporation rate of 1 Å/s for PbI₂ and 0.1 Å/s for CsBr as-measured by quartz crystal monitors, aluminum oxide crucibles.

evaporation temperatures of ~325°C for PbI₂, reached with a ramp up of 20°C/min, and of ~455°C for CsBr, reached with a ramp up of 20°C/min, substrate holder at 30°C).

Subsequently, a mixture of formamidinium bromide and iodide (in 1 to 3 molar ratio, 0.513 M in ethanol, Greatcell solar) was spin-coated onto this template layer. This step was performed using dynamic solution dispensing at a spin speed of 4000 rpm during 30 s in an inert atmosphere. The layers were then annealed at 150°C for 30 min in ambient air to crystallize the perovskite absorber. An electron-selective stack of LiF (1 nm, 99.98%, Sigma-Aldrich)/C₆₀ (15 nm, >99.95%, NanoC) was then thermally evaporated in an home-made evaporation system without breaking the vacuum in between these two depositions (base pressure of $< 2 \cdot 10^{-6}$ mbar, working pressure of $> 3 \cdot 10^{-6}$ mbar, evaporation rate of 0.1 Å/s for LiF and 0.3 Å/s for C₆₀ as-measured by quartz crystal monitors, aluminum oxide crucibles, power applied to the sources of ~180 W for LiF, reached with a ramp up of 40 W/min, and ~80 W for C₆₀, reached with a ramp of 20 W/min, substrate holder at room temperature). A buffer layer of 10 nm of SnO₂ was deposited by atomic layer deposition using an Oxford instrument system at 100°C using tetrakis(dimethylamino)tin and H₂O as precursors. 110 nm of indium zinc oxide (IZO) was then sputtered in an home-built system using a 4-inch target $(90\% \text{ In}_2\text{O}_3 + 10\% \text{ ZnO})$ with an RF power of 70 W (sheet resistance of 27 Ω/sq , which was measured using a 4-point probe HMS-5000 Hall setup in the Van der Pauw configuration). 120 nm of Ag was thermally evaporated at a rate of 2 Å/s through a shadow mask to form the front metal grid. Finally, an antireflective coating of MgF₂ (100 nm, > 99.99%, Umicore) was thermally evaporated at a working pressure of $< 1.10^{-5}$ mbar (base pressure of $< 1.10^{-5}$ mbar) at a rate of 2 Å/s as-measured by a quartz crystal monitor. During all layer depositions by thermal evaporation, the deposition rate was first stabilized to the targeted value before opening the substrate shutter. All thicknesses in this section are given for KOH-textured crystalline silicon substrates unless stated otherwise. These are determined by dividing the thicknesses deposited on flat glass witness samples by 1.7.

The final tandem cell active area, defined by the size of the IZO front electrode deposited through a shadow mask, was 1.81 cm², while the aperture area defined by the inside part of the metal frame was 1.42 cm².

Device characterization:

Total reflectance spectra were recorded using a UV-VIS-NIR spectrophotometer (PerkinElmer Lambda 950 equipped with an integrating sphere). In-house J-V measurements were obtained on a temperature-controlled vacuum chuck at 25°C, if not stated otherwise, using a two-lamp (halogen and xenon) class AAA WACOM sun simulator with an AM1.5G irradiance spectrum at 1000 W/m². Independently certified SHJ cells were used to calibrate our in-house solar simulator. Shadow masks were used to define the illuminated area (here 1.42 cm², determined by CalLab, Fraunhofer ISE). The cells were measured with a scan rate of 100 mV/s (using an integration time of 0.1 s and a delay of 0.1 s for each data point, the voltage step was 0.02 V). Three-point weight maximum power point measurements were performed using an in-house LabVIEW code. 49 J-V measurements were taken after the cell had undergone maximum power point tracking for at least 500 s. This was done to ensure that the cell reached a steady-state efficiency before J-V characterization. EQE spectra were measured with a custom-made spectral response setup where the samples are irradiated with chopped light at a frequency of 232 Hz and the response measured with a lock-in amplifier. For tandem cells, blue and red light biases were used to saturate the complementary subcell. To measure each subcell near short-circuit conditions, 0.7 V and 1 V bias voltages were applied to the cell when measuring the top and bottom cell, respectively. Both EQE and J-V measurements were carried out in air without any encapsulation if not stated otherwise. Secondary electron SEM images were acquired with acceleration voltages ranging from 1 to 5 kV using either an Everhart-Thornley or an in-lens detector (JEOL JSM-7500TFE or Zeiss NVision 40 microscopes). Cross sections of the fully textured perovskite/SHJ tandem were

prepared for SEM or TEM analysis using a focused ion beam workstation (Zeiss NVision 40). Thin TEM lamellae were quickly transferred in air (< 2 min) to a transmission electron microscope, either a FEI Tecnai Osiris or a double Cs corrected FEI TITAN Themis. STEM imaging using a high-angle annular dark-field detector was combined with energy-dispersive X-ray spectroscopy at an acceleration voltage of 200 kV and a probe current of either 100 or 200 pA. More details about the sample preparation, the TEM analysis and potential artefacts associated with the technique can be found in Ref.³⁶ XRD measurements were carried out in an Empyrean diffractometer (Panalytical) equipped with a PIXcel-1D detector. The diffraction patterns were measured using a Cu Kα radiation (wavelength of 1.54 Å).

Stability tests

The unencapsulated device was measured at 25°C in ambient air (20-30% RH) using a two-lamp (halogen and xenon) class AAA WACOM sun simulator with an AM1.5G irradiance spectrum at 1000 W/m². Maximum-power point tracking measurements were performed for 61 hours using an in-house LabVIEW code.²4 *J-V* measurements were recorded approximatively every 10 hours following the same procedure explained above. The cells were kept under illumination between MPP and *J-V* measurements.

A tandem cell was encapsulated between two 6 cm x 6 cm x 2 mm glass cover sheets. At the edges, a black butyl rubber edge sealant was applied. To prevent any inflow of the butyl edge sealant at elevated temperature, a frame of white silicone cord was placed on the cell side of the sealant. Ribbons with a conductive adhesive were placed on the front and rear side of the tandem cell to contact it. The encapsulated cell was light-soaked at ambient atmosphere (20-30% RH) using a Solaronix degradation system at 0.7 sun. During this measurement, the tandem cell was placed on a metal carrier plate with vacuum holes, kept at a controlled temperature of 30°C. The cell was kept at its MPP using an in-house-written algorithm uploaded to an Arduino microcontroller. *J-V* reverse and forward scans were initially recorded

every 5 minutes and, after 30 hours of light soaking, every 30 minutes. The cell was kept under constant illumination.

Data and code availability

The experimental data and maximum power point tracking codes (LabVIEW and Arduino microcontroller) that support the findings of this study are available from the corresponding authors upon reasonable request.

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