

High-quality Synthetic 2D Transition Metal Dichalcogenide Semiconductors

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Abstract—We give here an overview on our results on the large-area growth of 2D transition metal dichalcogenide semiconductors MoS₂, MoSe₂, WSe₂ using chemical vapor deposition. The growth of MoS₂ on sapphire occurs epitaxially with the crystalline orientation of the MoS₂ film closely matching that of the sapphire substrate, resulting in a high-quality continuous film. The use of H₂S results in more control over growth morphologies. WSe₂ and MoSe₂ have also been successfully grown using solid-state precursors. Room-temperature mobilities of all these materials exceed 10 cm²V⁻¹s⁻¹. In contrast to MoS₂, WSe₂ and MoSe₂ show ambipolar behavior.

Keywords: TMDCs, large-area growth, field-effect transistors

I. INTRODUCTION

The resurgence of semiconducting transition metal dichalcogenides in the context of few-layer films for mesoscopic devices [1,2] has motivated the development of scalable synthesis approaches which are necessary in order to achieve controllable thickness, size and uniformity of the 2D film. Chemical vapor deposition (CVD) is probably the most promising method for achieving this. The quality of the grown materials critically depends on the purity of the precursors but also the crystallinity of the substrate. We review here our recent work on this subject and present results which show that high-quality MoS₂, WSe₂ and MoSe₂ with large grain sizes as well as optical and electronic quality approaching that of the material exfoliated from bulk crystals can be achieved.

II. SYNTHESIS OF MoS₂ USING SOLID-STATE PRECURSORS

We have used highly polished sapphire substrates to achieve control over lattice orientation during CVD growth of monolayer MoS₂, Fig. 1. [3] Even though MoS₂ and sapphire interact only via the relatively weak van der Waals interaction, commensurability of the sapphire lattice with MoS₂ allows the van der Waals interaction to control the lattice orientation of MoS₂ in a similar way as the lattice matching requirement in covalent semiconducting materials. The growth process is based on the gas-phase reaction between MoO₃ and sulphur [4,5] evaporated from solid sources using ultrahigh-purity argon as the carrier gas.

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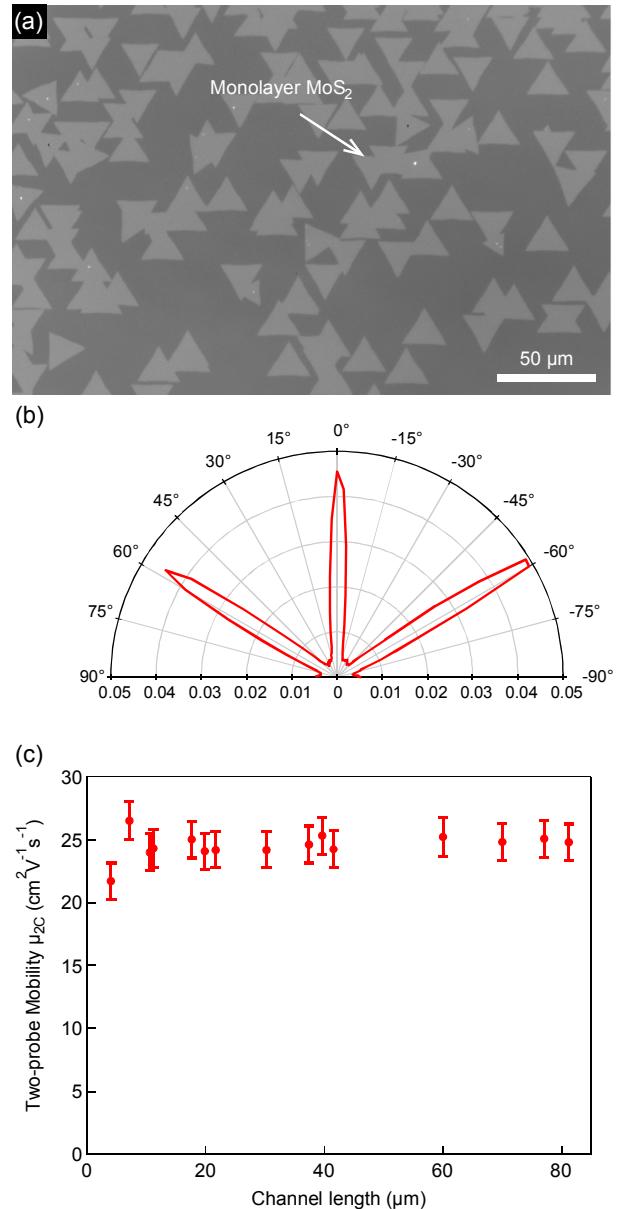


Fig. 1. (a) Optical image of triangular single-crystals of monolayer MoS₂ grown on sapphire. (b) Polar plot of the orientation distribution of single-crystal MoS₂. (c) Two-probe effective field-effect mobility of a polycrystalline film as a function of channel length.

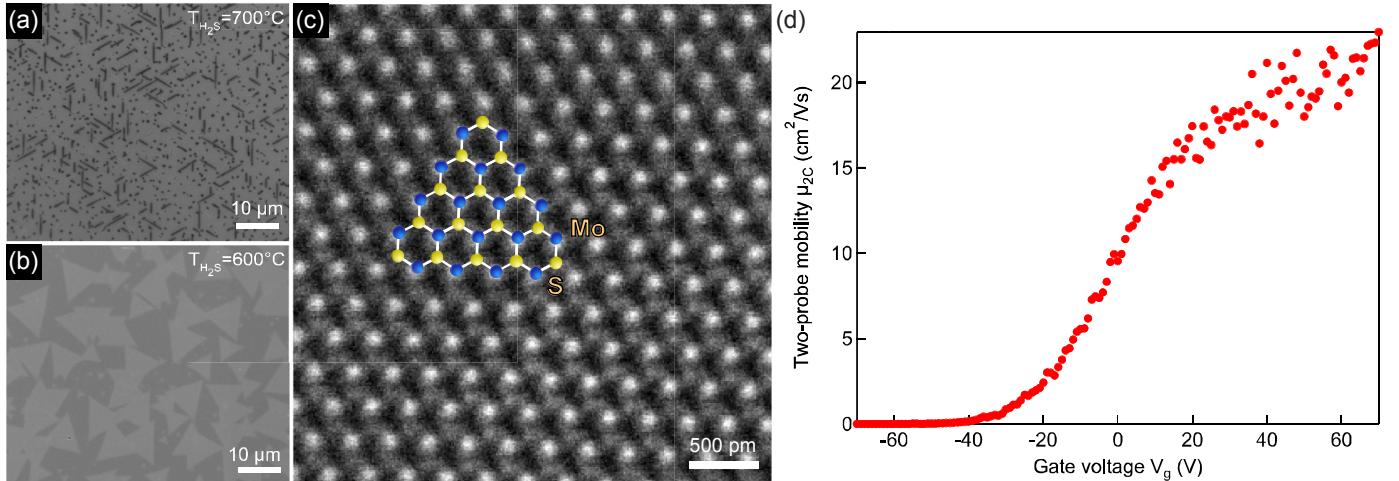


Fig. 2. (a) Growth of vertically-aligned MoS₂ domains with H₂S/H₂ mixture being introduced as the sample temperature reaches 700°C, for different H₂S and H₂ flow rate of 15:5 sccm. (b) Decreasing the injection temperature of the H₂S/H₂ mixture to 600°C leads to a reduction of longitudinal length of MoS₂ domains, while the growth morphology changes from vertically-aligned multilayer to horizontally-aligned monolayer. (c) STEM image of single-layer MoS₂ lattice with structural model overlaid, showing high-crystallinity of the grown material. (d) Two-probe field effect mobility measured on back-gated field-effect transistors based on CVD-MoS₂.

The growth procedure results in characteristic single-crystal domains in the shape of well-defined equilateral triangles that can (depending on growth time) merge into a continuous monolayer film covering a typical area of 6 mm x 1 cm in the middle portion of the growth substrate. The majority (91.5% of islands shown on Fig. 1(a)) of single-crystal domains are well aligned with the relative orientation of edges that can be expressed as multiples of 60°. This is confirmed by polar plot of orientations presented in Fig. 1(b), showing that the dominant edge orientations are 0° and ±60°. A small fraction of domains (6%) show edges with a relative angle of ±30°, while the rest (2.5%) show other orientations.

Optical characterization (photoluminescence and Raman spectroscopy) confirm the high quality of our films. Electrical transport measurements on single grains show the mobility reach a value of 43 cm²/Vs. Good stitching of such high-quality single crystals into continuous films is necessary in order to realize large-area MoS₂ with the same quality as that of the single crystal. We have examined the electrical properties of polycrystalline films by performing measurements on a section of a continuous film with the length $L = 81.2 \mu\text{m}$ and width $W = 36.6 \mu\text{m}$, contacted by multiple electrodes. The effective field-effect mobility as a function of channel length, Fig. 1(c) extracted from two-contact measurements in various lead configurations is shown on Fig. 1c. In stark contrast to previous studies by Najmaei et al. [6], where a 50% degradation in mobility was seen as the channel length was increased from 3 μm to 30 μm, we see no degradation of the mobility with increasing channel length, with the longest ~80 μm long segment showing the same mobility as the shortest (~4 μm long), within the measurement error. We estimate that there are ~16 junctions between single crystals along the length of the 82 μm long channel. This shows that the superior electrical properties of the single crystals are preserved over length scales larger than those corresponding to single grains.

III. SYNTHESIS OF MoS₂ USING H₂S AS A SULPHUR SOURCE

We have continued our work towards improving CVD growth of semiconducting TMDCs by replacing solid-state precursors with gas-phase sources which are easier to control. In this respect sulphur can be easily replaced with H₂S. The growth procedure results in highly-oriented vertically-aligned multilayers or horizontally-aligned monolayer domains in the shape of well-defined equilateral triangles (Fig. 2) [7]. At a high H₂S/H₂ injection temperature of 700 °C with different H₂S:H₂ ratios (5:15, 10:10 and 15:5 sccm), vertically-aligned multilayer MoS₂ domains with lengths of up to 10 μm have been observed. The lateral length of MoS₂ domains decreased in size to 4 μm as the temperature at which the H₂S/H₂ mixture is first introduced is lowered to 625 °C. When this temperature is further reduced to 605 °C, together with vertical growth, monolayer MoS₂ domains start to grow in a lateral direction on the surface of the sapphire substrate. For an even lower injection temperature of 600 °C, the growth is dominated by monolayer MoS₂ triangles. In general, we observe that an earlier introduction of the H₂S/H₂ mixture favors horizontal over vertical growth. Moreover, by varying the ratio of H₂S/H₂ mixture, the morphology and domain size of monolayer MoS₂ flakes is changed. Under more sulphidizing conditions (H₂S:H₂ = 15:5), MoS₂ domains adopt a triangular shape.

Fig. 2(c) presents the lattice of MoS₂, imaged in this way and with a negligible beam-induced damage. Here, brighter spots correspond to Mo atoms and darker spots to stacks of two sulfur atoms. In general, contrast in STEM mode under these conditions of monolayer imaging is considered to obey the $Z^{1.7}$ contrast relation, which in the absence of contamination allows distinguishing between the atoms with different Z number or between columns with different occupancy. Extracted from Fourier-transformed STEM images and used to draw the overlaid structural model, the lattice

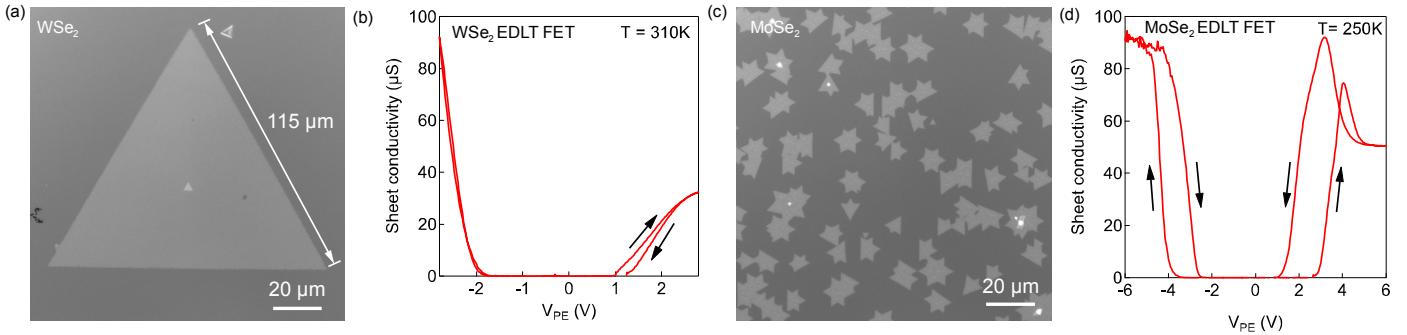


Fig. 3. (a) Optical image of monolayer WSe₂ single crystal grown using CVD. (b) Transfer characteristic of a CVD-WSe₂ electric double-layer field-effect transistors using ionic liquid gating. The device shows ambipolar behavior and large hole conductivity. (c) Optical image of monolayer MoSe₂ crystals grown using CVD. (d) Transfer characteristic of a CVD-MoSe₂ electric double-layer field-effect transistors using ionic liquid gating.

constant of $\sim 3.18\text{\AA}$ is in good agreement with the literature values [8].

After characterization of structural and optical properties of our material, we moved to device fabrication and electrical characterization. Material was transferred from sapphire substrates to conventional Si/SiO₂ (270 nm thickness) and contacts were defined by standard electron beam lithography (EBL). Gold with a thickness of 90 nm was used as the contact material (see Methods). Some of the devices were etched in rectangular shape using the second step of EBL followed by oxygen plasma etching.

We characterized our devices in vacuum (base pressure 10^{-6} mbar) after annealing at 140 °C for 24 h. We present the characteristics of a typical device after annealing on Fig. 2d. This device was etched in rectangular shape and was realized in a back-gated geometry using a degenerately doped Si, covered by a 270 nm thick layer of SiO₂. The two-probe mobility, which does not take into account the effect of contact resistance, presents the lower estimate of intrinsic properties of material. Values of $\mu_{2C} \sim 20 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ are in good agreement with recent reports on CVD MoS₂, grown from solid precursors on SiO₂ [9] and sapphire [3] substrates, which indicates a similar quality of material. We estimate an I_{on}/I_{off} ratio $\sim 10^6$ of this transistor at $V_s = 5\text{V}$. Overall, the device characteristics demonstrate good current injection and mobility, high I_{on}/I_{off} ratio and electron doping of the channel, typical for exfoliated or CVD material. The observed doping levels are most probably related to sulfur vacancies and other clusters of defects, as observed in our material by STEM.

IV. SYNTHESIS OF MOSE₂ AND WSE₂ USING SOLID-STATE PRECURSORS

We have also grown monolayer WSe₂ and MoSe₂ using solid state precursors in the form of corresponding oxides and selenium. Grain sizes over 100 μm have been achieved in case of WSe₂ and grains in the range of 10-50 μm in case of other TMDs. Electrical, photoluminescence and Raman characterization show properties comparable to exfoliated materials. For the purposes of electrical characterization, we

have used ionic liquid gating, in order to achieve high charge densities and efficient electron and hole injection regardless of the contact material [10]. Devices are characterized in vacuum, and close to room temperature. Both CVD-grown WSe₂ and MoSe₂ show perfect ambipolar behavior. Both materials show room-temperature electron mobilities of $\sim 10 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ and low-temperature (4K) electron mobilities of $\sim 100 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$.

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