

# **MEGAFRAME: a fully integrated, time-resolved 160×128 SPAD pixel array with microconcentrators**

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Delft University of Technology

# Outline

- Aims and scope of MEGAFRAME
- Key application: FLIM
- Architecture
- Results
- Conclusions

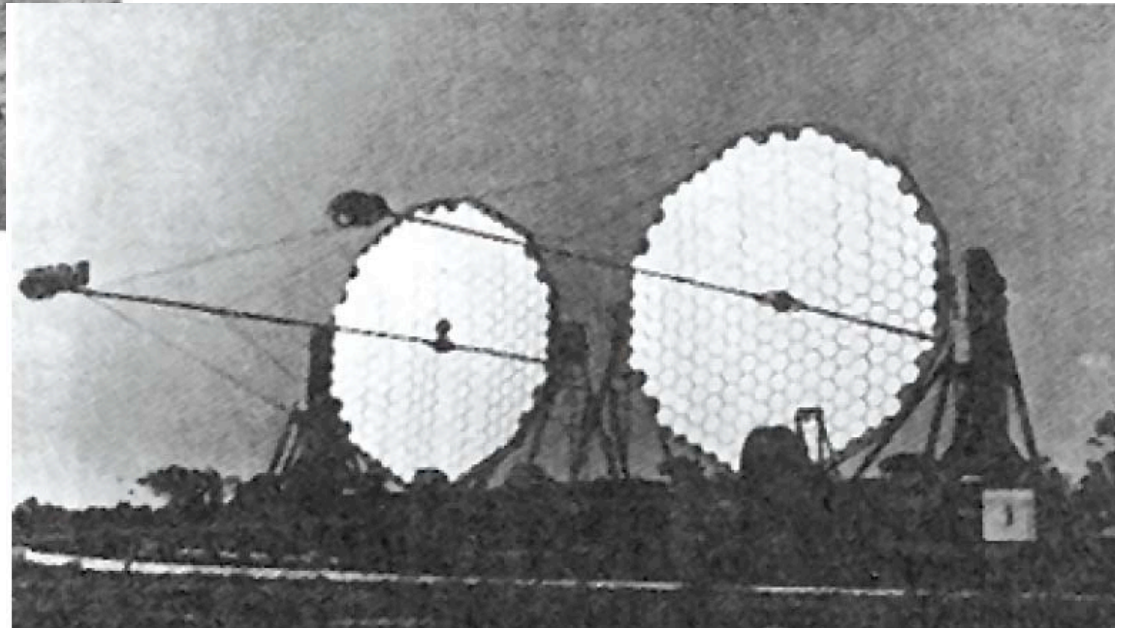
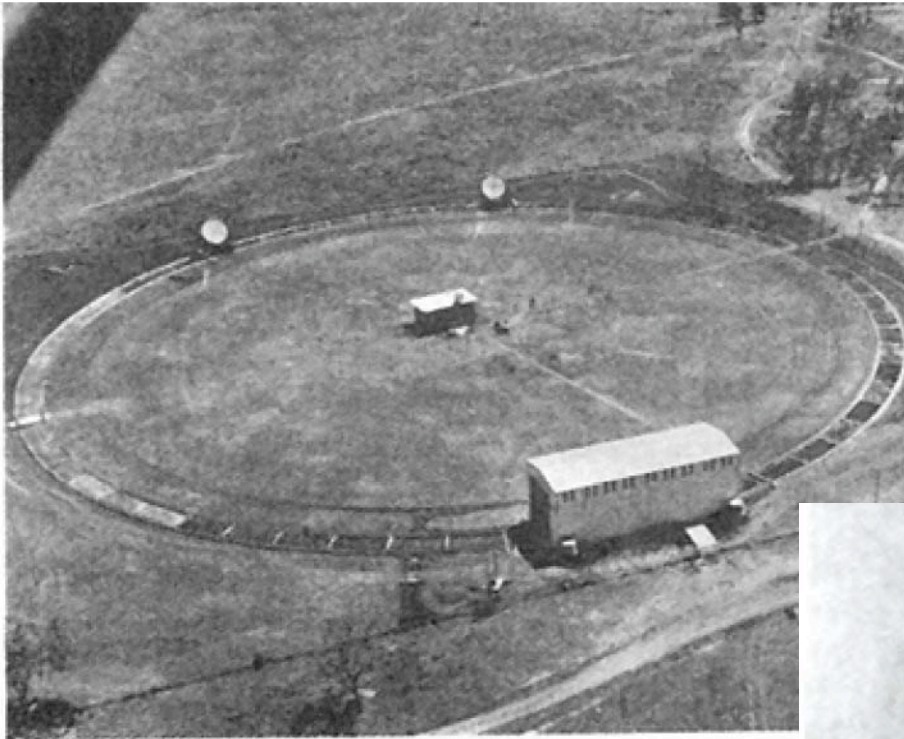
# Aims and Scope of MEGAFRAME

Fully digital,  
scalable sensor capable  
of counting single photons and of  
detecting their  
time-of-arrival for bioimaging  
applications

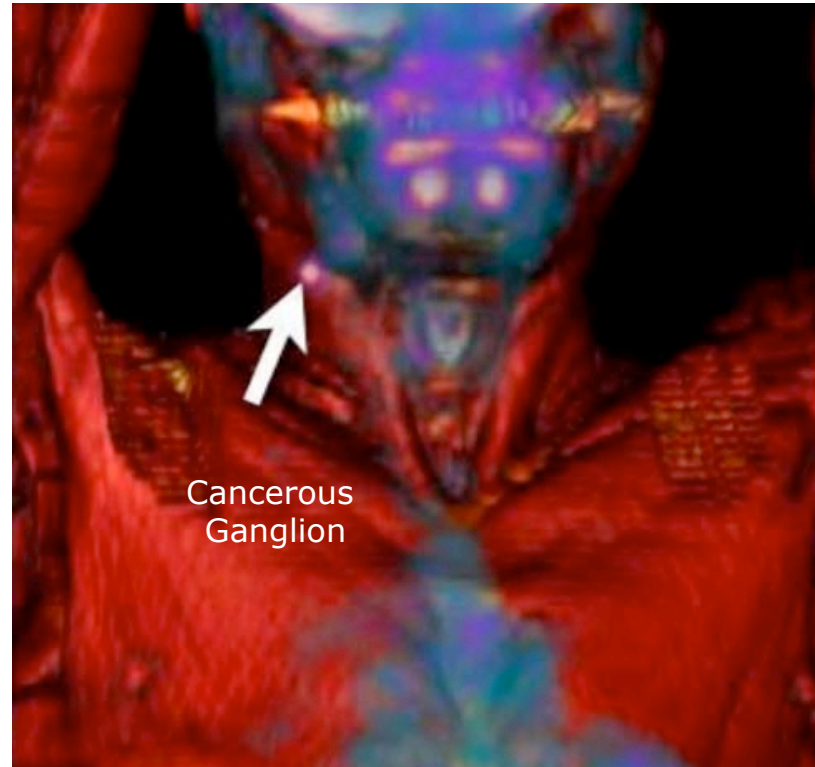
# Time-of-arrival and Time-of-flight of Photon Bursts are Useful to...

- compute distances
- reconstruct 3D scenes
- evaluate speed of moving objects
- analyze nuclear reactions
- count molecules
- analyze molecular environment
- “see” subatomic particles and high energy rays  
etc.

# Stellar Hanbury-Brown Twiss Interferometer



# Positron Emission Tomography (PET)

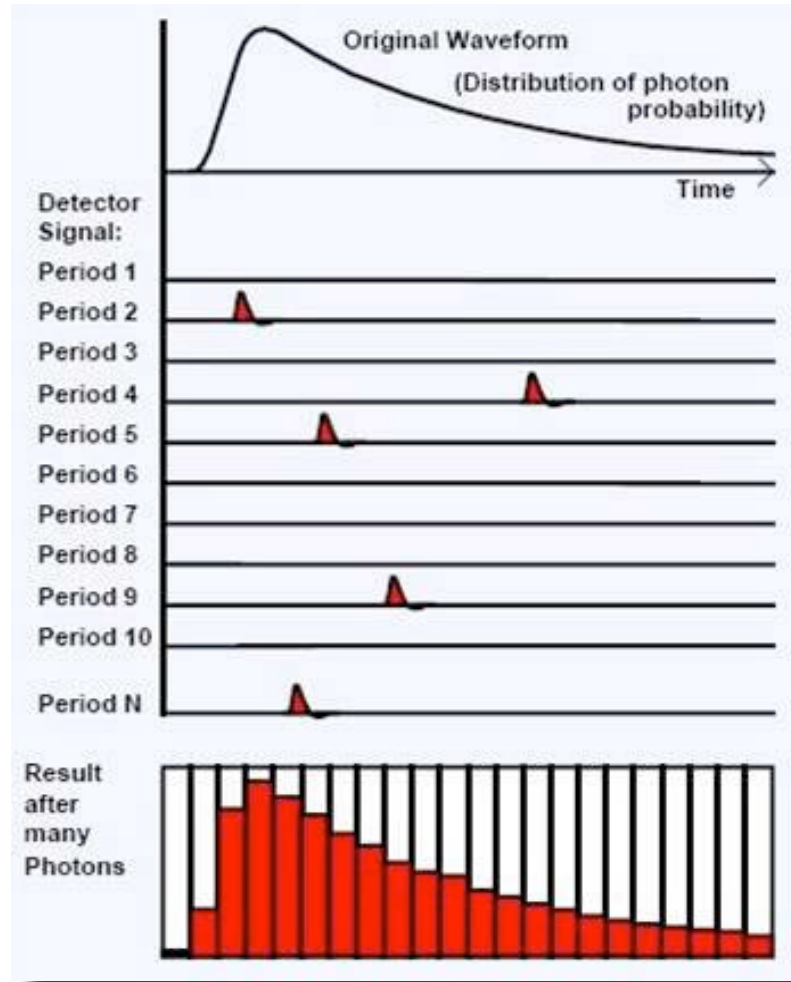


PET visualizes  $\beta^+$  emission from  $^{18}\text{F}$ FDG metabolized by cancer cells...

**in 3D!**

# Key Application: FLIM

# Goal: Time-resolved Imaging $\Rightarrow$ Fluorescence Lifetime Imaging

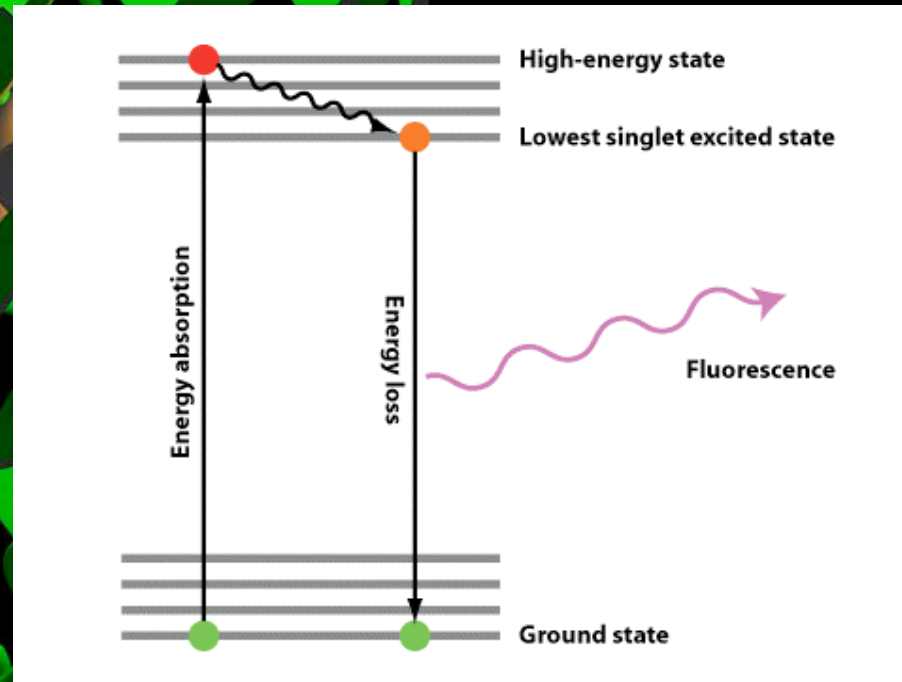
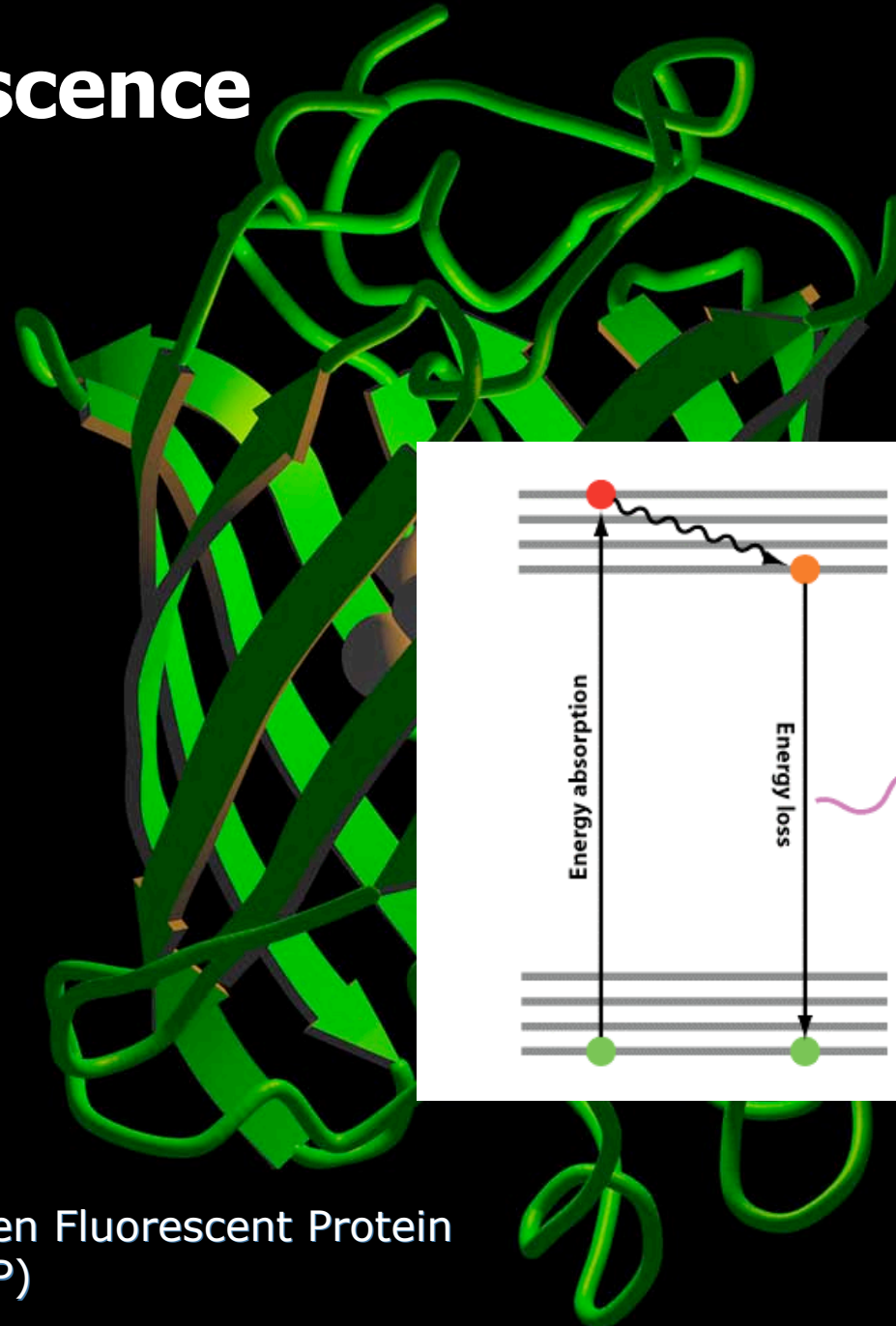


Individual single-photon time-of-arrivals

Bollinger & Thomas, 1961

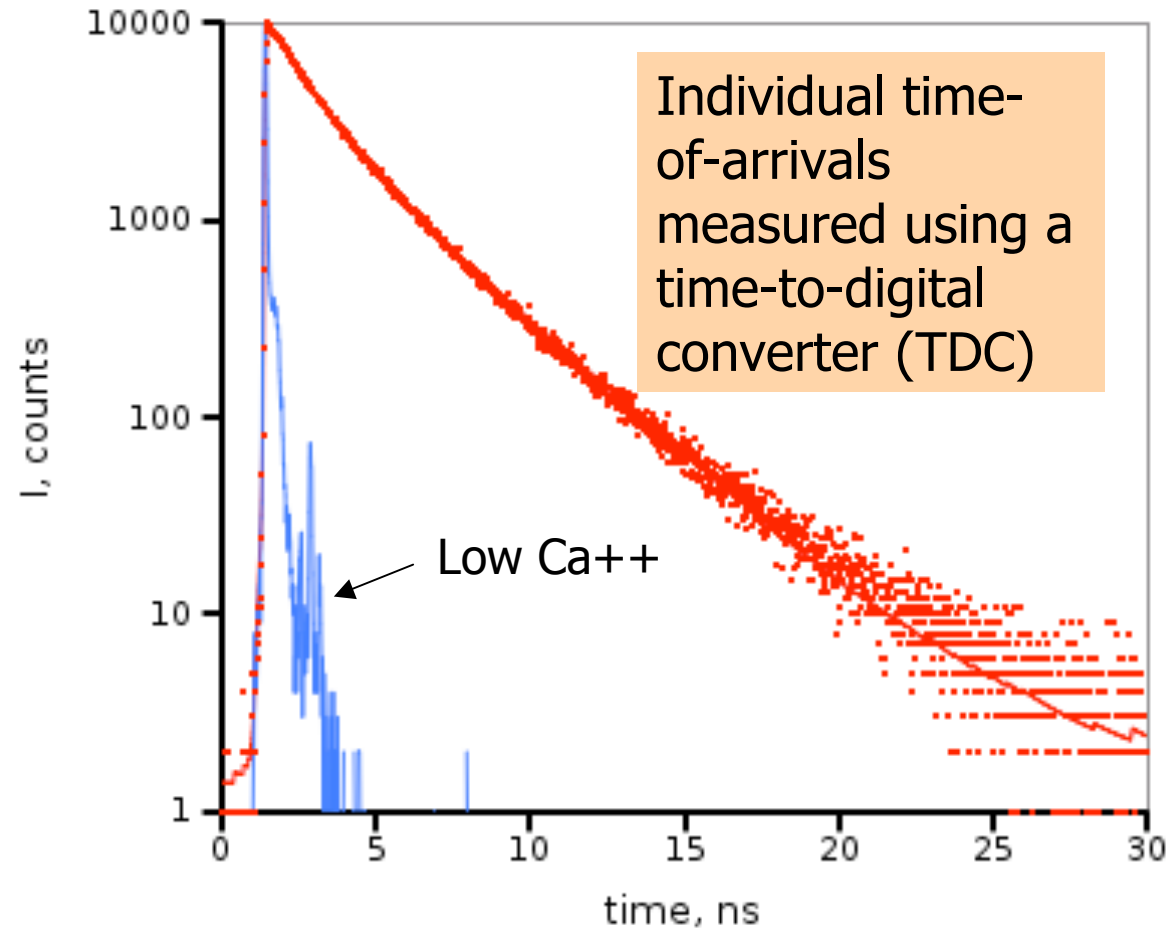


# Fluorescence



Green Fluorescent Protein  
(GFP)

# Fluorescence Lifetime vs. Environment



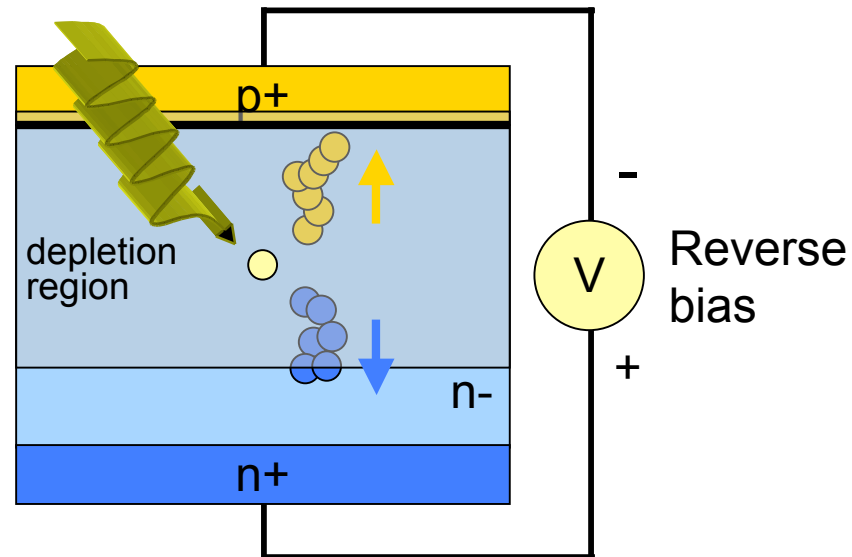
# Novelty of MEGAFRAME

- Use a **massive array** of single-photon avalanche diodes (SPADs) implemented in standard 130nm CMOS imaging process
- Use a TDC **in every pixel**
  - Eliminate scanning, gating/shuttering
  - Increase frame rate
  - Decrease exposure time, fit time
  - Move towards video-rate FLIM
- Recover fill factor losses with **microlenses**

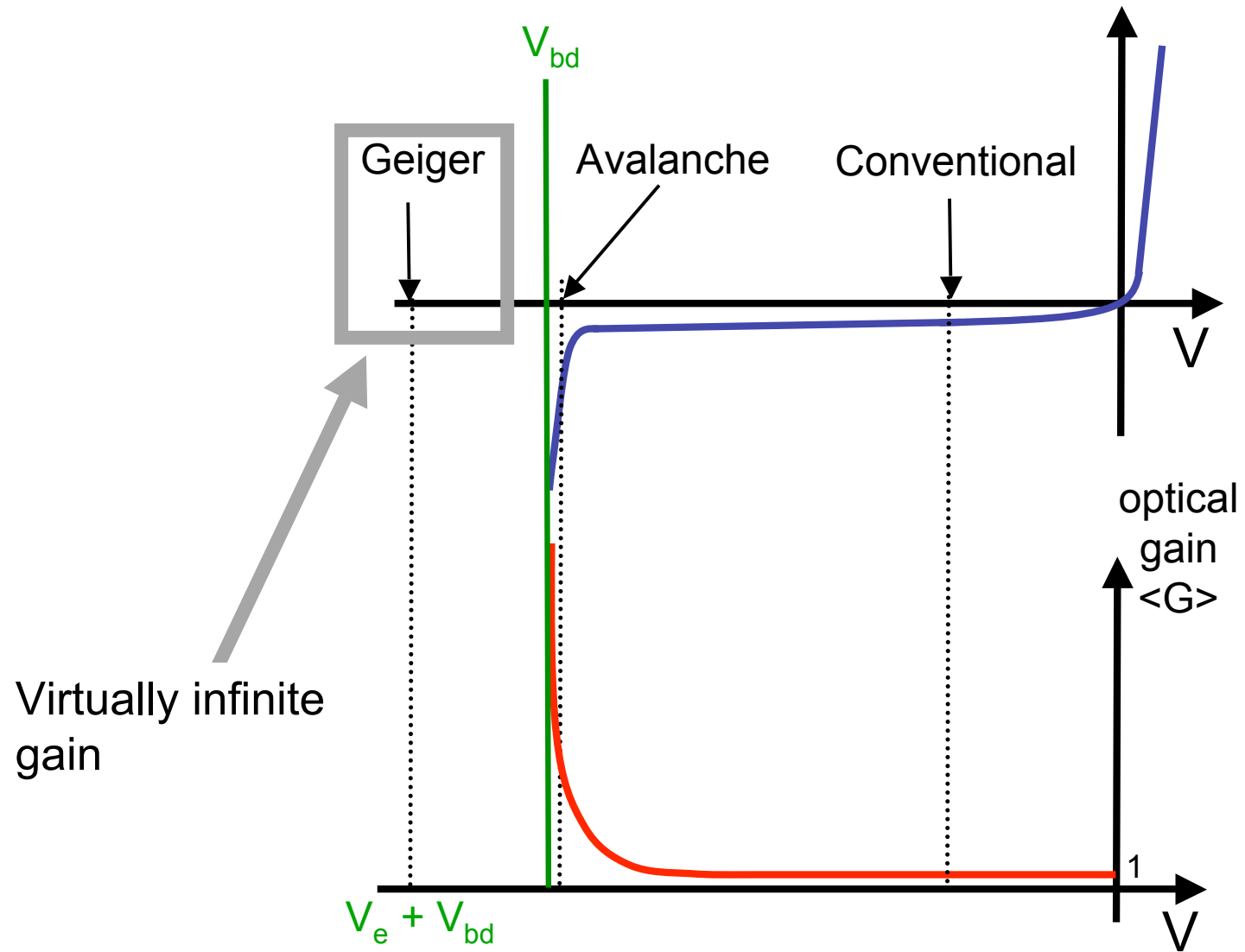
# Single-photon Avalanche Diode (SPAD)

- Review:

Photon to electron - Secondary electron - Multiplication  
Multiplication in depletion region by impact ionization



# SPAD

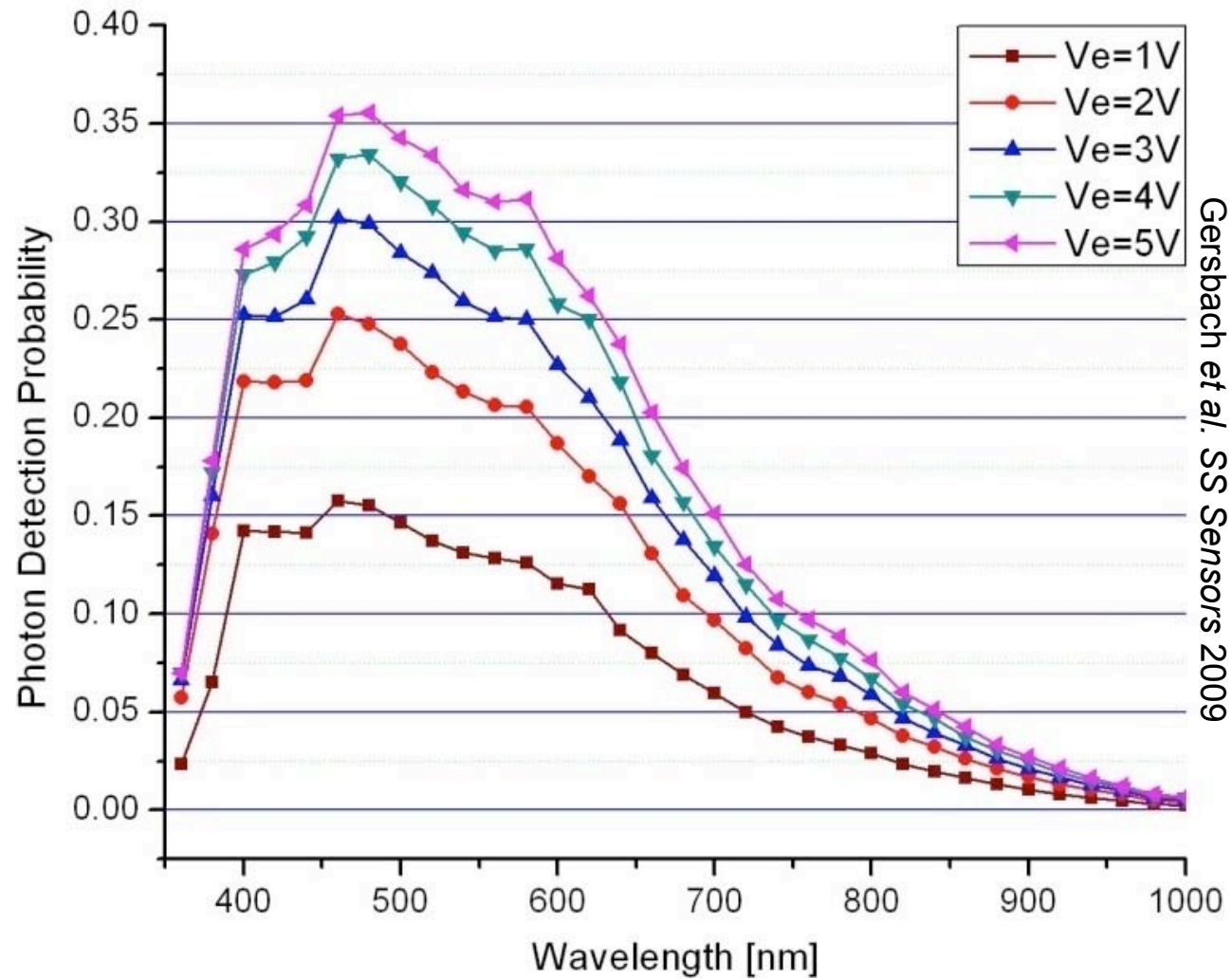




# Deep-submicron SPAD Performance

- Technology: 130nm CMOS IS
- Timing uncertainty < 125ps
- DCR (median, RT): 100Hz
- DCR (median, -60C): < 20Hz
- Dead time < 100ns
- PDP (sensitivity): 26% @ 460nm
- Range: 380-900nm

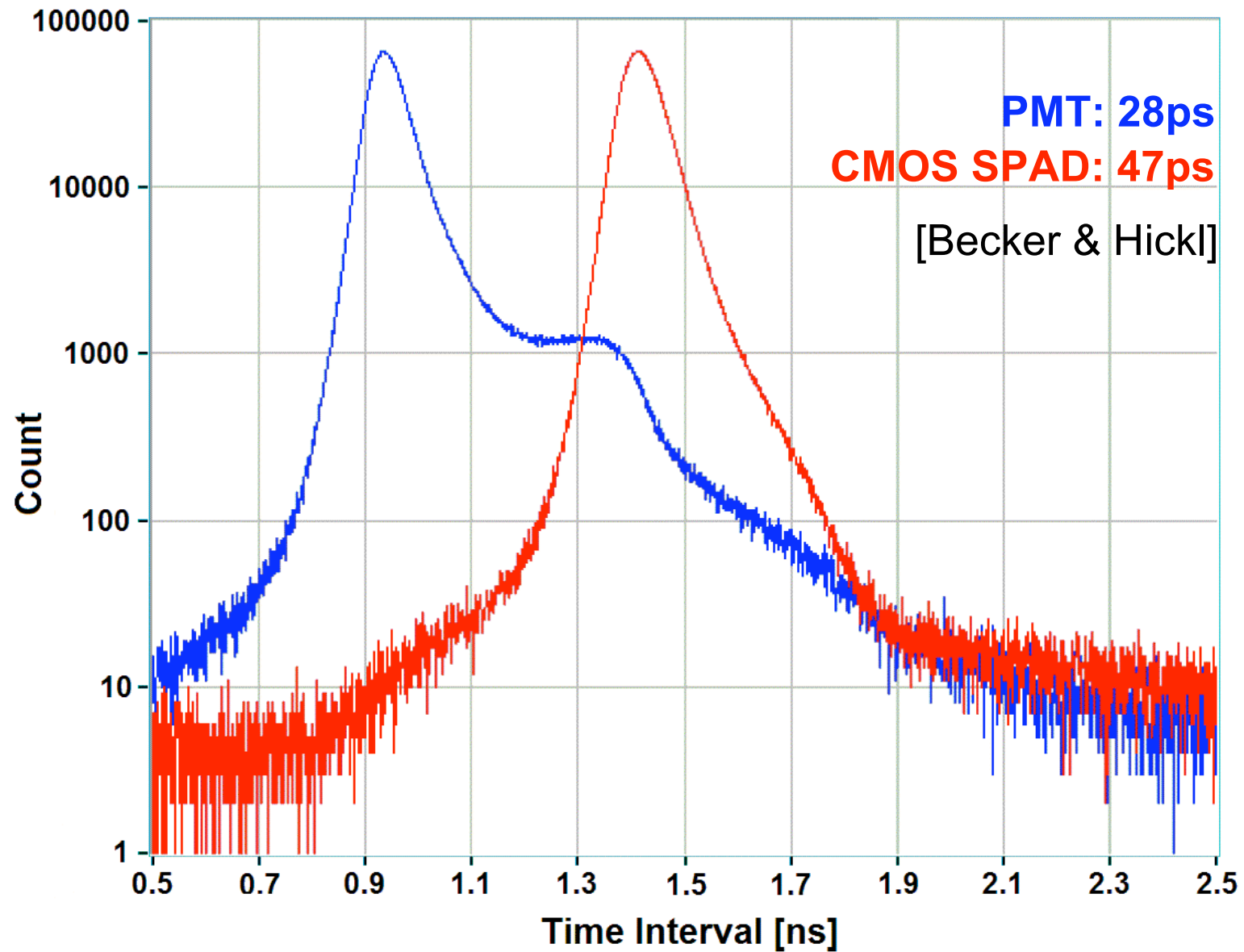
# SPAD Spectral Performance



Gersbach et al. SS Sensors 2009

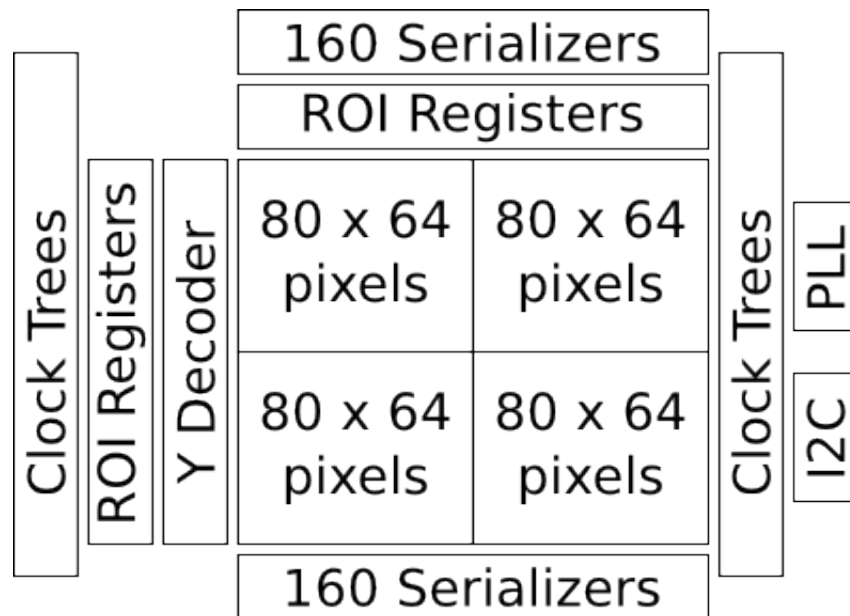


# SPAD Temporal Performance

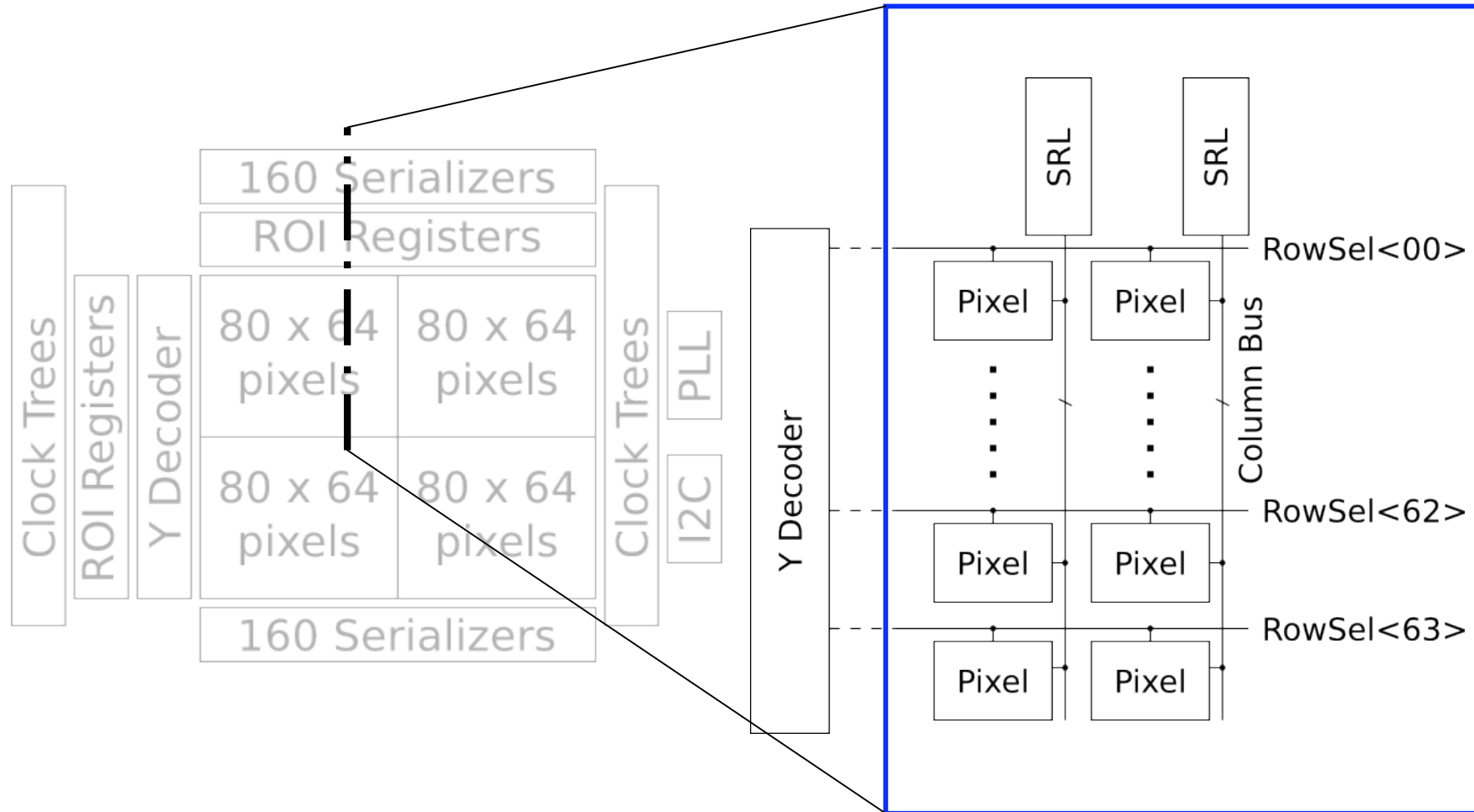


# Architecture

# Imager Block Diagram

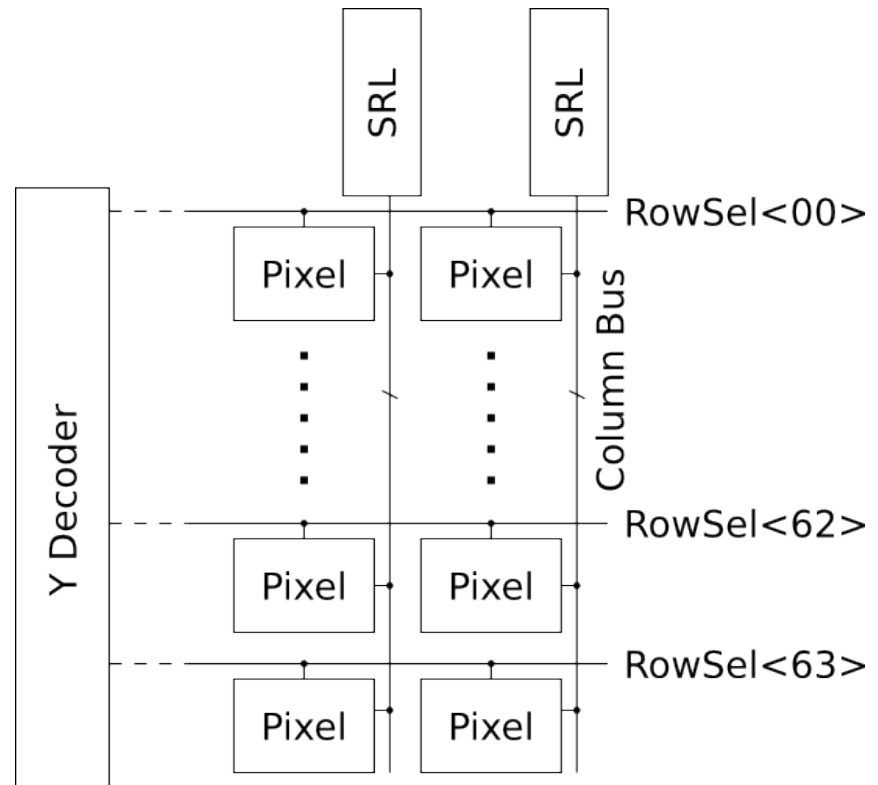


# Imager Block Diagram

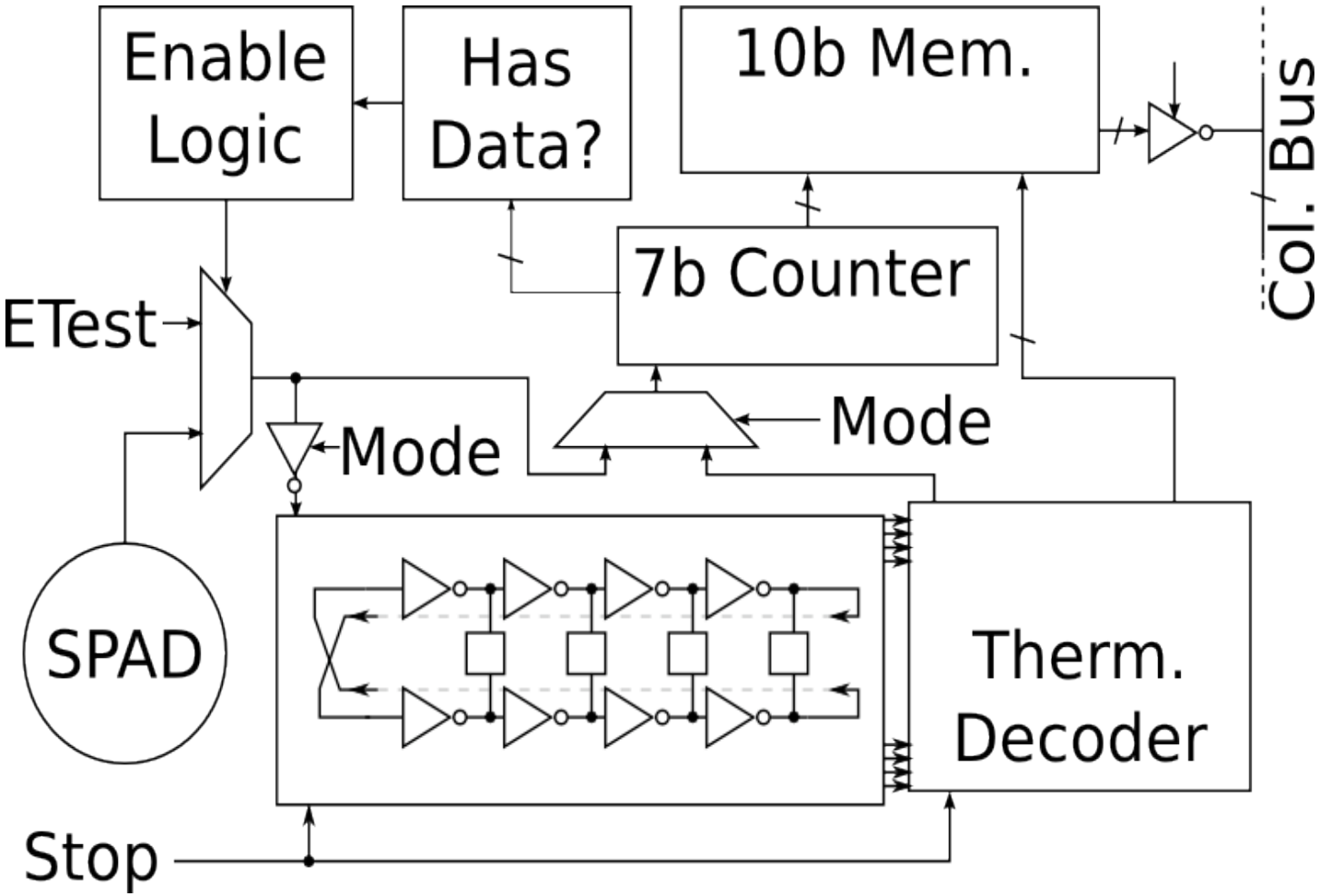


# Imager Readout

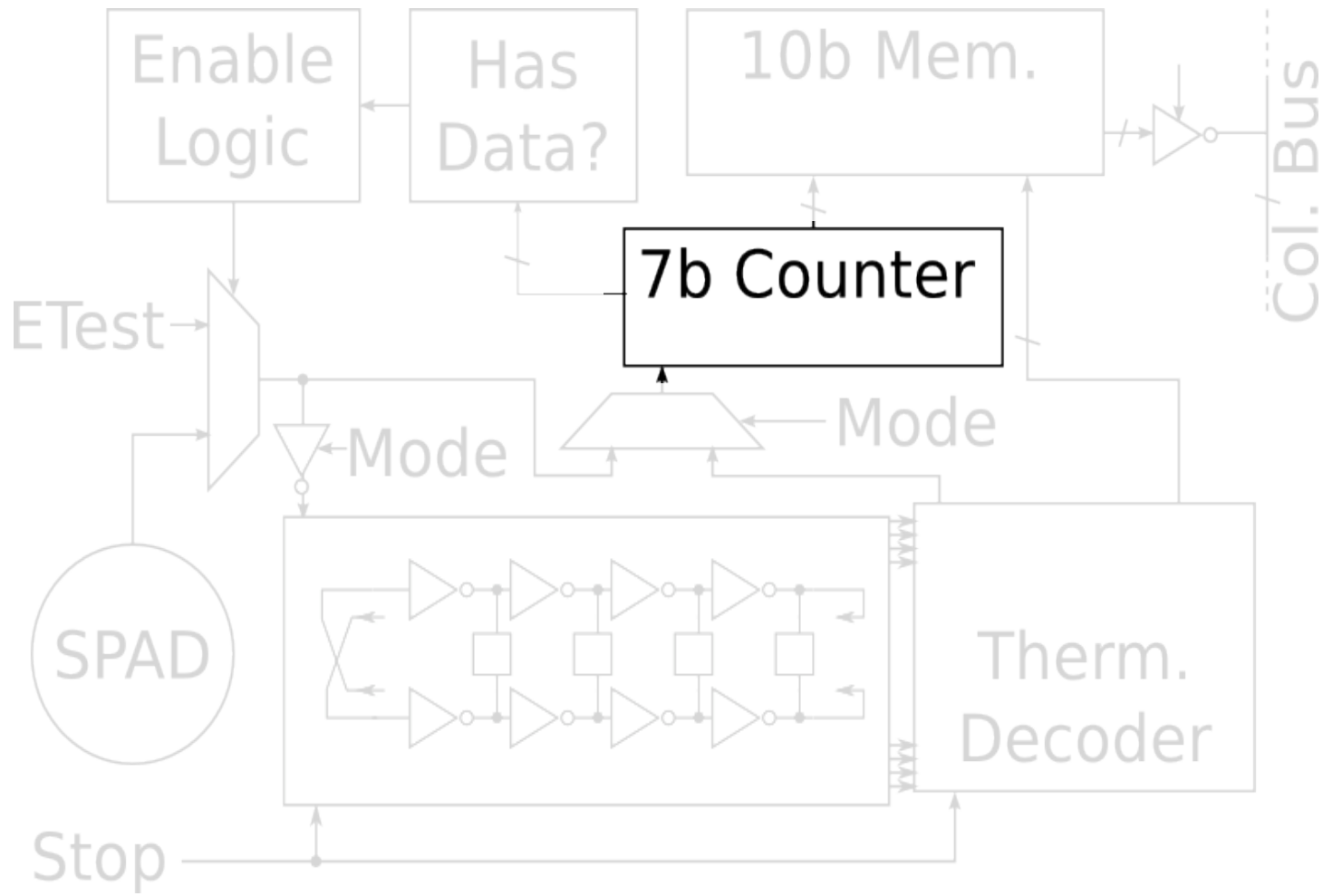
- Rolling shutter with a serializer per half-column
- Region-of-interest registers disable rows or columns to save bandwidth
- 10b on-pixel memory allows acquisition to occur while read-out takes place



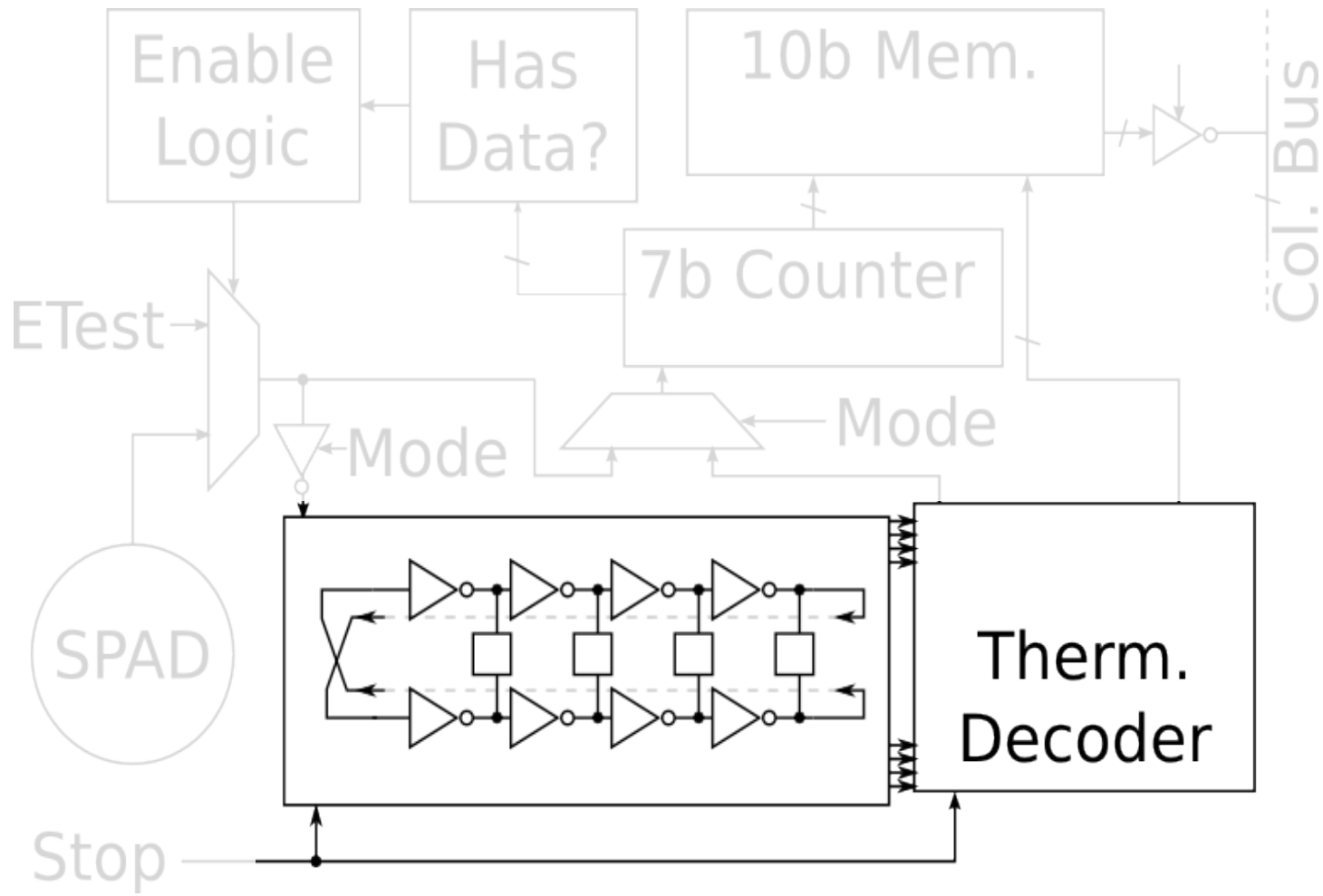
# Pixel Architecture



# Time-Uncorrelated Photon Counting



# TCSPC



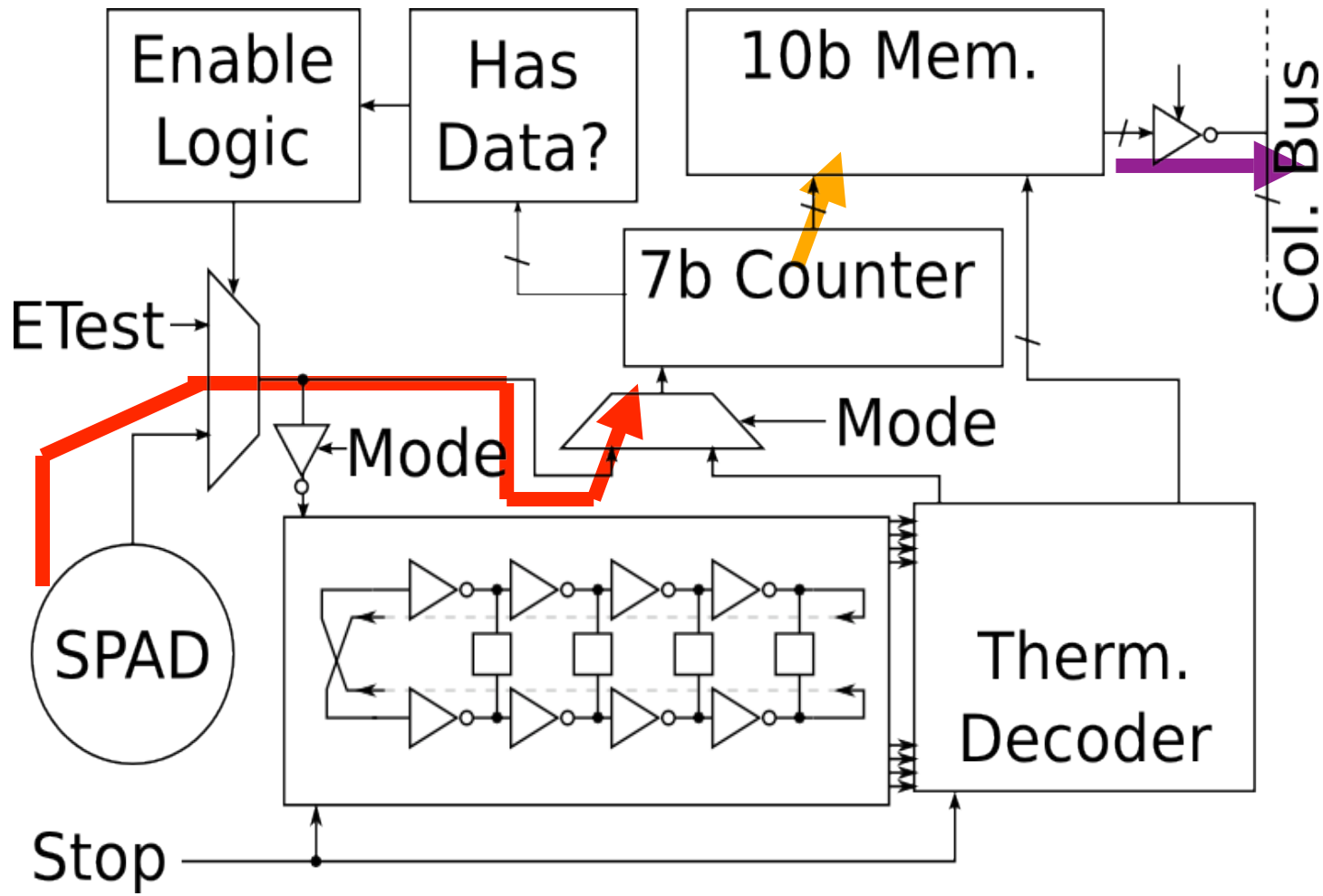




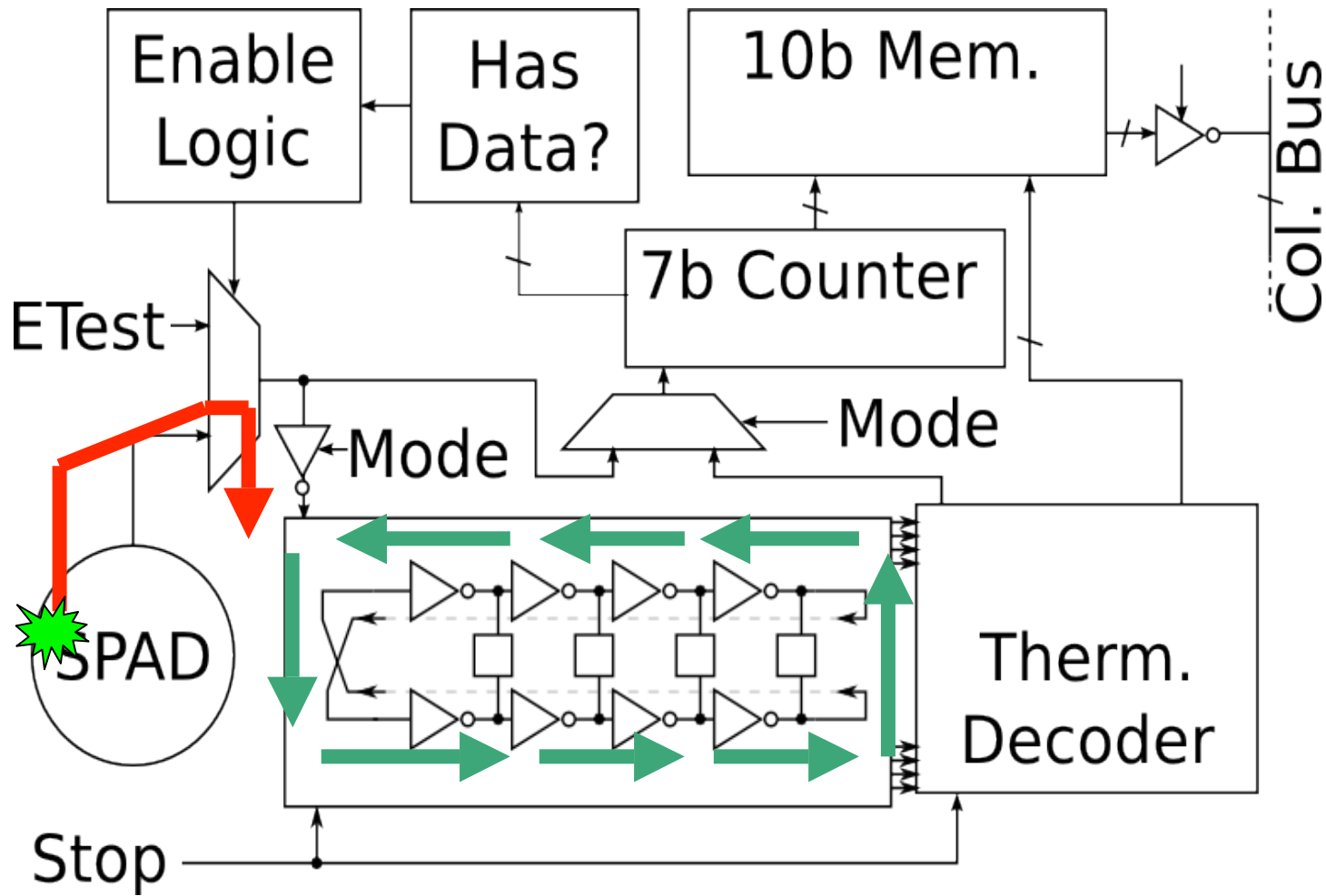
# Ring Oscillator (Cont.)

- On-chip PLL contains a copy of the ring oscillator
  - Allows PVT compensation
- Better DNL, INL performance
- Resolution: 55ps -> 75ps

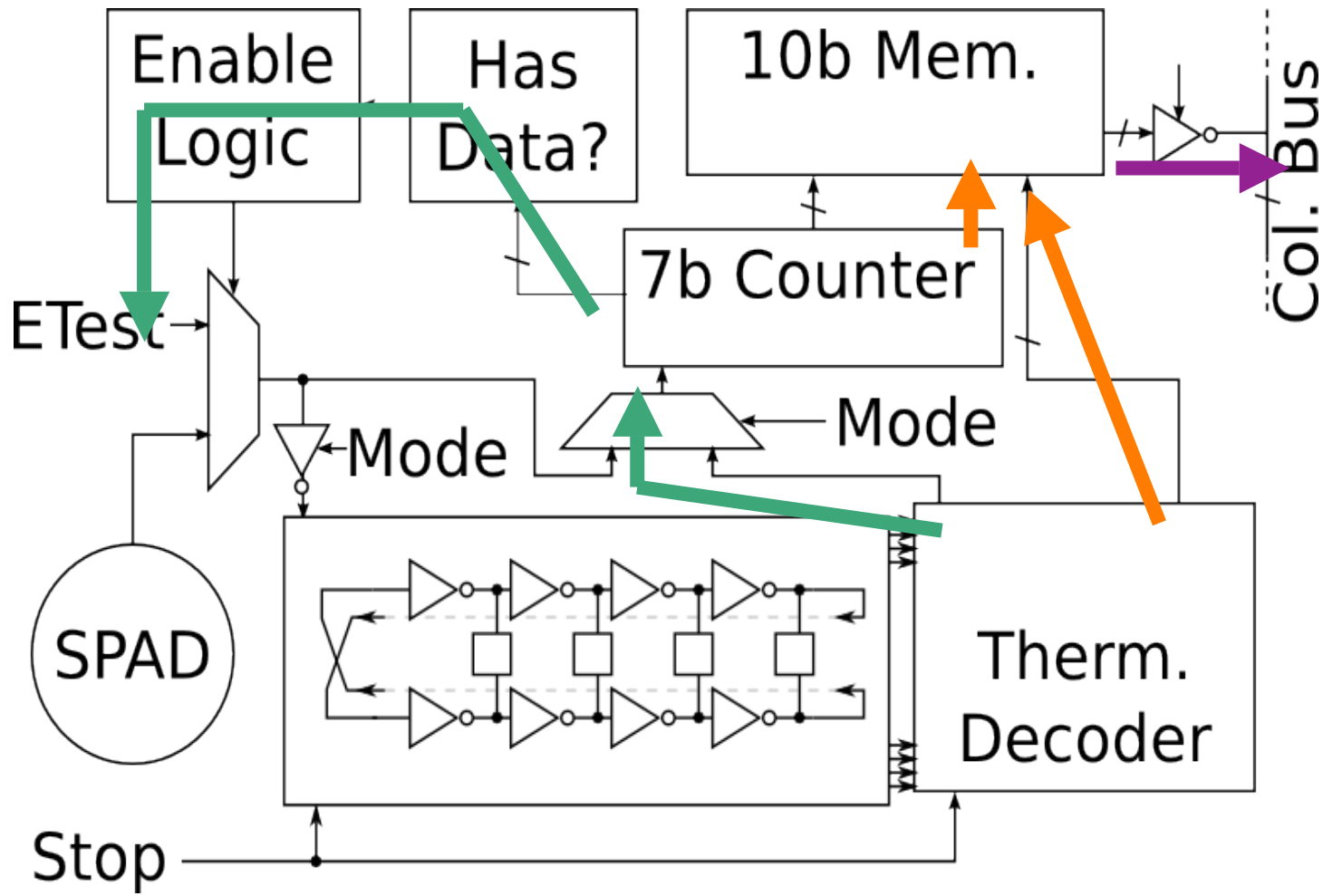
# Time-Uncorrelated Photon Counting Operation



# TCSPC Operation: Photon Arrival & Ring Oscillator

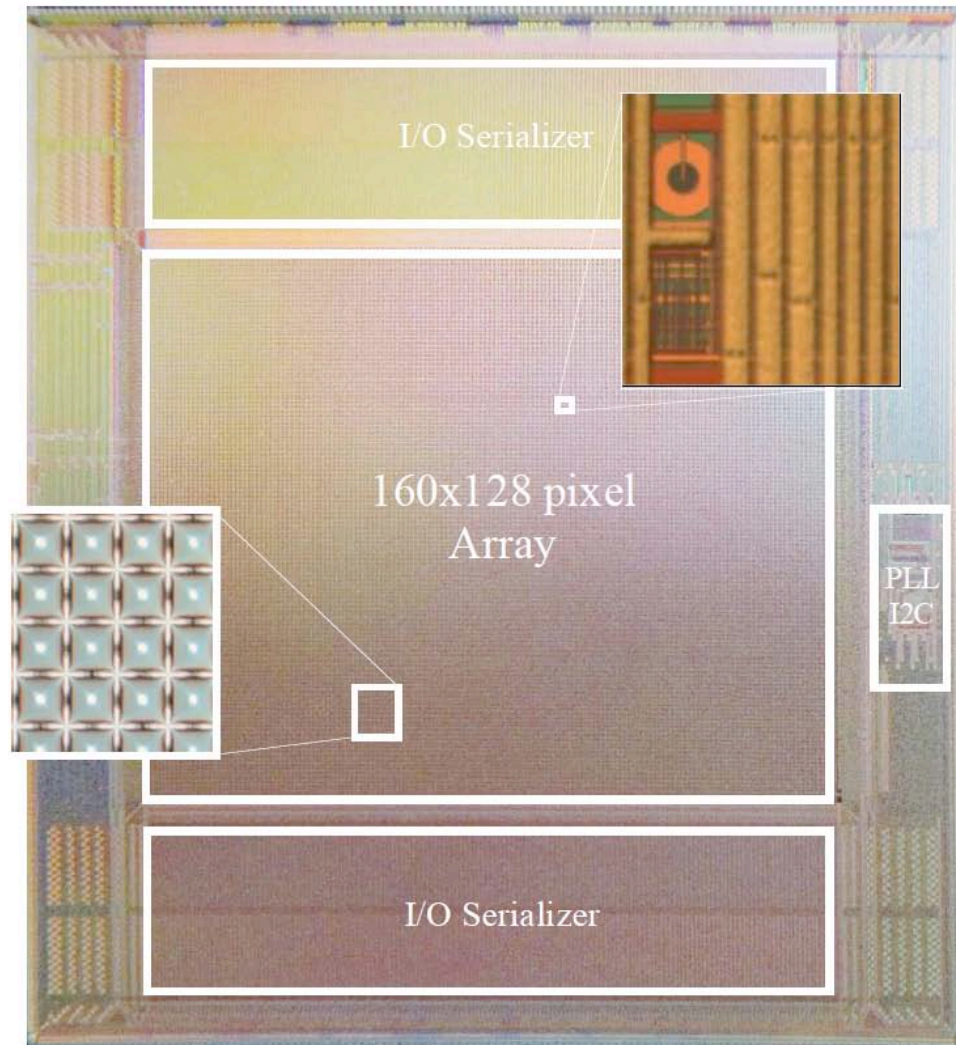


# TCSPC Operation: Conversion & Readout



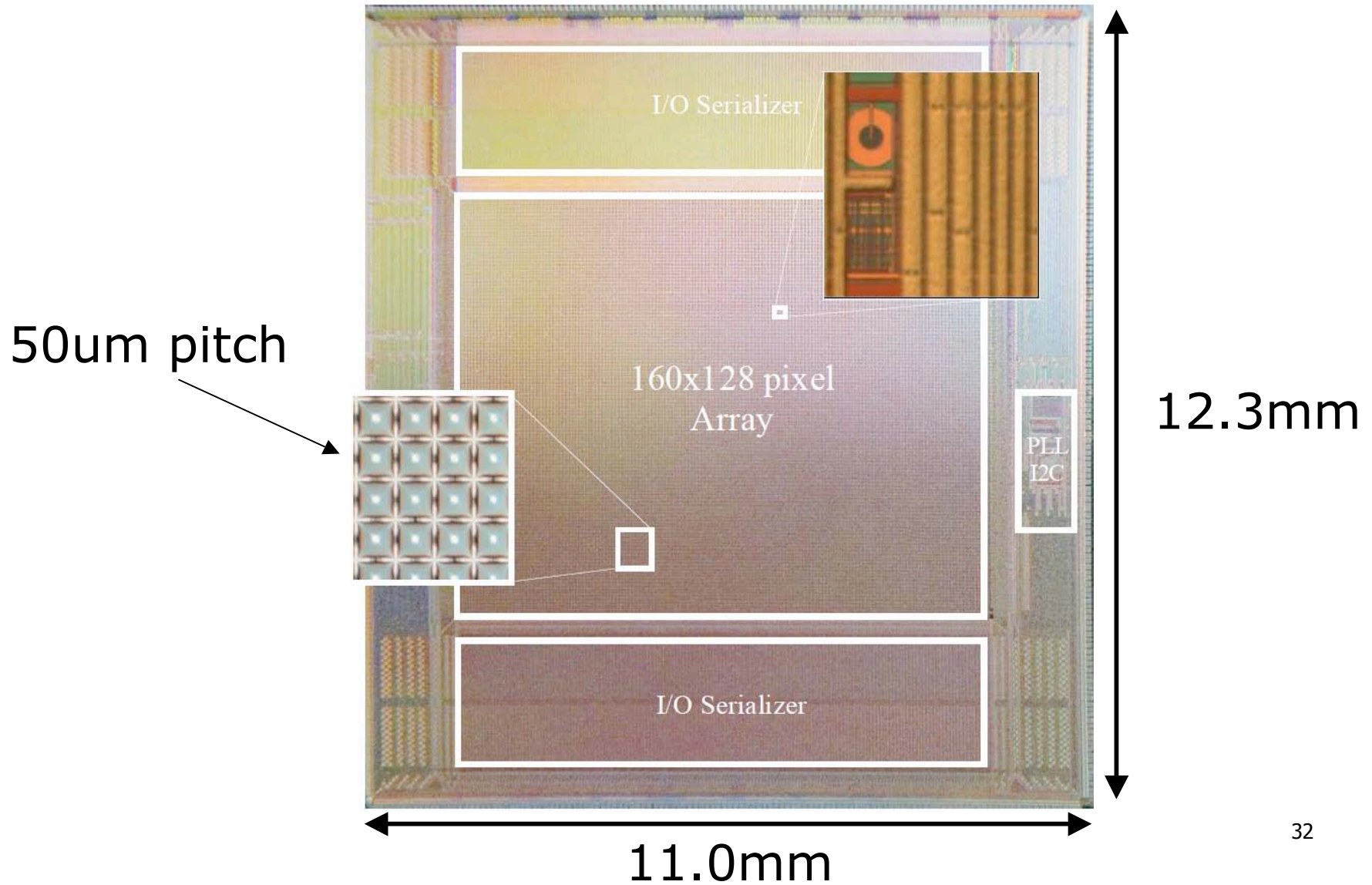
# Results

# The Megaframe-128 Chip



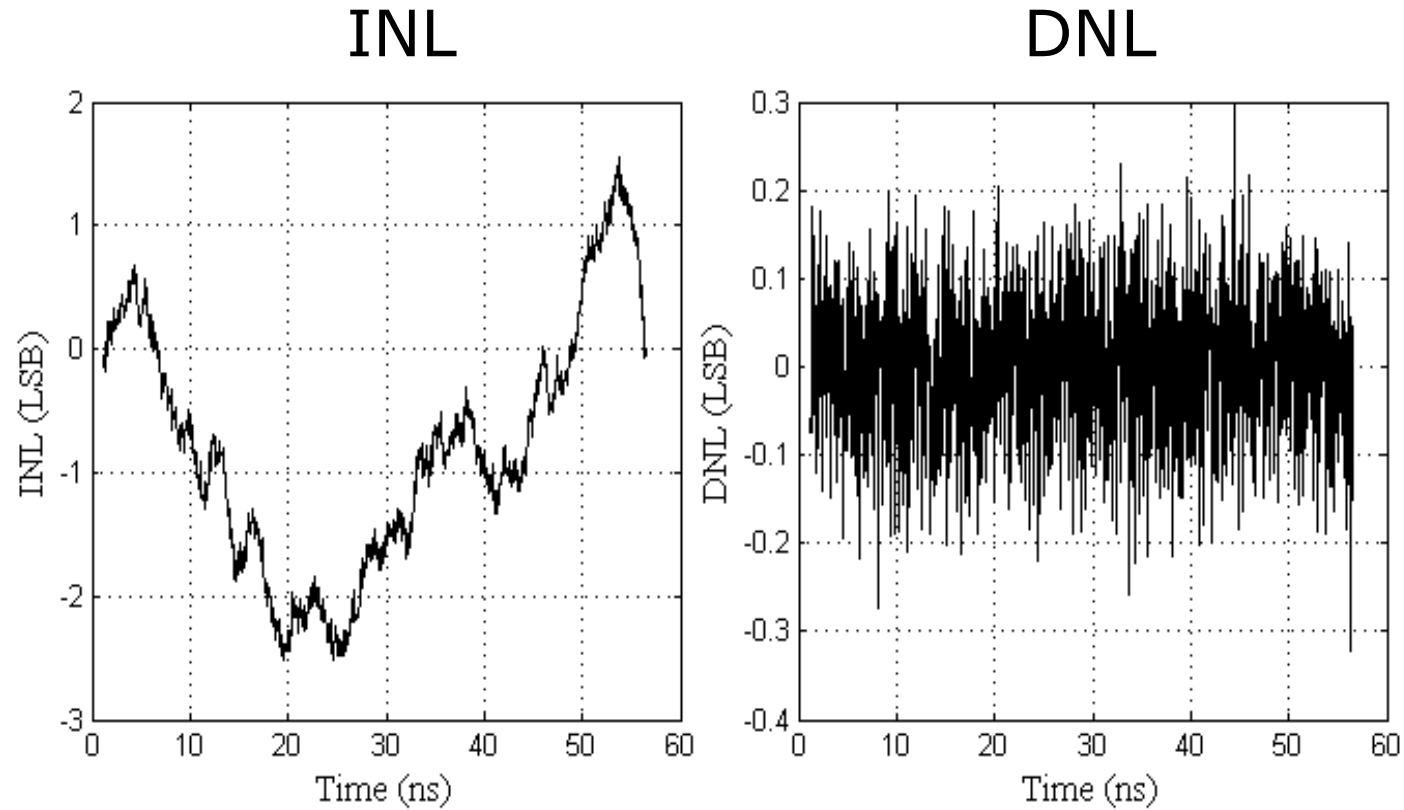
C. Veerappan, J. Richardson, R. Walker, D.-U. Li, M. W. Fishburn, Y. Maruyama, D. Stoppa, F. Borghetti, M. Gersbach, R.K. Henderson, E. Charbon, *ISSCC2011*

# The Megaframe-128 Chip





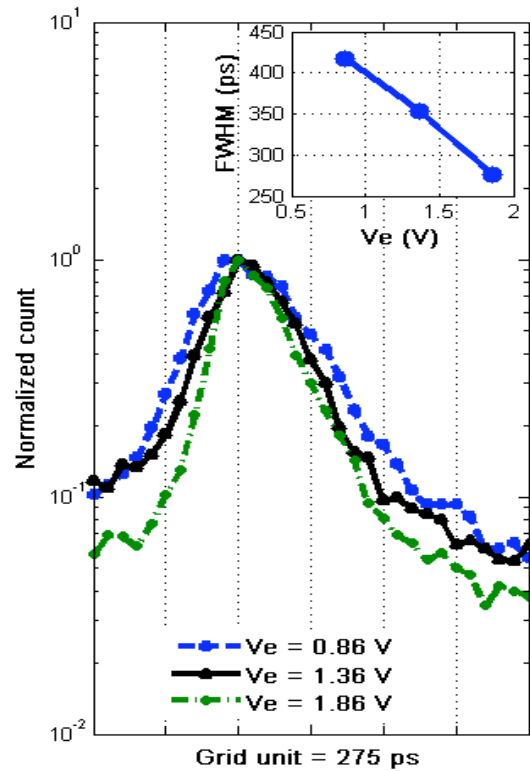
# TDC Characterization



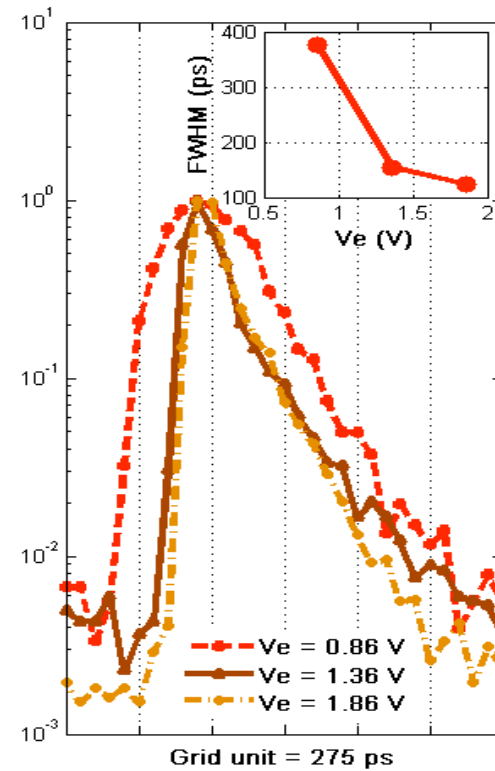
55ps resolution, 55ns range

# System-level Timing Uncertainty

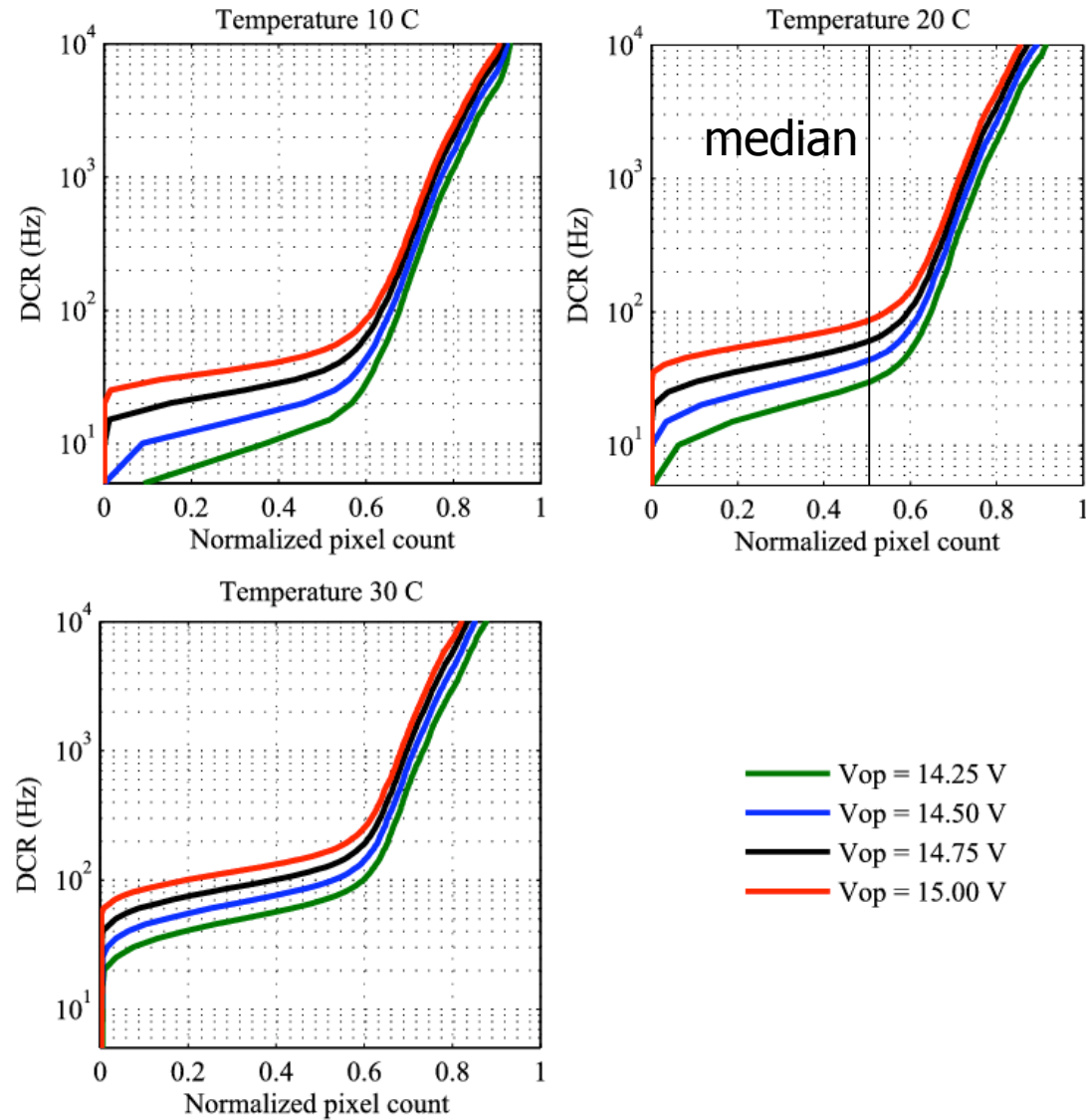
## Blue laser



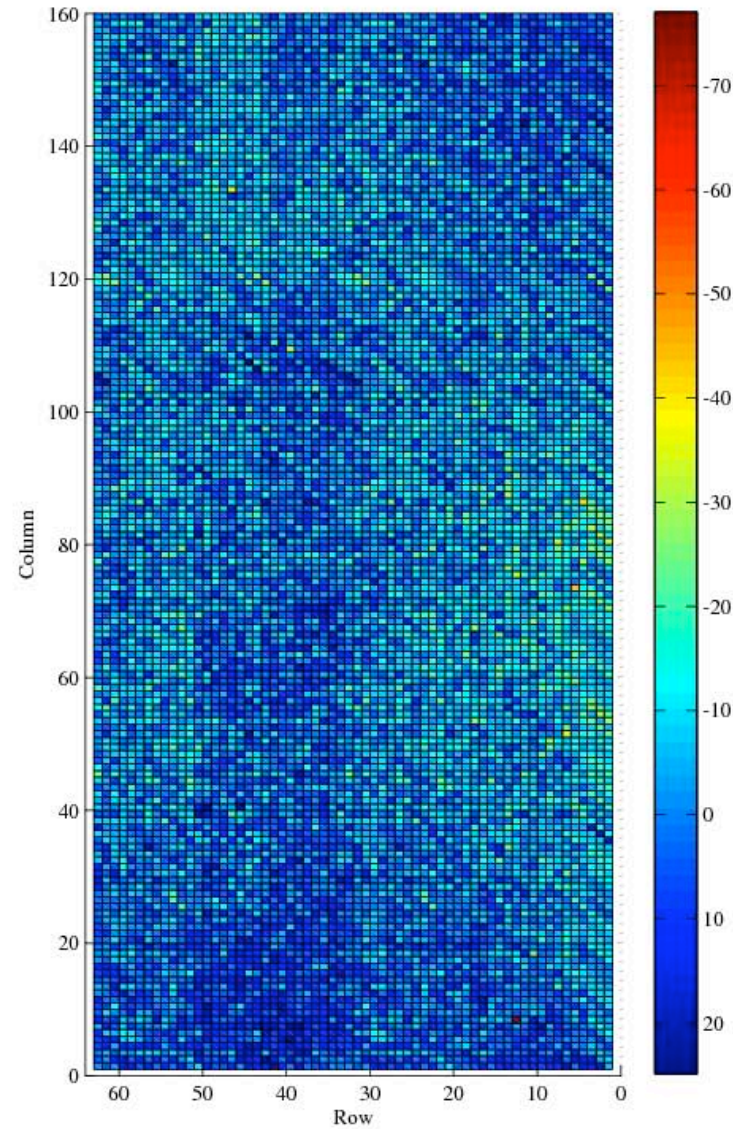
## Red laser



# Cumulative Noise

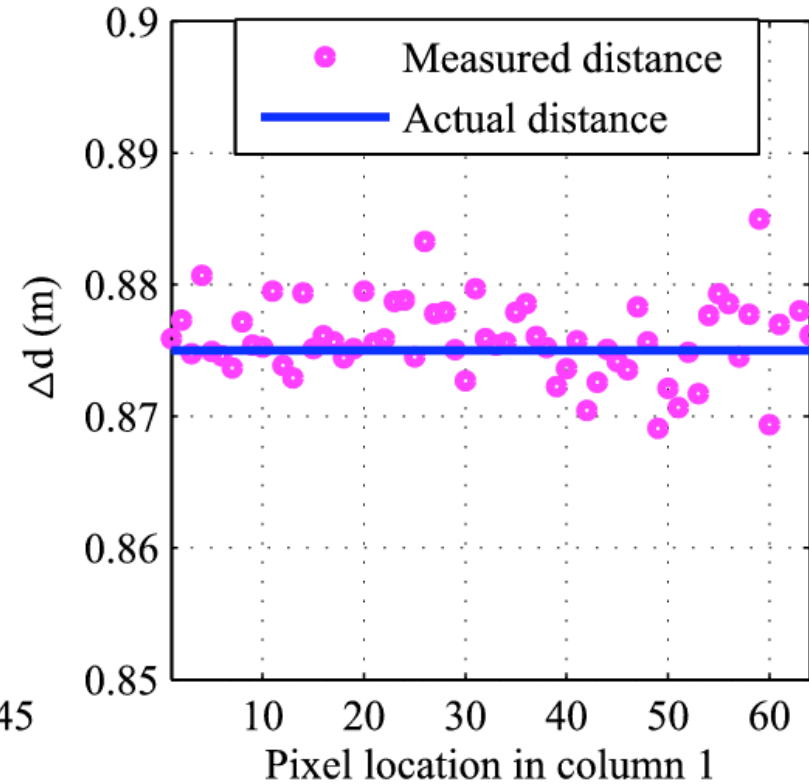
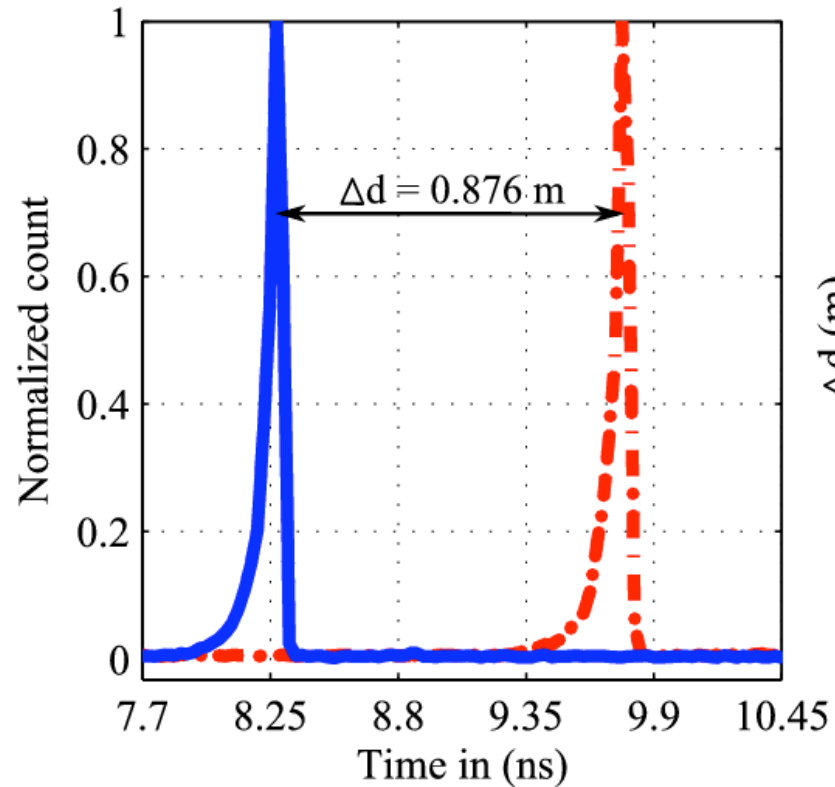


# INL Uniformity



**TDC INL**

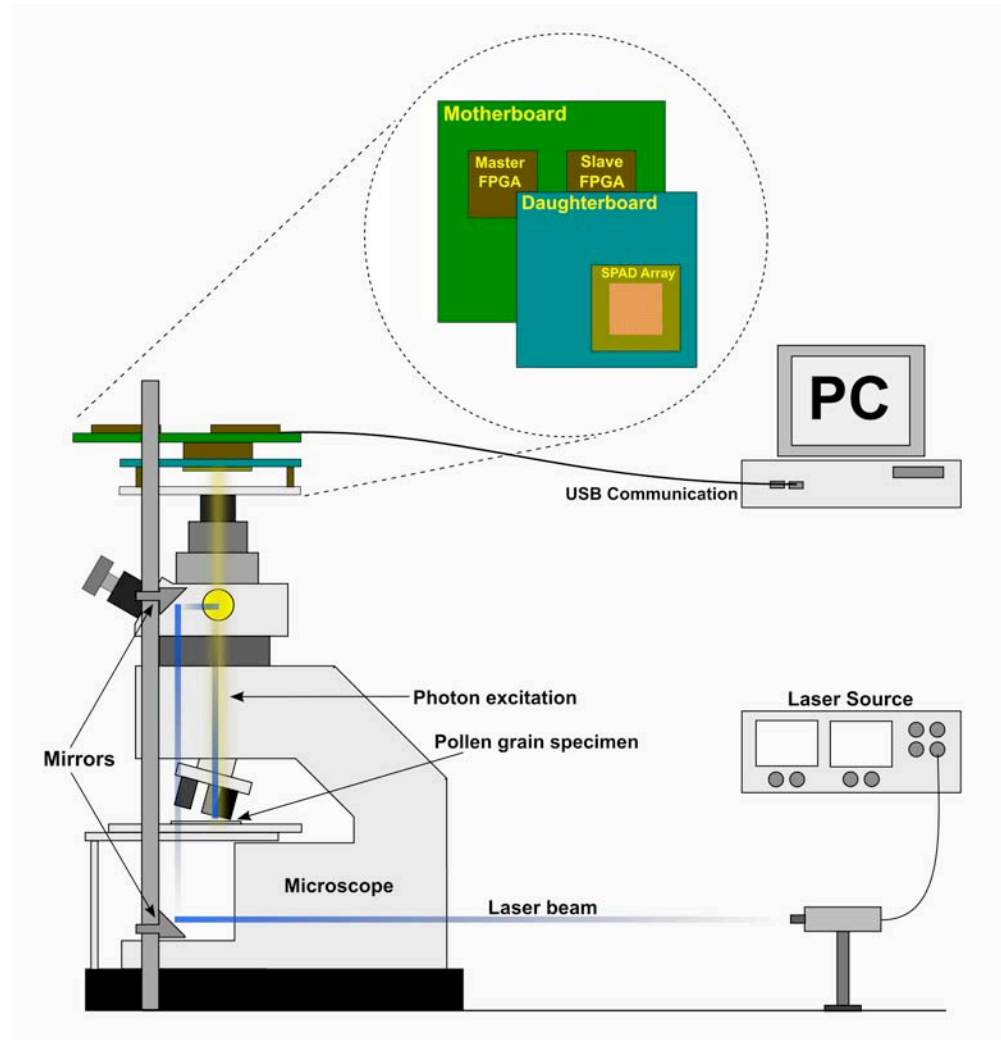
# Optical Burst Detection Uniformity



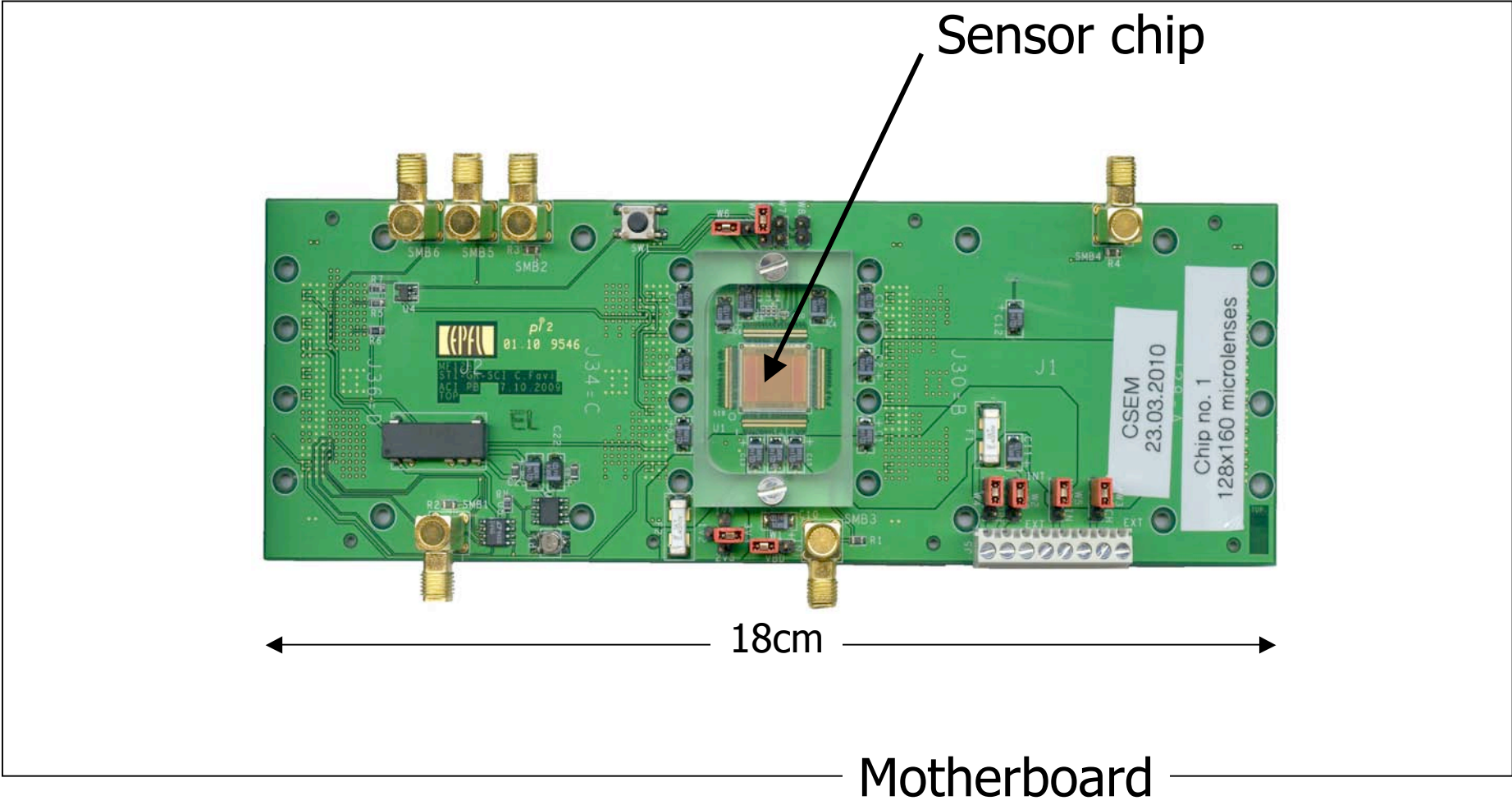
# Performance Summary

	Parameter	Condition	Min.	Typ.	Max.	Unit
Pixel	Photon detection probability	$V_c=0.62V$	3		19	%
	Photon detection probability	$V_c=1.40V$	3		27.5	%
	Sensitivity spectrum		350		900	nm
	Dead time			100		ns
	TDC measurement range			55		ns
	TDC resolution (1 LSB)			55		ps
	Measurement rate			1		MS/s
	TDC DNL / INL			$\pm 0.3 / \pm 2$		LSB
	Nominal fill factor			1		%
System	Clock frequency			16	32	MHz
	Chips size			11.0x12.3		mm <sup>2</sup>
	Total I/O bandwidth			51.2		Gbps
	Power dissipation			550		mW
	CMOS Process			130nm		-
	Median DCR	$V_c=0.73V$		50		Hz
	Mean FWHM jitter	$\lambda=637nm$		140		ps
	1 $\sigma$ jitter non-uniformity	$\lambda=637nm$ , PLL on, count rate = 50kHz		27		ps
FLIM Experiment	Number of measurements per pixel			$3 \times 10^5$		-
	Frame rate			25	50	kfps
	Laser source average power			2		mW
	Average count rate per pixel			15		kc/s
	Target area			125x50		$\mu m^2$

# FLIM Experiment

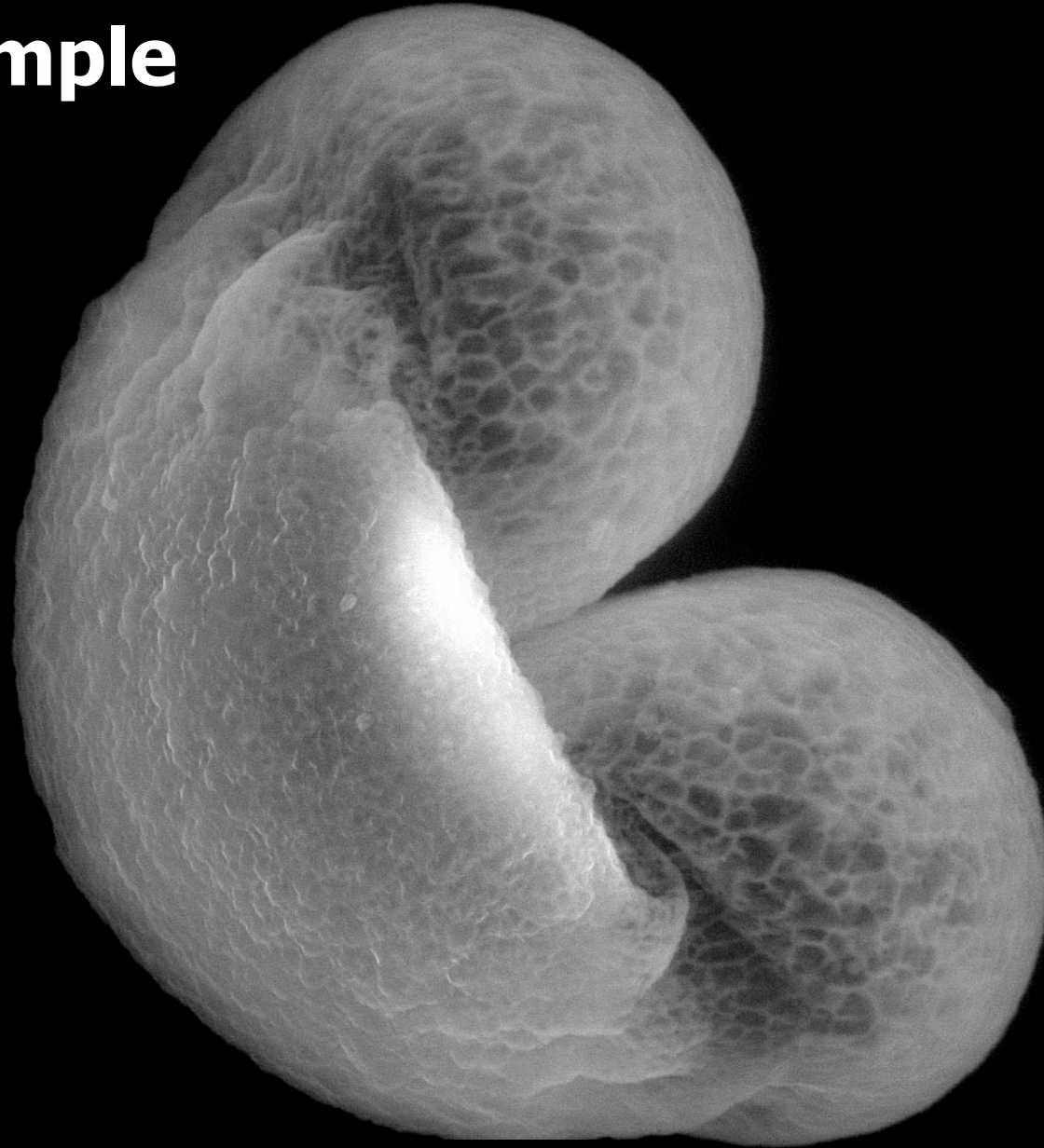


# Megaframe-128 Daughterboard





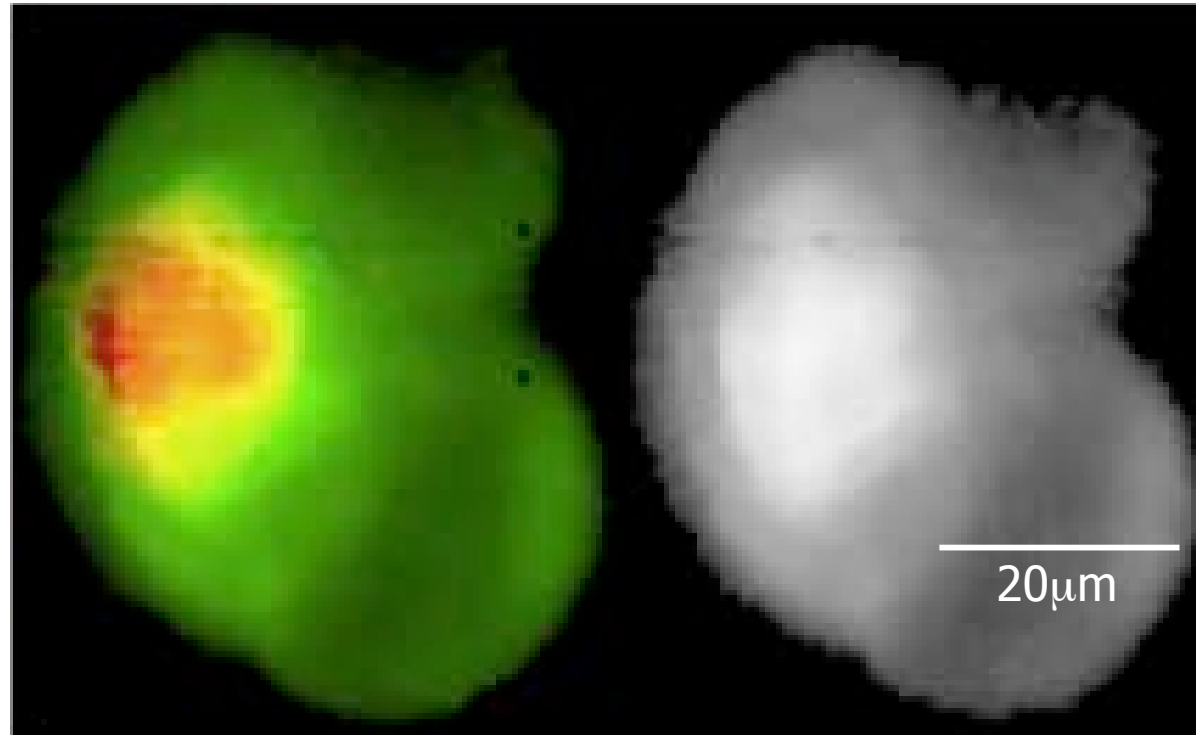
# The Sample



Bisaccate Pine Pollen (Magnification: 3200x)

20.0µm  
Pine Pollen by S. Taylor

# FLIM Image



- 300,000 frames over 12 second exposure time
- 40MHz laser
- Lifetimes ranging from  $\sim 500\text{ps}$  to  $\sim 5\text{ns}$

# Conclusions

- Target application, requirements are critical for selecting trade-offs
- Avalanche diodes show competitive performance in a 130nm imaging process
  - $<1\text{kHz}$  noise,  $>30\%$  PDP

# Future Directions

- Video-rate FLIM
- SPAD scaling trends
  - <1kHz noise
  - >30% PDP in 90nm
  - UDSM: 65nm? 45nm?
- 3D integration
- Microlens work
- TDC sharing (positron emission tomography)

**<http://www.megaframe.eu>**