

MIXED SIGNAL SIMULATION APPLIED TO MODERN ADJUSTABLE SPEED DRIVES

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Abstract: The paper deals with the main problems to solve in order to simulate precisely the behavior of complex power systems with mixed elements (power components and digital regulation devices). A simulation tool developed at the Swiss Federal Institute of Technology is shortly described. Finally, an industrial application of an induction motor fed by a three-level inverter tuned with a DTC (Direct Torque Control) is presented.

electrical and mechanical system components, for the power electronics and for the analog regulation devices), a mixed signal simulation tool must include a model for the most important digital regulation and control devices used today as shown in figure 2.1.1.

1.- INTRODUCTION

During the last few years, the efficiency of numerical simulation tools has been considerably improved. It is possible today to simulate in details complex electrical systems comprising several components, like large power systems or modern adjustable speed drives [1]. The electrical, mechanical and electronic power components are represented with sophisticated models taking also into account non-linear properties. As the regulation part of these complex systems is today more and more based on digital devices, the numerical simulation tools used for the design and the optimization of these systems must be extended, they have to offer corresponding models for the different digital regulation devices. Such extended tools are called analog / digital or mixed signal simulation tools. The simultaneous presence in one simulation tool of both analog and digital elements, which interact together, must be analyzed very carefully. It is necessary to take into account the interactions and the information exchange between the elements of both types according to their physical behavior. This requirement induces some questions, which must be solved in order to built an efficient mixed signal simulation tool.

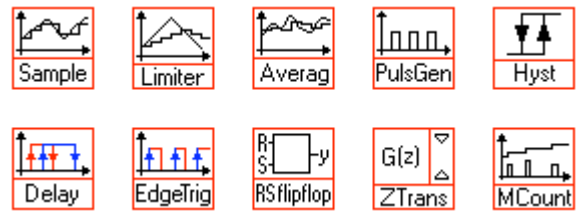


Fig. 2.1.1: Examples of digital control devices

To respect the digital behavior of the regulation, like for example, the different sampling periods or tasks of a DSP (Digital Signal Processor), the simulation tool must be able to calculate each digital value in accordance with its sampling period. The first type of mixed signal simulation tools is based on a constant integration step. The sampling periods have to be a multiple of the defined integration step. This approach can detect events and change states only at the end of the previous step (or beginning of the next one). The major inconvenient of this approach is the low flexibility to take into account events during the step (threshold function detection) and asynchronous digital behavior of the regulation part (not synchronized tasks). The second type of simulation tools uses back-tracking procedures to restart a new integration with a shorter step in order to reach exactly the detected event. That type of tool is able to simulate correctly analog and digital signals. Figure 2.1.2 illustrates digital and analog signals generating events in the same integration step.

2.- MIXED SIGNAL SIMULATION TOOLS

This section will present the main characteristics of a mixed signal simulation tool.

2.1.- Analog and digital components

In addition to the possibilities offered by a conventional numerical simulation program (models for the different

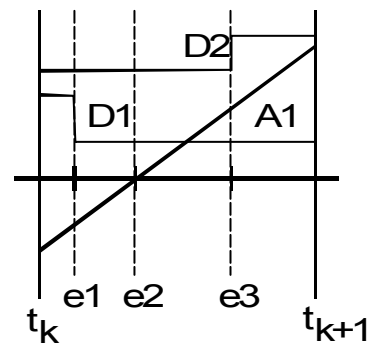


Fig. 2.1.2: Digital and analog signals generating events in the same integration step

The solution that has been developed is the following: After each integration step, the main system is calling all the units involved in the simulation (analog or digital). The system calculates all the new values corresponding to the end of the last integration step. These values are provisional and will be used to detect events during the last integration step. If no event is detected, the system saves the new values and start a new integration step. If one or more events are detected, the system chooses the first one and restarts a new integration with a reduced step in order to reach exactly the detected event.

2.2.- Measurements and A/D converters

The measurement modeling has to take correctly into account the accuracy of an A/D converter. This means that the sampling unit must not only read the measured value but also convert it in accordance with the range of measurement and the number of bits available for the conversion. The following example can illustrate this feature for a voltage measurement:

4 kV measurement with 10 bits. Removing one bit for the sign, the accuracy is given by:

$$\frac{4kV}{2^9 \text{ levels}} = \frac{4000V}{512 \text{ levels}} = 7.81V / \text{level} \quad (2.1)$$

The same example is illustrated in figure 2.2.1 for a DC-link voltage with the above accuracy and a 100µs sampling period.

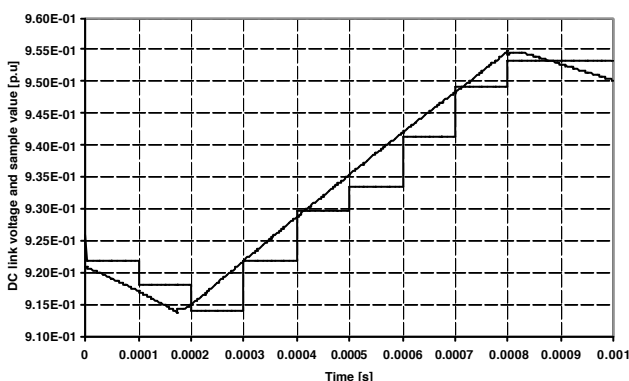


Fig. 2.2.1: Real and sampled values

2.3.- Synchronous and asynchronous behavior

To allow the simulation of asynchronous tasks, it is necessary to implement digital devices with their own clock.

This means that each digital component is working independently. The new regulation processes can even use several independent DSP's having sampling periods without an integer ratio between each other. The synchronization of several digital components belonging to the same task must however be possible. During the simulation, a control task must detect the sampling instant of each digital device. This detection corresponds to an event appearing during the last integration step. In other words, all the values of a digital unit are calculated only at its sampling instant. The main simulation system doesn't make a difference between an event coming from the change of state of a semi-conductor or from the reached sampling instant of a digital device. If these two conditions are fulfilled, the simulation tool can be called mixed signal simulation system.

2.4.- Exchange of values

To combine easily digital and analog signals in the regulation part, a possible solution is to develop units that can work independently. Each unit contains input values x and output values y . A digital unit reads its inputs x and calculates its outputs y only when the sampling instant is reached. On the other hand, an analog unit does it all the time. In the upper part of figure 2.4.1, the two mathematical functions (FMath) are analog. As their inputs x are calculated values coming from a digital unit (Sample), the output values y are also digital.

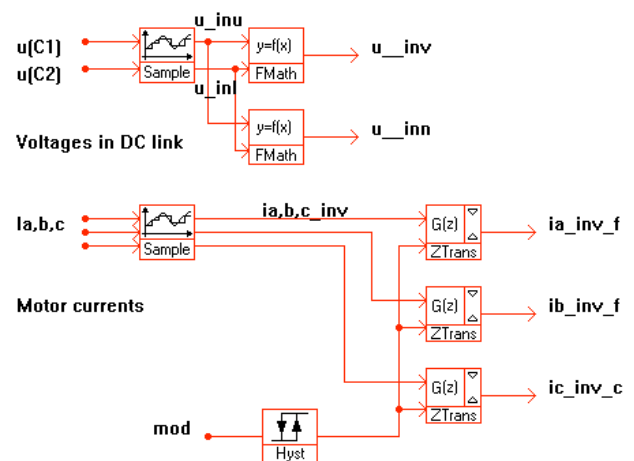


Fig. 2.4.1: Combination of digital and analog units

To improve the flexibility of the system, each unit is able to modify parameters of other units. In the lower part of figure 2.4.1, the hysteresis control (Hyst) tunes on-line the time constant of three digital low-pass filters (ZTrans).

2.5.- Regulation sequence

To implement the regulation part, a simple solution is to use several predefined units having each a special function. By exchanging values between the different units, it is possible to easily implement the desired regulation algorithm. To respect the regulation sequence, all the regulation units are sorted according to the following rule: each input value x of a regulation unit must already be calculated before its use in any other regulation unit. Unfortunately, there are some cases where this rule cannot find a solution, as for example, the case of a Phase Locked Loop (PLL) shown in figure 2.5.1.

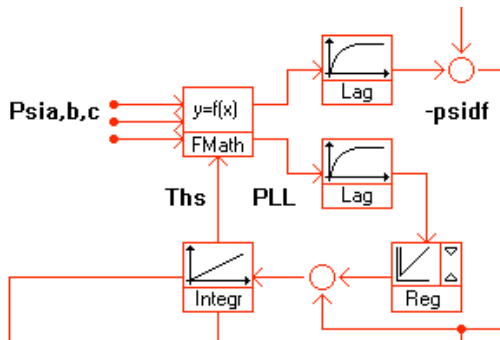


Fig. 2.5.1: Phase Locked Loop (PLL)

The mathematical function FMath is calculating the coordinates transformation from 3 phases to d and q axes. The unit is using 3 phase values $\Psi_{ia,b,c}$ and an angle Θ_{hs} coming from an other unit Integr. One output y of the FMath unit is used in a low-pass filter Lag. The output y of that low-pass filter is used in a regulator unit Reg. The output y of that regulator unit is used in an integrator unit Integr. One can easily see the problem of the closed loop. The unit FMath is using the output y of the integrator Integr. Applying the rule mentioned in the above section, the check for the regulation sequence will not find a solution. In that case, the simulation tool must give the possibility for the user to either by-pass this check or to require his own sequence. This possibility has been provided using an additional symbol. If the checking task encounters that symbol, the input signal x will not be taken into account for the definition of the regulation sequence. In the end, the user must have the possibility to show the selected sequence in a netlist (list with all the components active in the simulation). In the case of a PLL, it is clear that if the unit FMath is using an angle Θ_{hs} coming from an integrator, there is only a very small error if the function is using the angle Θ_{hs} calculated after the last integration step instead of the present angle at the end of the new integration step. Another advantage of this method is the possibility to implement in details the regulation of a complex power system. Simulation tools are very inefficient when they have to implement a lot of closed loops or when

they need to automatically adapt parameters in the regulation part to the operating point of the power system. We can easily check if the problem described in this section is satisfactorily taken into account.

3.- THE SIMULATION TOOL SIMSEN

Since 1992, the Swiss Federal Institute of Technology is developing a simulation tool called SIMSEN [2]. This tool fulfills all the requirements mentioned in the section 2. It will be shortly presented.

3.1.- Main Features

- Graphical interface
- Modular structure with arbitrary topology
- No restriction on the network size
- Events detection and back-tracking
- Load-Flow calculation
- Initial conditions entirely, partly or not defined
- Stable operating point entirely saved
- Interactive read/write access to any parameter
- Harmonics analysis
- Parameterization
- SI or per unit outputs
- Runs on PC with Windows NT

3.2.- Application fields

The tool has been developed in close relation with several complex studies concerning electrical power networks and adjustable speed drives. SIMSEN has shown its performance, especially in complex power systems and adjustable speed drives simulations. Computation results have been compared successfully to industrial application measurements. Figure 3.2.1 shows an example of High Voltage DC (HVDC) transmission with a static Var Compensator (SVC).

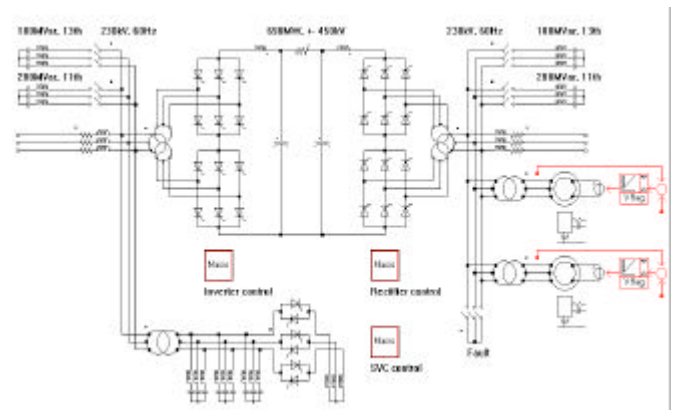


Fig. 3.2.1: HVDC transmission with SVC

3.2.1.- Adjustable speed drives. Special machines, power electronics converters, cyclo-converters, Voltage Source Inverters (VSI), multi-level inverters, analog / digital mixed signal simulation, control and regulation.

3.2.2.- Electrical power networks. Electrical machines with mechanical shaft, electromagnetic transients in AC/DC networks, transient stability and general fault analysis, SubSynchronous Resonance (SSR), Flexible AC Transmission Systems (FACTS), High Voltage DC

transmission (HVDC), Static Var Compensation (SVC), control and regulation.

3.2.3.- Regulation part. Easy to build a new regulation scheme, mathematical functions, logical table, S-transfer function, regulators, digital devices, Z-transfer function, enhanced control devices.

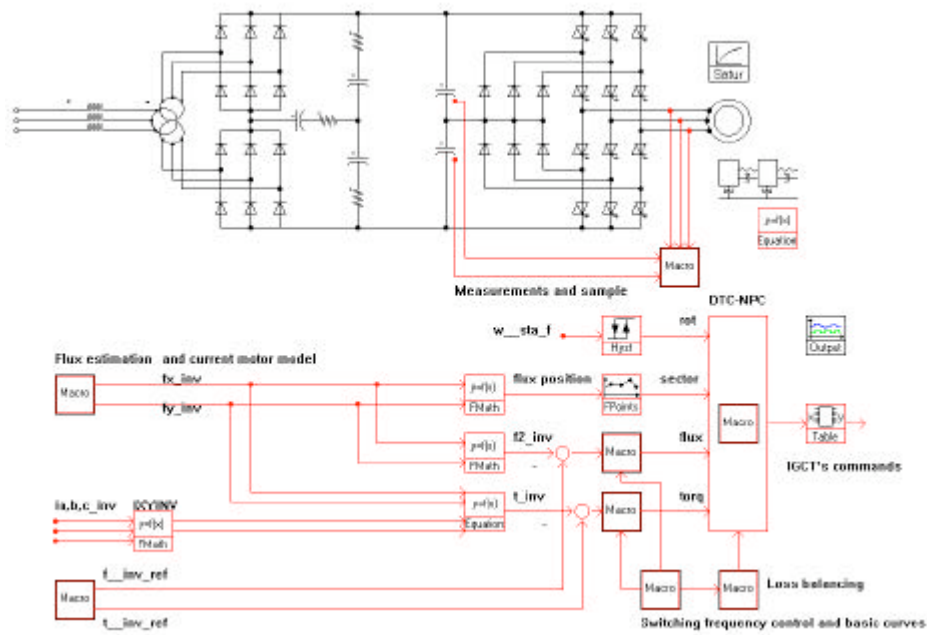


Fig. 4.1.1: Induction motor fed by a three-level inverter

4.- EXAMPLE OF INDUSTRIAL DRIVE

4.1.- Power system part modeling

Figure 4.1.1 shows the studied system. The squirrel cage rotor induction motor is supplied through a three-level Voltage Source Inverter (VSI) containing 12 IGCT's (Integrated Gate Control Thyristor [3]) and 6 Neutral Point diodes. The DC-link voltage is stabilized with 2 capacitors. The DC-link is supplied through a 12 pulse diode rectifier supplied itself by the AC network through a three-winding transformer. Additional snubber circuits in the DC-link are also taken into account. The mechanical shaft, rotor and pump have been modeled with 2 rotating masses. The mechanical load is a square function of the speed to respect the pump behavior. All the semi-conductors are modeled with lumped R-L elements and voltage supply. That kind of modeling is good enough to analyze complex power systems

as long as the physical behavior is correctly taken into account. It presents two other advantages: speed of computation and numerical stability.

4.2.- Regulation part modeling

4.2.1.- Measurements. Even if it is possible to measure the motor voltages, this solution is actually too complicated and too expensive. For that reason, the control is only measuring the two DC-link voltages in the upper and lower parts of the inverter. These two voltages are sampled with 100µs. At the output of the inverter, only the three phase currents are measured. These currents are sampled with 25µs.

4.2.2.- Stator flux estimation. All the regulation is defined in a fixed referential using two axes x and y 90° phase shifted. The principle of the stator flux estimation is based on a voltage integration acting every 25µs. Depending on the switching state of the inverter, the voltages in the x and y axes are deduced from the two DC-link voltages. Such an

estimation is naturally not good enough at low frequency (< 10%) even if the stator winding resistance is taken into account. An additional and complex current motor model has been implemented in details to correct the output of the voltage integration every 200µs. The obtained results are very good, even at standstill.

4.2.3.- Direct Torque Control (DTC). The DTC is explained in [4]. The main advantage of this regulation is the fast response of the electromagnetic torque (some milliseconds. Figure 4.2.1 shows the voltage vectors numbering, the allowed transitions and the x, y axes defined for the regulation. The principle of the DTC regulation is to select an optimal vector in order to correct the stator flux magnitude and position. The x, y area is split in 12 sectors of 30°. The sector 1 is defined between the x axis and the vector number 1.

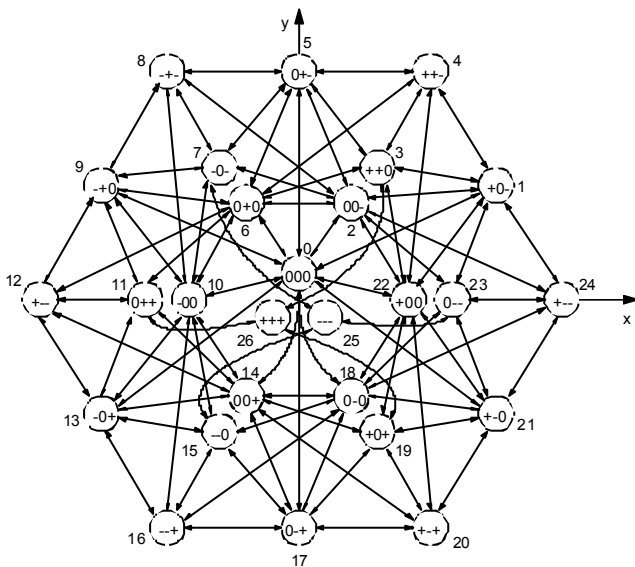


Fig. 4.2.1: Vector numbering and allowed transitions

At each regulation step (25µs), the control calculates in which sector the stator flux is located. The flux magnitude and the estimated torque are compared to their reference values through hysteresis controls. Depending on the flux sector and the output signals of the hysteresis controls, an optimal vector is selected among the set of available vectors shown in figure 4.2.1. For example, if the stator flux is in the sector No 1 (0-30°) and the flux and the torque magnitudes are too small, the DTC will choose the voltage vector No 5 (0+-) to increase the flux magnitude and the flux angle (this will increase the torque). As the inverter is built using only one snubber circuit for the three phases, one can only have one transition in each half of the inverter at one time. This is the reason why the allowed transitions are also taken into account. The whole DTC control has been implemented with logical tables.

4.2.4.- Neutral Point Control (NPC). It is interesting to observe that some vectors with different numbers provide the same output voltage but are acting either on the upper DC voltage U_{DC1} or the lower DC voltage U_{DC2} . The Neutral Point Control (NPC) is responsible for the deviation between the 2 DC-link voltages keeping this one as small as possible [5]. This control is also working with hysteresis control. Having a look at figure 4.2.1, one can easily see that for vectors 2-3, 6-7, 10-11, 14-15, 18-19, 22-23, one phase is connected to the middle point of the inverter. That means that a current will charge or discharge the related capacitor. Without any regulation, the deviation can increase. To avoid this problem, the NPC will choose between the different possible vectors of the above set.

4.2.5.- Switching Frequency Control (SFC). As the switching instants are not provided by a carrier signal like in a Pulse Width Modulation (PWM), the hysteresis widths have to be well selected. Constrained by the power electronic part of the drive, the switching frequency of each IGCT cannot go over 500 Hz. The SFC is acting on the hysteresis width of both flux and torque controls to increase or decrease the switching frequency (average value). The goal of that regulation is to keep the switching losses in an acceptable range.

4.3.- Simulation results

All the simulations have been applied to a real industrial drive with the rated values 1.1 MVA, 4 kV, 60 Hz.

4.3.1.- Steady operating point at 100% flux, 90% speed and 85% torque.

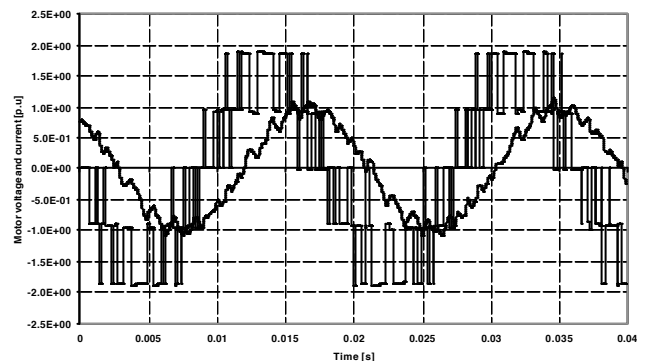


Fig. 4.3.1: Motor voltage and current

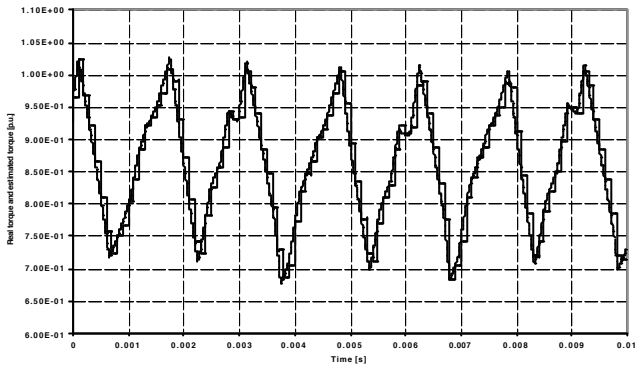


Fig. 4.3.2: Real motor torque and estimated torque (200µs)

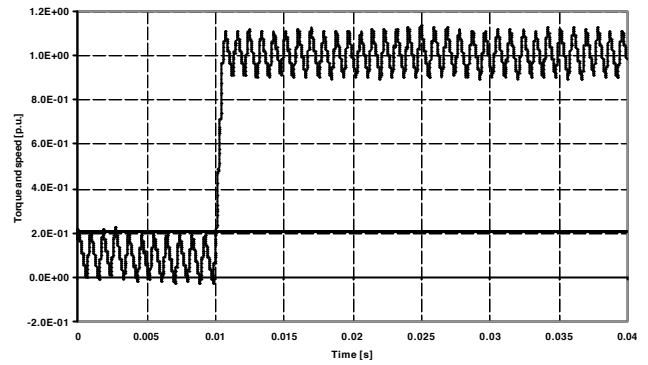


Fig. 4.3.5: Torque and speed

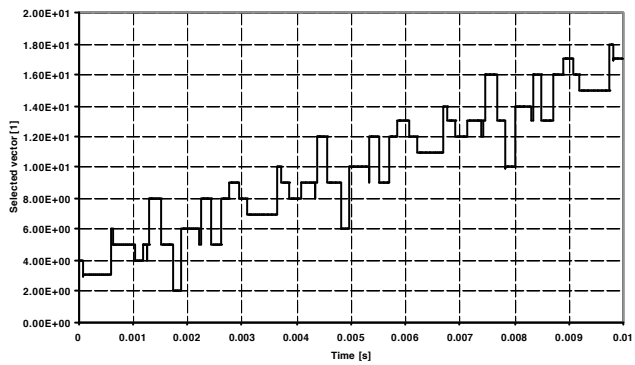


Fig. 4.3.3: Number of selected vector (25µs)

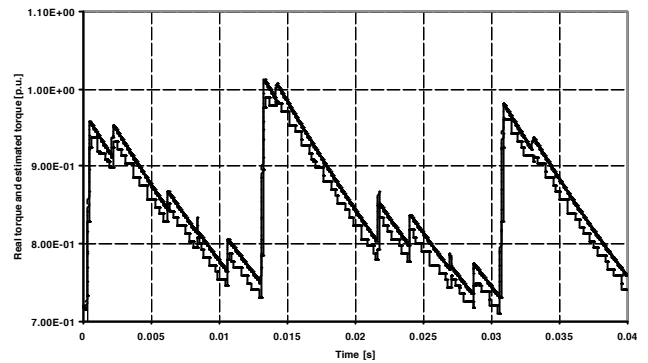


Fig. 4.3.6: Real torque and estimated torque at standstill

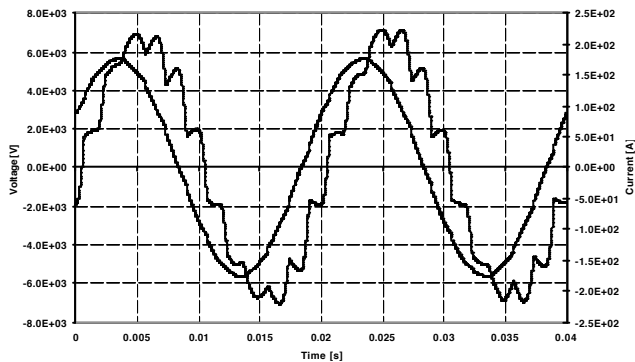


Fig. 4.3.4: Line to line voltage and current of the network

The simulation is providing one results file per unit. The user can see more than 1000 signals coming from both the power and the regulation parts. The main problems that have been successfully solved are the multiple interactions defined in the regulation. For example, the SFC is adapting the width of the hysteresis control used in the DTC. The calculation of the new selected vector is based on the previous value of the vector. The 25µs calculation time of the DSP are also taken into account.

4.3.2.- Torque step response at 100% flux and 20% speed.

5.- CONCLUSION

The paper presents the main problems that have to be solved in order to simulate precisely drives including power electronics, machines, analog and digital regulation devices. The requirements an efficient mixed signal simulation tool has to fulfill have been explained. A simulation program that allows complex systems analysis has been shortly presented. Finally, an example of a real industrial drive for medium voltage applications has been described.

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