# Optical developments for silicon thin film solar cells in the substrate configuration

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#### **ABSTRACT**

In the nip or substrate configuration thin film silicon solar cells, the choice of front TCO contact is critical because there is a trade off between its transparency which influences the current in the solar cell and its conductivity which influences the series resistance. Here, we investigate the optical behavior of two different TCO front contacts, either a 70 nm thick, nominally flat ITO or a 2  $\mu$ m thick rough LPCVD ZnO. The back contact consists of LP-CVD ZnO with random texture. First we investigate the influence of the rough and flat front TCOs in  $\mu$ c-Si:H and a-Si:H solar cells. With the back contact geometries used in this work, the antireflection properties of ITO are effective at providing as much light trapping as the rough LP-CVD ZnO. In the second part, we demonstrate that total of 25 to 26 mA/cm² is achievable in nip micromorph tandem cells and show short circuit current up to 11.7 mA/cm² using an SIO based intermediate reflector.

#### 1. INTRODUCTION

In the thin film silicon solar market, the substrate or nip configuration can be a competitive advantage because flexible non transparent substrate can be used [1]. Furthermore, roll to roll deposition techniques [2] can be applied. Several companies develop solar cells with initial efficiencies over 15 % [3-5]. Here, we develop low temperature processes (below 200°C) compatible with plastic substrates such as PEN or PET. Our primary goal is to increase the stabilized efficiency of thin film silicon solar cell in the substrate configuration by using textured substrates, tandem micromorph structures [6].

In the nip configuration, substrate texture is compulsory to increase the current in the thin film silicon solar cell while reducing cell thicknesses. This leads to reduced light induced degradation effect [7] and effective cycle time in production. For efficient light trapping, the substrate textures should be in the dimension range of the incoming light wavelength. The common substrate used by the industries is hot silver which provide elevated light trapping in the cells. Here, we develop LP-CVD ZnO on glass substrate and it can also be applied on plastic. In addition, we compare two transparent conductive oxide (TCO) candidates as front contact; the first one is a flat thin ITO layer and the second one is a rough thick ZnO LP-CVD.

First, we describe the difference between the TCOs by reflection and transmission measurements on glass substrate only. Then, we investigate the behavior of ITO and ZnO front contact on amorphous (a-Si:H) and microcrystalline ( $\mu$ c-Si:H) solar cells. Optically the situation between thin a-Si:H (200 nm) absorber and a thick (1  $\mu$ m)  $\mu$ c-Si:H is different. First of all the optimum dimension of the substrate texture is different for the two types of cell because the light trapping region is increased toward the red part of the spectrum for  $\mu$ c-Si:H. In the third part, we describe the possible routes to further enhance the short circuit current ( $J_{sc}$ ) in micromorph

tandem cells. We propose to use a thicker absorber layer, a rougher substrate texture and a  $SiO_x$  based intermediate reflector to match the current of the top and bottom cells.

### 2. EXPERIMENTAL

The transparent front electrodes are zinc oxide (ZnO) deposited by low pressure chemical vapour deposition (LP-CVD) [8, 9] or indium tin oxide (ITO) deposited by DC sputtering at room temperature. The ZnO is doped with boron, its deposition conditions result in a textured surface with RMS of about 70 nm for standard 2µm thick layers and its sheet resistance is below 10 ohm square. The ITO is deposited nominally flat, has a thickness of 70 nm to act as an antireflection layer on Si and has a sheet resistance of 30 ohm square on glass. The total transmission (TT), diffuse transmission (DT), and total reflexion (TR) are measured with a photo-spectrometer, (Perkin Elmer lambda 900) with integration-sphere within the spectral range of 400 nm to 2000 nm. The absorbance (A) is calculated from TT and TR with A=1-TT-TR. The measured structure in the spectrometer is TCO/glass. The ZnO is measured as grown and with CH<sub>2</sub>I<sub>2</sub> which means that a thin film of index matching liquid di-iodomethane (CH<sub>2</sub>I<sub>2</sub>) is added between the ZnO layer and a 0.7 mm thick AF45 glass. It removes the effect of the rough interfaces and interference fringes during transmission and reflectance measurements [10].

As substrates, we use glass coated with LP-CVD ZnO which develops pyramidal textures during the growth process. This texture provides high current for nip single a-Si:H solar cells [6] with feature size of typically 360 nm with root mean square (RMS) roughness of 70 nm. The size of the LP-CVD ZnO can be also adapted for  $\mu$ c-Si:H solar cells by increasing the lateral dimension to 1  $\mu$ m and RMS to 150 nm. Similar morphologies are used for pin cells in our laboratory [11]. In addition, a white paint is added at the back of the glass as back reflector.

The silicon films are deposited by plasma enhanced chemical vapour deposition, at very high excitation frequencies (VHF-PECVD, 50-150 MHz). The main gases for the deposition are silane (SiH<sub>4</sub>), hydrogen (H<sub>2</sub>), and methane (CH<sub>4</sub>). Phosphine (PH<sub>3</sub>) and trimethylboron (TMB) are added for the n and p doped layers.

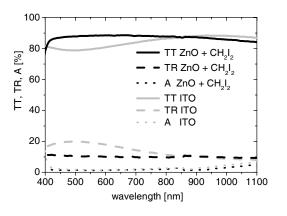
The current-voltage (IV) measurements are performed at  $25^{\circ}$ C under AM1.5g solar spectrum with a solar simulator (Wacom WXS-140S-10). From that IV curve the  $V_{oc}$  and FF are obtained. The external quantum efficiency (EQE) is measured with a spectral response setup at short circuit condition except when something else is specified. The short circuit current-density ( $J_{sc}$ ) is calculated from the EQE by convolution with the AM 1.5g solar spectrum and by integrating over the wavelength range. This method avoids uncertainties in the determination of the solar cell surface area.

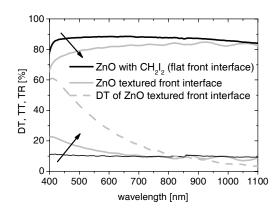
## 3. RESULTS AND DISCUSION

# 3.1. Rough LP-CVD ZnO on glass

Figure 1 (left) shows interference effects in the TT, TR and A curves of the thin flat ITO on glass which are not apparent in the case of ZnO measured with the CH<sub>2</sub>I<sub>2</sub> index matching. The

absorbance of the ZnO measured with  $CH_2I_2$  is below 2% and is similar to the absorbance of ITO. Thus, the difference between the two TCOs can be completely attributed to the structure of the two devices, i.e. flat thin against thick rough TCO. The rough interface of the ZnO leads to an increase of the absorbance (not shown here for the sake of clarity) and reflection, see Figure 1 (right). It is attributed to light trapping within the 2  $\mu$ m thick ZnO. Note that the Haze (H=DT/TT) is 90% at 400nm but only 22% at 700nm. It means that for the wavelengths between 650 and 800 nm, they are only slightly scattered by the rough TCO/air interface. However, This TCO/glass device does not give direct information on the optical behavior of the rough TCO/Si interface in the solar cell because light trapping due to total internal reflection will not take place at this interface ( $n_{Si} > n_{TCO}$ , which is different from  $n_{TCO} > n_{glass}$  in this test structure).





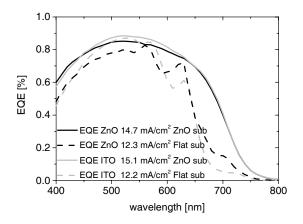
**Figure 1:** On the left, we compare the TT (plain line), TR (dashed line) and A (dotted line) of a flat 70 nm ITO (light grey), and a 2  $\mu$ m ZnO (black) LP-CVD with CH<sub>2</sub>I<sub>2</sub>. On the right, we compare TT and TR for 2  $\mu$ m thick ZnO LP-CVD with flat (black) and rough (grey) air/ZnO interface.

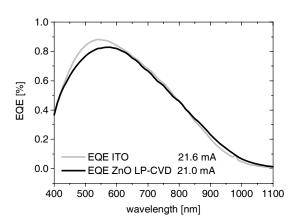
### 3.2. Rough ZnO and flat ITO in solar cells

In Figure 2 and Table 1, we compare the same flat 70 nm ITO and the 2 µm thick LP-CVD ZnO as front TCO in 270 nm a-Si:H and 1.1 µm thick µc-Si:H solar cell. The cells are deposited on LP-CVD ZnO back contact on glass substrate. The lateral dimension of the back ZnO LP-CVD has been adapted for the light scattering of both types of solar cells as described in section 2. A flat a-Si:H deposited on plastic substrate coated with 80 nm of silver and 60 nm of sputtered ZnO is added for reference. The results show that on textured substrates, there is advantage in using a flat ITO. Indeed, J<sub>sc</sub> is increased, relatively, by 3% for thin and thick absorber layers on textured substrates. The antireflection effect of the ITO yields increased spectral response close to 550 nm while the red part of the spectrum the response is similar for both TCOs on textured substrate. However, for a-Si:H on flat substrate the light trapping is increased with a rough LP-CVD ZnO substrate compared to ITO. The LP-CVD ZnO even compensates the reflection losses at 550 nm by an increase of EQE between 600 and 750 nm.

Table 1: Comparison of the short circuit current density of a-Si:H and μc-Si:H solar cell with ZnO and ITO front contact

J <sub>sc</sub> [mA/cm2]	substrate	ZnO LP-CVD	ITO	$\Delta J_{sc}(\%)$
a-Si:H	Flat	12.3	12.2	0
a-Si:H	ZnO CVD	14.7	15.1	3
μc-Si:H	ZnO CVD	21.0	21.6	3





**Figure 2:** Left, comparison between flat ITO and rough LP-CVD ZnO front contact with a-Si:H solar cell on flat (dashed lines) and LP-CVD ZnO coated substrates (plain lines). Right, comparison between flat ITO and rough LP-CVD ZnO front contact with μc-Si:H solar cell on LP-CVD ZnO substrate wit large feature size.

### 3.3. Micromorph tandem cells

In this part, we report on micromorph solar cells contacted with the front ITO and the possible routes to further improve the tandems performances. The cell are deposited on a LP-CVD ZnO substrate with large feature size of the pyramids. The thicknesses of the layers are 300 nm a-Si:H, and 1.2  $\mu$ m  $\mu$ c-Si:H, respectively, and 70 nm for the ITO front contact. The initial efficiency is 10.2% with  $V_{oc} = 1.33 V$ ,  $J_{sc} = 11.3$  mA/cm² and FF = 68%. For higher efficiency clearly the  $J_{sc}$  needs to be increased. In fact best nip solar cell can reach total current up to 28 mA/cm² with ITO front contact [5].

We currently develop two strategies to further increase the efficiency of the micromorph cells. This development is carried out with LP-CVD front contact because this thick layer act as a protective coating for the Si films which facilitate the solar cell process and measurements by reducing shunting of the thin devices. First, we increase the total current of the tandem cells by increasing the thickness of the  $\mu$ c-Si:H solar cell from 1.2  $\mu$ m to 2.5  $\mu$ m, and second we use LP-CVD ZnO substrate with structure that shows better light trapping performance [12]. In Figure 3 (left), we show that this increases the total current by 2.3 mA/cm² to a total of 25 mA/cm². Thus, with an ITO front contact, we expect to be able to achieve 26 mA/cm². But, it still does not increase the matched  $J_{sc}$  of the tandem because so far all the increase of total current is in the

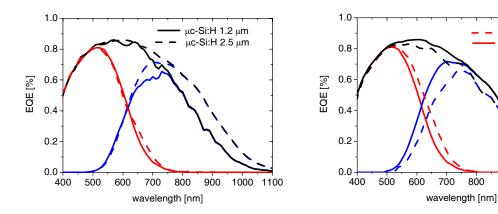
bottom cell. The matching can not be achieved by increasing the top cell thicknesses without compromising the cell light induced stability [7]. Thus, we increase the top cell current by introducing an SiO based intermediated reflector (SOIR) between the top and bottom cell [13]. The 80 nm SOIR produces a refractive index difference with the silicon which increases the reflection from the solar cell. Figure 3 (right) shows that the top cell current can be increased by 1 mA/cm² and the bottom cell current is decreased by 2.4 mA/cm². Thus, the total current is decreased by 1.4 mA/cm² because part of the green light is in fact also reflected out of the solar cells. This value corresponds to observations with SOIRs in pin cells [13] which were however partially attributed to internal absorption. Thus, the performance of our intermediate reflector is not yet sufficient. We think that the refractive indexes of the IR can still be reduced below 2.1 which is our current status development. In fact this year in Fukuoka, M. Ichikawa reported a strong improvement of an IR material by achieving a refractive index of n = 1.6 [14].

with SOIR

900

1000

without SOIR



**Figure 3:** On the left, comparison of the EQE of micromorph cells with 1.2  $\mu$ m  $\mu$ c-Si:H (full line) and thick 2.5  $\mu$ m  $\mu$ c-Si:H bottom (dashed line) cells on LP-CVD ZnO substrate and with LP-CVD ZnO front contacts. On the right, we compare the EQE of micromorph cells with (dashed curve) and without SOIR (plain curve) on LP-CVD ZnO substrate and with ZnO LP-CVD front contact.

Table 2: Comparison of the short circuit current density of micromorph with a 1.2  $\mu$ m and 2.5  $\mu$ m thick microcrystalline absorber layer and comparison of  $J_{sc}$  with and with SOIR between the top and bottom cell

Jsc [mA/cm2]	Тор	Bottom	Total	
1.2 μm mc-Si	10.6	12.0	22.6	
2.5 μm mc-Si	10.7	14.3	25.0	
2.5 µm mc-Si with SOIR	11.7	11.9	23.6	

## 4. CONCLUSIONS

Our study gives guidelines for front TCO optimization in the nip configuration taking into account different substrates for a-Si:H solar cells. In fact, on flat substrate the rough ZnO LP-CVD can achieve similar level of current density compare to thin ITO. In addition, this ZnO has the advantage to have lower sheet resistance compare to ITO and thus can be used in

modules with an extended cells width and without silver grid. For textured substrate, we do not observe additional current gain due to additional light scattering in a:Si:H or  $\mu$ c-Si:H and thus an ITO front contact is preferred thanks to the antireflection effects with the Si. Finally, we think that by combining an appropriate substrate texture, high quality flat ITO front contact and an adapted intermediate reflector which are all compatible with fabrication on low-T plastics we should be able to increase the stable efficiency of tandem devices above 10% in the near future.

## 5. ACKNOWLEGEMENTS

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