

# Towards a LED based on a photonic crystal nanocavity for single photon sources at telecom wavelength

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## Abstract

A fundamental step towards achieving an “on demand” single photon source would be the possibility of electrical pumping for a single QD and thus the integration of such a device in an opto-electronic circuit. In this work we describe the fabrication process and preliminary results of a Light Emitting Diode (LED) to be integrated with a PhC nanocavity at telecom wavelength. We demonstrate the possibility of an effective electric pumping of the QDs embedded into the membrane by contacting the n-doped and p-doped layers of the thin membrane, which allows the fabrication of a PhC nanocavity on it.

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## 1. Introduction

The technology of real single-photon devices is still in its infancy, while the physical basis for single-photon emission from single QDs seems well-established. The required telecom wavelength (1300 or 1550 nm) poses significant challenges both in the epitaxial growth of the quantum dots (QDs) and in the measurement (InGaAs or Ge avalanche photodiodes must be used, with lower quantum efficiency and much higher noise). For these reasons, the few demonstrations of single-photon emission in the telecom bands [1–4] do not yet match the application requirements. An approach to the fabrication of *efficient* single-QD LEDs still has to be demonstrated [5], and a systematic investigation of the temperature limitations is missing. The enhancement of the spontaneous emission rate of an emitter on resonance with a mode of an optical

cavity (Purcell effect [6]), can be used to increase the efficiency of the source. Recently, using a modified L3 defect nanocavity (3 in-line missing holes) in a photonic crystal (PhC) on a GaAs membrane with a single layer of low density ( $5\text{--}7 \text{ dot}/\mu\text{m}^2$ ) QDs in its center, we obtained quality factors  $Q$  as high as 16500 and measured, in resonance conditions, a Purcell factor of 8 at 1300 nm for the first time [7,8]. On the basis of these achievements, we have developed an original design to integrate an LED device at 1300 nm with a PhC nanocavity. The fabrication process is very challenging and we demonstrate an effective electrical pumping of the QDs into the membrane both in case of high and low areal density QDs.

## 2. Fabrication process

The light source at  $\lambda = 1300 \text{ nm}$  consists of InAs QDs grown by molecular beam epitaxy (MBE) [9,10]. The LEDs have been fabricated on two heterostructures grown on a GaAs substrate: the first one has 3 layers of high QDs density ( $\sim 300 \text{ dots}/\mu\text{m}^2$ ) emitting at 1300 nm

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at room temperature (*RT*); the second one has a single layer of low QDs density ( $5\text{--}7$  dots/ $\mu\text{m}^2$ ) emitting at  $1300$  nm at  $5$  K. The QDs were grown at the center of a  $320$  nm-thick GaAs membrane on top of a  $1500$  nm-thick  $\text{Al}_{0.7}\text{Ga}_{0.3}\text{As}$  sacrificial layer and the doped layers are contained in the membrane. The main idea is to inject electrons from a top annular contact and holes from the sides of the mesa, using highly-doped GaAs contact layers to spread the current throughout the mesa to the center of the cavity. Recombination will occur on the entire surface of the mesa but the emission from the cavity center is enhanced and can be isolated with a combination of spatial and spectral filtering.

The fabrication process is based on e-beam lithography (EBL) (Vistec, EPBG 5HR working at  $100$  kV) and thin-film techniques. The first step consists in patterning of the top, ring-shaped n-contact by lift-off of a multilayer of Ni/Ge/Au/Ni/Au. The pattern is transferred by EBL on a  $1$   $\mu\text{m}$  thick UVIII resist. The large thickness of this resist layer allows us to lift-off the metal contact, deposited by gun-evaporation (Ni/Ge) and thermal evaporation (Au) for a total thickness of  $155$  nm. The contact is annealed at  $400$   $^{\circ}\text{C}$  for  $30$  min with a thermal ramp of  $5$   $^{\circ}\text{C}/\text{min}$ . Together with the contacts, markers to be used for the alignment of the successive exposures are also deposited. Then, we expose mesa patterns with different diameters

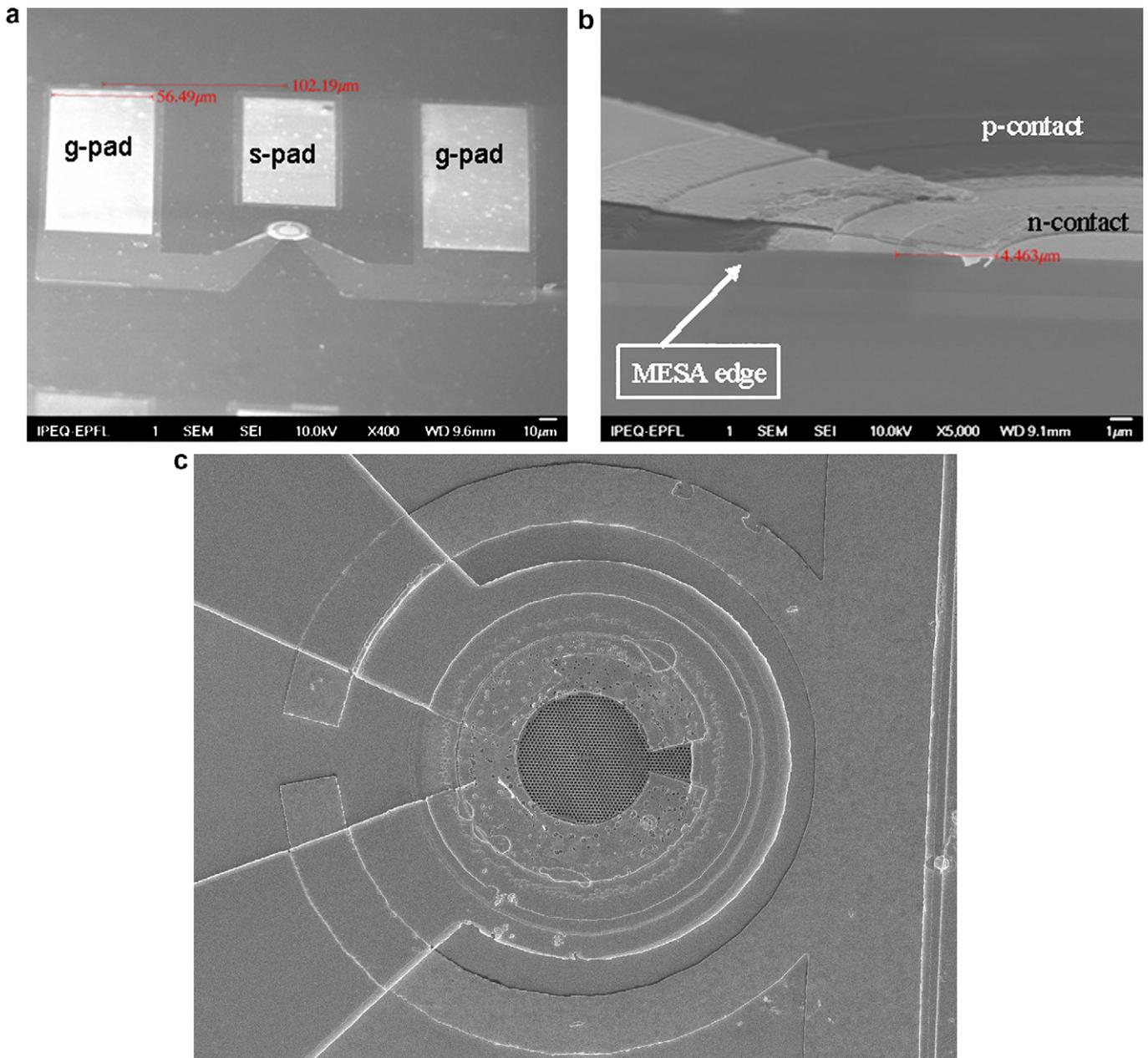


Fig. 1. (a) Top view (SEM image) of a LED at the end of the fabrication process, s-pad: signal pad, g-pad: ground pad; (b) same device type in section (after cleavage); (c) particular of the device with a PhC at the end of the process.

(32–26–20–15–10  $\mu\text{m}$ ) on a 300 nm-thick HSQ (hydrogen silsesquioxane) resist and etch the mesa using a  $\text{H}_3\text{PO}_4:\text{H}_2\text{O}_2:\text{H}_2\text{O} = 3:1:30$  solution to obtain a height between 290 nm and 330 nm. This wet etching process is the most crucial step of our fabrication process: we need to stop the wet etching exactly on the p-region of our membrane. The membrane is composed by layers of GaAs (n and p doped) with AlGaAs layers in between, so the etching rate is not constant: to reach reproducible results we have to optimise this step accurately. The HSQ layer is then removed with a HF based solution. The p-contact is also shaped in a ring, in order to obtain the best hole injection around the mesa and it consists of 110 nm Ti/Au evaporated on a patterned UVIII layer and subsequent lift-off in acetone. A 200 nm-thick  $\text{Si}_3\text{N}_4$  layer is deposited by PECVD to create an insulating layer between the n and p-contacts. The  $\text{Si}_3\text{N}_4$  is then removed by selective reactive ion etching (RIE) (50 sccm  $\text{CHF}_3$ , 10 sccm  $\text{O}_2$ , pressure: 55 mTorr, power: 200 W) from the n-contact on the top of the mesa and from part of the p-contact. By lift-off of a Cr/Au (thickness: 110 nm) layer we can connect the n-contact on the top of the mesa with the ground pads on the  $\text{Si}_3\text{N}_4$  surface by two bridge contacts. In the same process step we also connect the p-contact with the signal pad on the  $\text{Si}_3\text{N}_4$  layer. In order to carry out a continuous film over the mesa lateral edge we performed two tilted Cr/Au evaporations. The lift-off step ends the fabrication of the LED. Fig. 1a shows a SEM image of a LED (mesa diameter: 32  $\mu\text{m}$ ) at this stage of the process. In Fig. 1b a cleaved section of a device of the same type is shown. It is possible to see the n-contact onto the mesa and the bridge that brings the n-contact down on the sample. The annular p-contact under the  $\text{Si}_3\text{N}_4$  layer should also be noted. To integrate the PhC nanocavity on the LED a 150 nm-thick  $\text{SiO}_2$  layer has been deposited by ECR-PECVD on the top of the substrate. The PhC pattern is transferred by EBL on a 200 nm-thick PMMA resist and the usual process to fabricate PhC nanocavities has been performed [11]. In Fig. 1c is shown a particular of the mesa with a PhC cavity transferred on its surface at the end of the process.

### 3. Results

The electro-optical characterization of the LED has been performed in a cryogenic electro-probe station coupled with a microelectroluminescence ( $\mu\text{EL}$ ) setup. The sample is mounted on a cold-finger cooled by a holder dipped in liquid He (about 3 K). The optical emission from the top of devices can escape from the cryogenic set up through a window and is collected by a microscope objective (numerical aperture 0.3). A mirror reflects the Infra-Red (IR) radiation (which is sent to an IR camera or focused into an optical-fiber and sent to the spectrometer) and transmits the visible radiation (that allows to observe the samples surface by a CCD camera). The electroluminescence (EL) is dispersed into a 1 m focal length monochromator equipped with a cooled InGaAs photodiode

array detector; the spectral resolution of the setup is better than 30  $\mu\text{eV}$  ( $\approx 0.04$  nm).

The first tests have been carried out on high density InAs QDs samples emitting at 1300 nm at RT. The I-V curves measured on these devices (mesa area:  $\sim 1400 \mu\text{m}^2$ ) show typical rectifying p-n junction characteristics with a voltage threshold in direct polarization of  $\approx 1.5$  V. At low temperatures, a lower forward-bias current is measured, presumably due to reduced thermal activation through contact and heterostructure barriers. Nevertheless, the current injected through the junction is in the range of mA, for an applied voltage  $\geq 4$  V, sufficient to excite the InAs QDs in the active region. In fact EPL signal has been measured (Fig. 2a). From the IR camera image of a device under electrical injection we observe (Fig. 2b) a large emission from the edges of the mesa as well as from the center, attributed to scattering of light propagating in a waveguide mode within the mesa. So, we have processed the sample with low density InAs QDs, implementing a gold cover around the mesa border to reflect the scattered light to the sub-

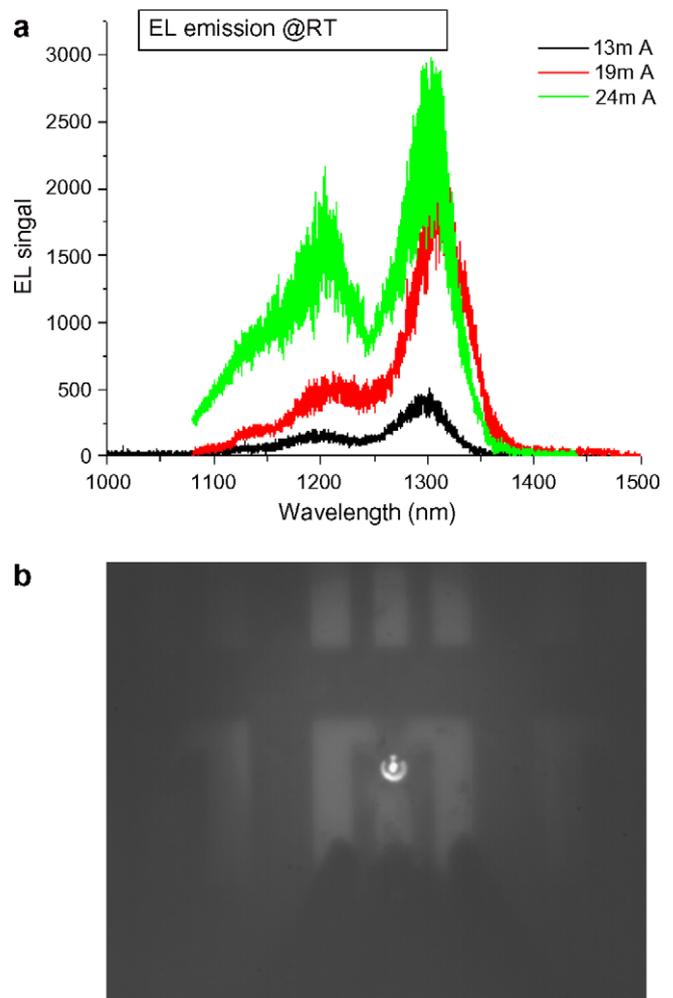


Fig. 2. (a) EPL emission (RT) from a high density QD based LED for different pumping currents. (b) Picture from an IR camera of a high density QD LED under electrical injection. Note the light emitted from the mesa edges.

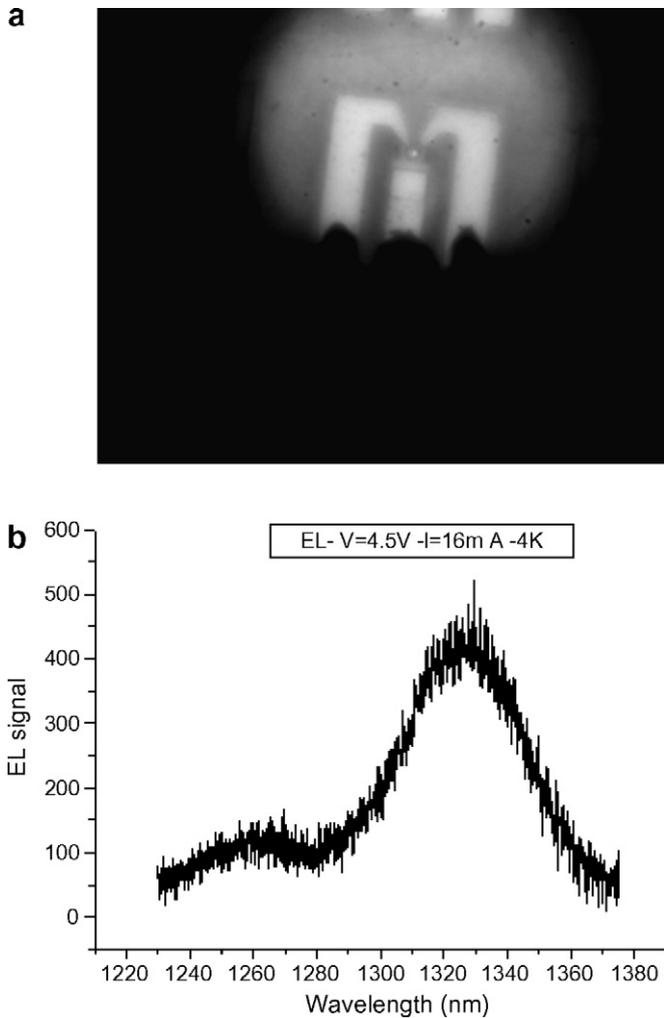


Fig. 3. (a) EL emission from the same LED at 4 K. (b) Picture from an IR camera of a low density QD LED under electrical injection.

strate side. Fig. 3a shows the IR camera picture of a working device: the emission comes from the proper region and also the measured EPL spectrum (Fig. 3b) is in agreement with the PL signal of the same sample. The measurements on the low density samples have been performed at 4 K.

#### 4. Conclusions

We have presented a LED structure working at 1300 nm, designed to be integrated with a PhC nanocavity. First steps in this last direction have been already performed, optimizing the exposure process of the PhC nanocavities on the LED top and their transfer into the membrane. Our next goals will be to demonstrate the coupling of InAs QDs at 1300 nm to a PhC cavity mode, also in electrical pumping conditions.

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